

1

2

3

4

5

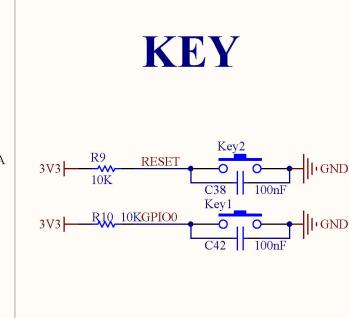
6

KEY

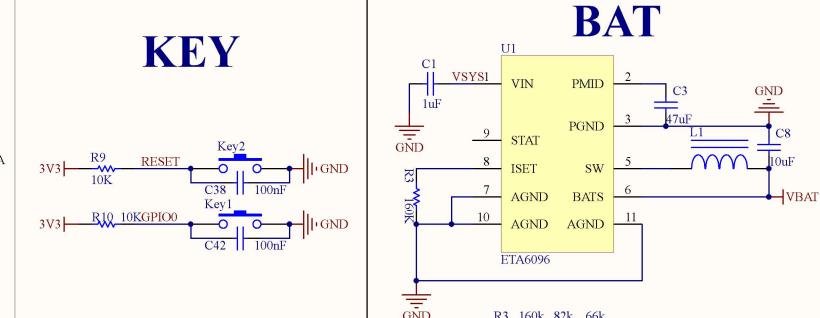
A

BAT

1



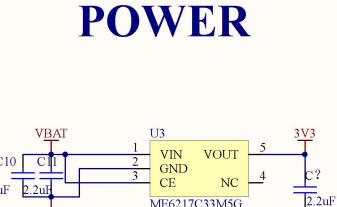
2



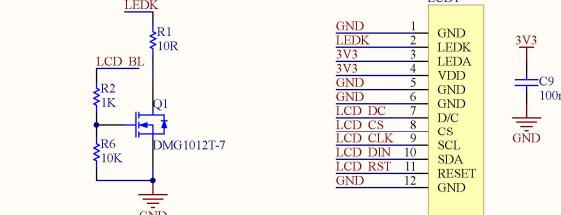
3

POWER

4

**LCD**

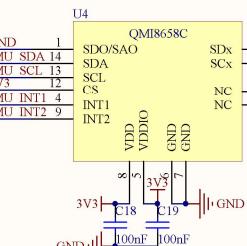
5



A

IMU

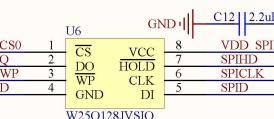
B



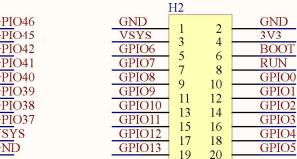
6

FLASH

7

**GPIO_OUT**

8



B

IMU SDA
IMU SCLGPIO6
GPIO7IMU INT1
IMU INT2GPIO47
GPIO48

LCD DC

GPIO8

LCD CS

GPIO9

LCD CLK

GPIO10

LCD DIN

GPIO11

LCD RST

GPIO12

LCD BL

GPIO40

BAT ADC

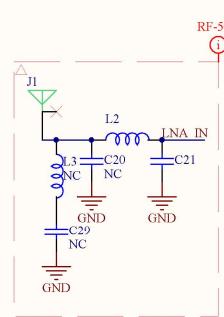
GPIO1

BOOT

GPIO0

RUN

RESET

**FLASH**

9

LNA IN VDD3V3 RESET CHIP_PU

VDD3P3 VDDA XTAL_P XTAL_N

GPIO0 5 GPIO046 51 GPIO45

GPIO1 6 GPIO01 50 U0RXD

GPIO2 7 GPIO02 49 U0TXD

GPIO3 8 GPIO03 48 GPIO42

GPIO4 9 GPIO04 47 GPIO41

GPIO5 10 GPIO05 46 3V3

GPIO6 11 GPIO06 45 GPIO40

GPIO7 12 GPIO07 44 GPIO39

GPIO8 13 GPIO08 43 GPIO38

GPIO9 14 GPIO09 42 GPIO37

GPIO10 15 GPIO10 41 GPIO36

GPIO11 16 GPIO11 40 GPIO35

GPIO12 17 GPIO12 39 GPIO34

GPIO13 18 GPIO13 38 GPIO33

GPIO14 19 GPIO14 37 GPIO47

GPIO15 21 VDD3P3_RTC 36 GPIO48

GPIO16 22 XTAL_32K_P 35 SPICLK_N

XTAL_32K_N 34 SPIQ

GPIO17 23 34 SPIQ 33 SPICLK

GPIO18 24 32 SPICS0

GPIO19 25 31 SPWP

GPIO20 26 30 SPIHD

GPIO21 27 29 VDD_SPI

ESP32-S3R2 57 GND

J1 L2 L3 C20 C21 C29 NC GND

3V3 C4 1uF C5 100nF 0402

C6 2 100nF C26 2 100nF

C27 100nF C28 100pF 0402

C29 100pF C30 100pF

C31 100pF C32 100pF

C33 100pF C34 100pF

C35 100pF C36 100pF

C37 100pF C38 100pF

C39 100pF C40 100pF

C41 100pF C42 100nF

C43 100nF C44 100nF

C45 100nF C46 100nF

C47 100nF C48 100nF

C49 100nF C50 100nF

C51 100nF C52 100nF

C53 100nF C54 100nF

C55 100nF C56 100nF

C57 100nF C58 100nF

C59 100nF C60 100nF

C61 100nF C62 100nF

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