



Algorithms for Real-Time Rendering

186.192, 2022S, 3.0ECTS



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Vulkan Lecture Series, Episode 7: **Synchronization**

Johannes Unterguggenberger

Institute of Visual Computing & Human-Centered Technology
TU Wien, Austria

Recap: Commands

Graphics Pipeline Commands:

vkCmdDraw
vkCmdDrawIndexed
vkCmdDrawIndirect
vkCmdDrawMeshTasksNV
vkCmdClearAttachments

Ray-Tracing Acceleration Structure Build Commands:

vkCmdBuildAccelerationStructuresKHR
vkCmdBuildAccelerationStructuresIndirectKHR

Synchronization Commands:

vkCmdPipelineBarrier[2]
vkCmdSetEvent[2]
vkCmdEndRenderPass[2]
vkCmdBeginRenderPass[2]
vkCmdEndRenderPass[2]

Compute Pipeline Commands:

vkCmdDispatch
vkCmdDispatchIndirect

Ray Tracing Pipeline Commands:

vkCmdTraceRaysKHR
vkCmdTraceRaysIndirectKHR

Transfer Commands:

vkCmdCopyBuffer
vkCmdCopyImage
vkCmdCopyBufferToImage
vkCmdCopyImageToBuffer
vkCmdCopyAccelerationStructureKHR
vkCmdBlitImage
vkCmdResolveImage
vkCmdClearColorImage

Bind Commands:

vkCmdBindDescriptorSets
vkCmdBindPipeline
vkCmdBindVertexBuffers
vkCmdBindIndexBuffer

Other Commands:

vkCmdPushConstants
vkCmdPushDescriptorSetKHR
vkCmdSetScissor
vkCmdSetViewport
vkCmdSetDepthBias
...



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vkCmdPipelineBarrier[2]
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vkCmdWaitEvents[2]
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Recap: Pipeline Stages

The Stages of...

Graphics Pipelines:

CONDITIONAL_RENDERING(EXT)
DRAW_INDIRECT
INDEX_INPUT(KHR)
VERTEX_ATTRIBUTE_INPUT(KHR)
VERTEX_SHADER
TESSELLATION_CONTROL_SHADER
TESSELLATION_EVALUATION_SHADER
GEOMETRY_SHADER
TRANSFORM_FEEDBACK(EXT)
FRAGMENT_SHADING_RATE(KHR)
EARLY_FRAGMENT_TESTS
FRAGMENT_SHADER
LATE_FRAGMENT_TESTS
COLOR_ATTACHMENT_OUTPUT

Compute Pipelines:

CONDITIONAL_RENDERING(EXT)
DRAW_INDIRECT
COMPUTE_SHADER

Ray Tracing Pipelines:

DRAW_INDIRECT
RAY_TRACING_SHADER(KHR)

Ray-Tracing Acceleration Structure Build Commands:

ACCELERATION_STRUCTURE_BUILD(KHR)

Full List of Pipeline Stages:
VkPipelineStageFlagBits2

The Khronos Group. Vulkan 1.3.212 Specification

Copy Commands:

COPY

Blit Commands:

BLIT

Resolve Commands:

RESOLVE

Clear Commands:

CLEAR

None/Aggregate Stages:

NONE
ALL_GRAPHICS
ALL_TRANSFER
ALL_COMMANDS



Pipeline Stages

The Stages of...

Graphics Pipelines:

CONDITIONAL_RENDERING(EXT)
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The Khronos Group. Vulkan 1.3.212 Specification

Copy Commands:

COPY

Blit Commands:

BLIT

Resolve Commands:

RESOLVE

Clear Commands:

CLEAR

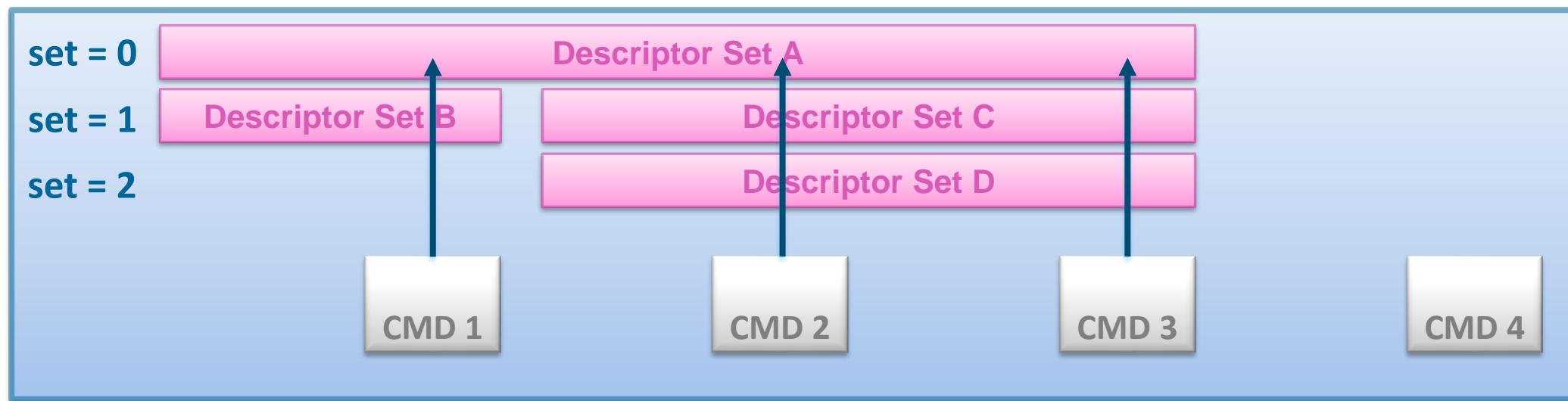
None/Aggregate Stages:

NONE
ALL_GRAPHICS
ALL_TRANSFER
ALL_COMMANDS



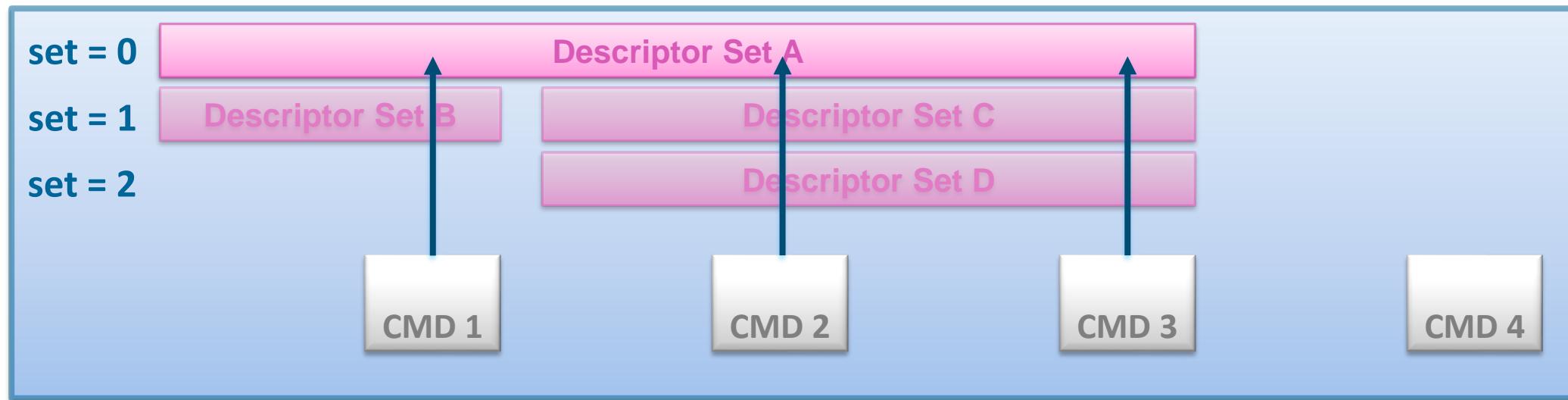
Command Recording and Queue Submission

COMMAND BUFFER

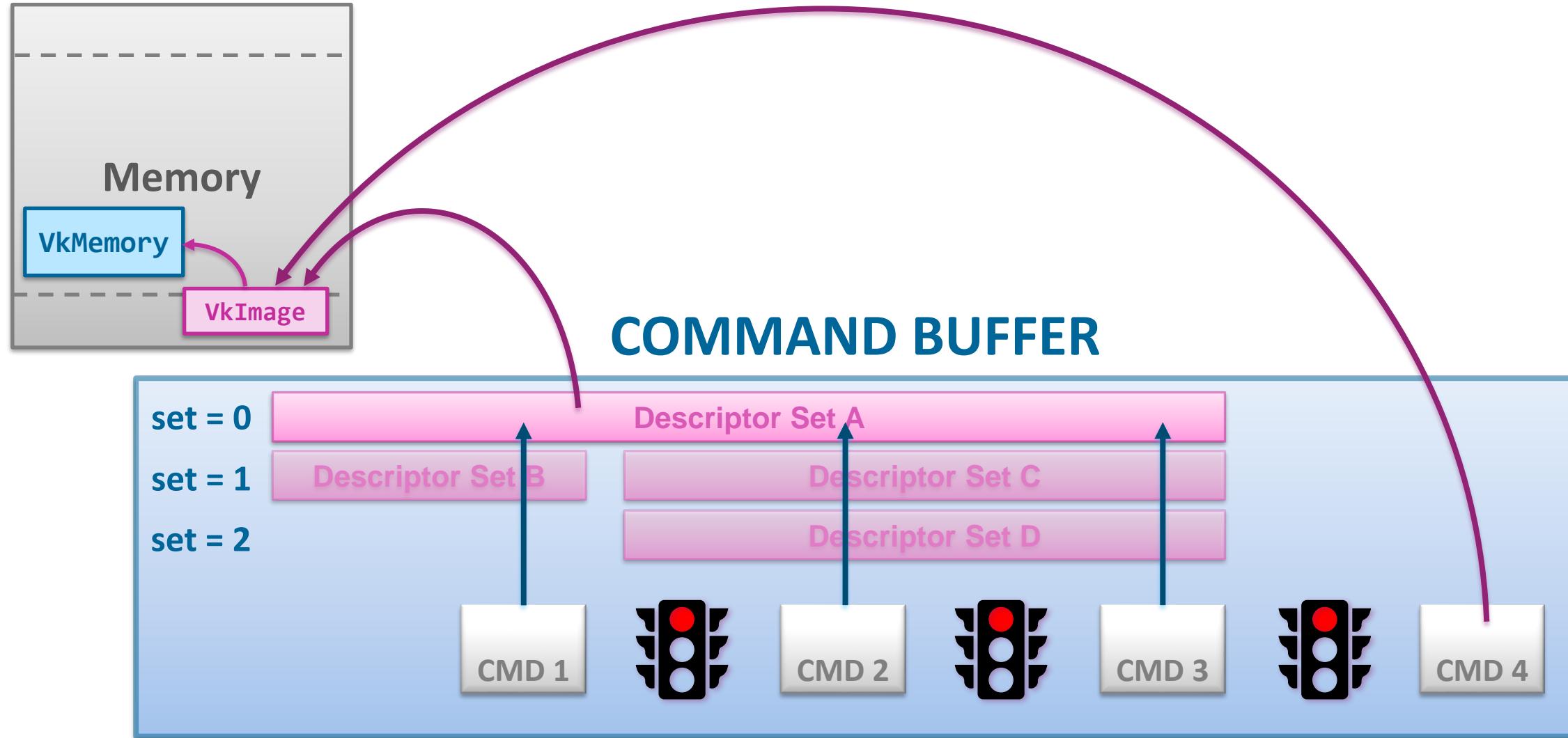


Command Recording and Queue Submission

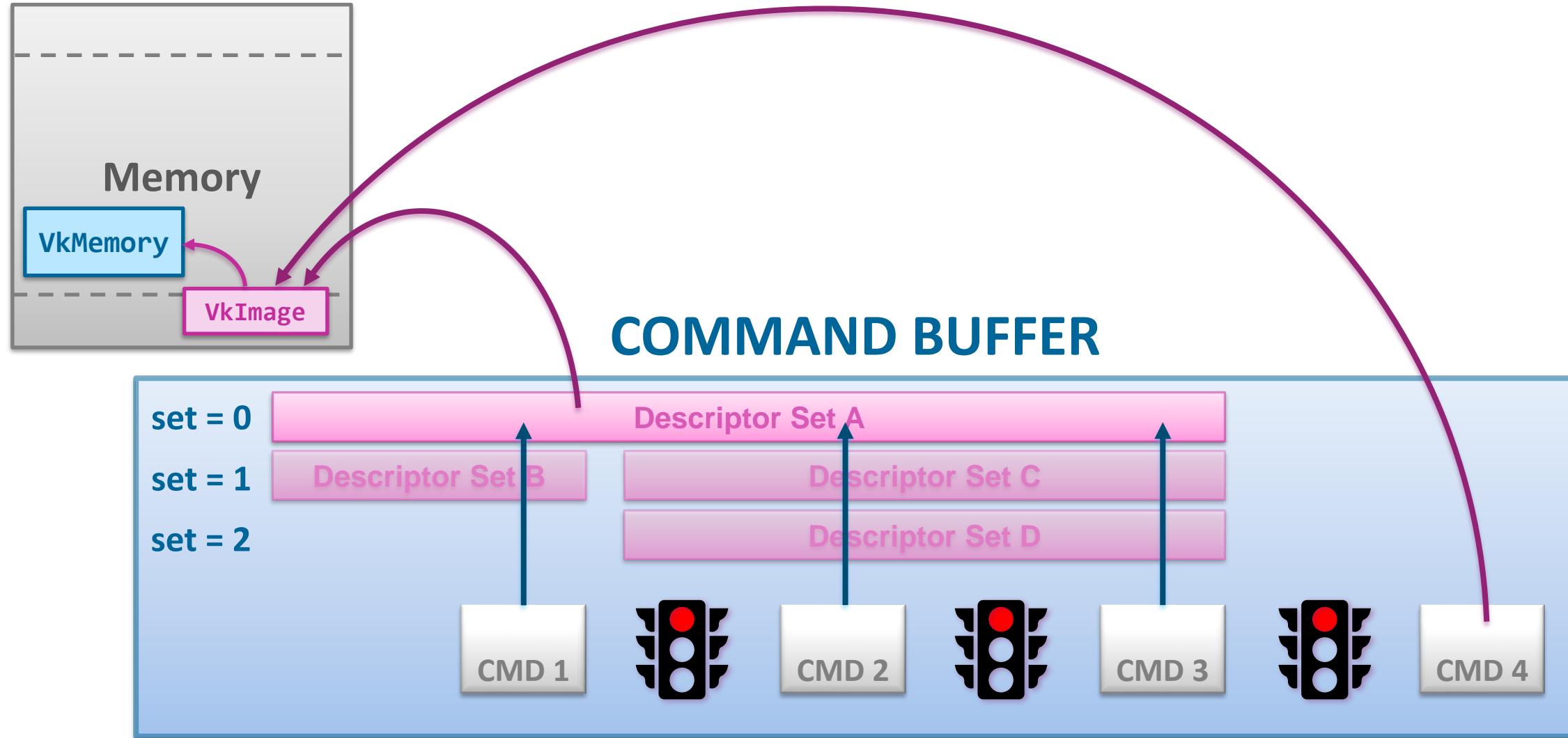
COMMAND BUFFER



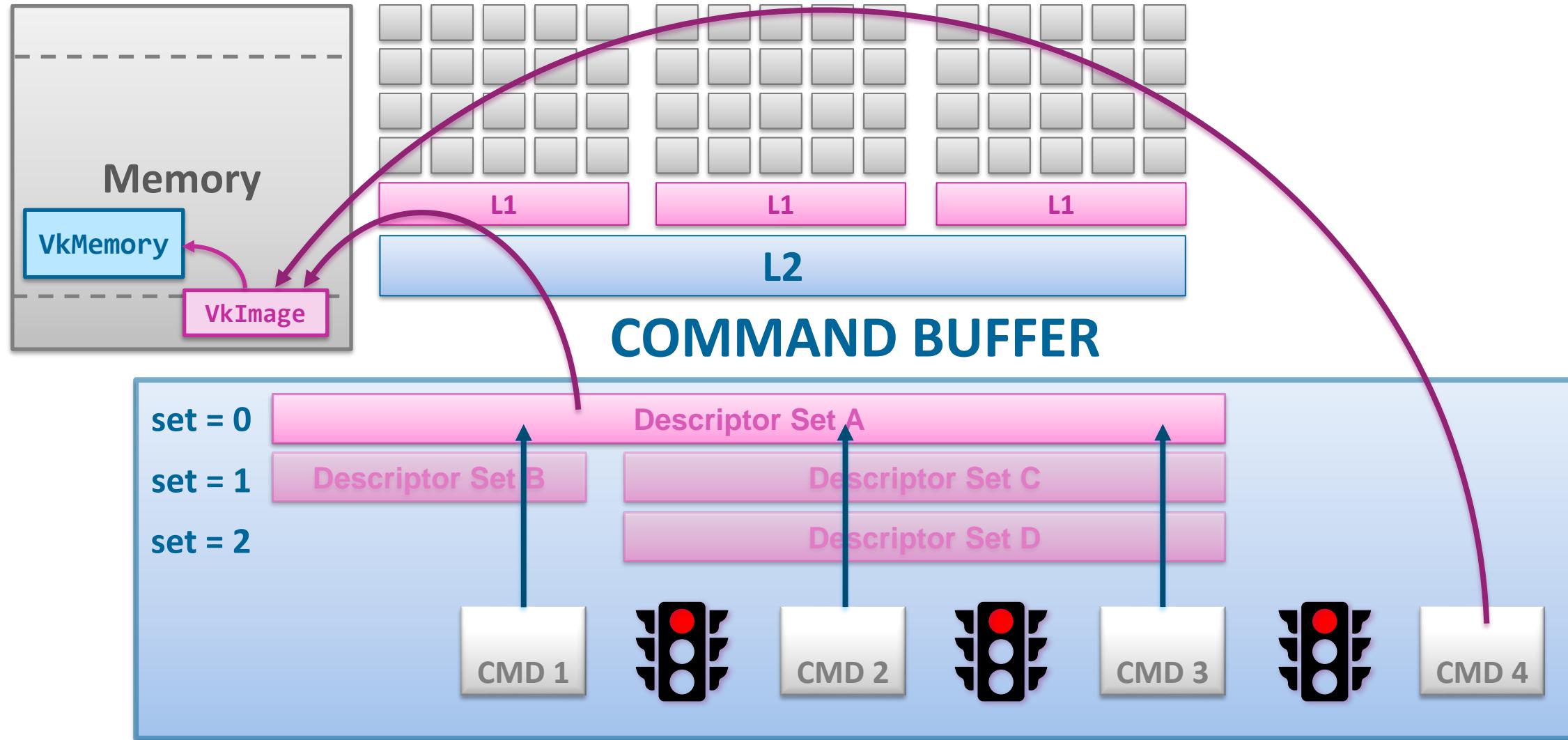
Command Recording and Queue Submission



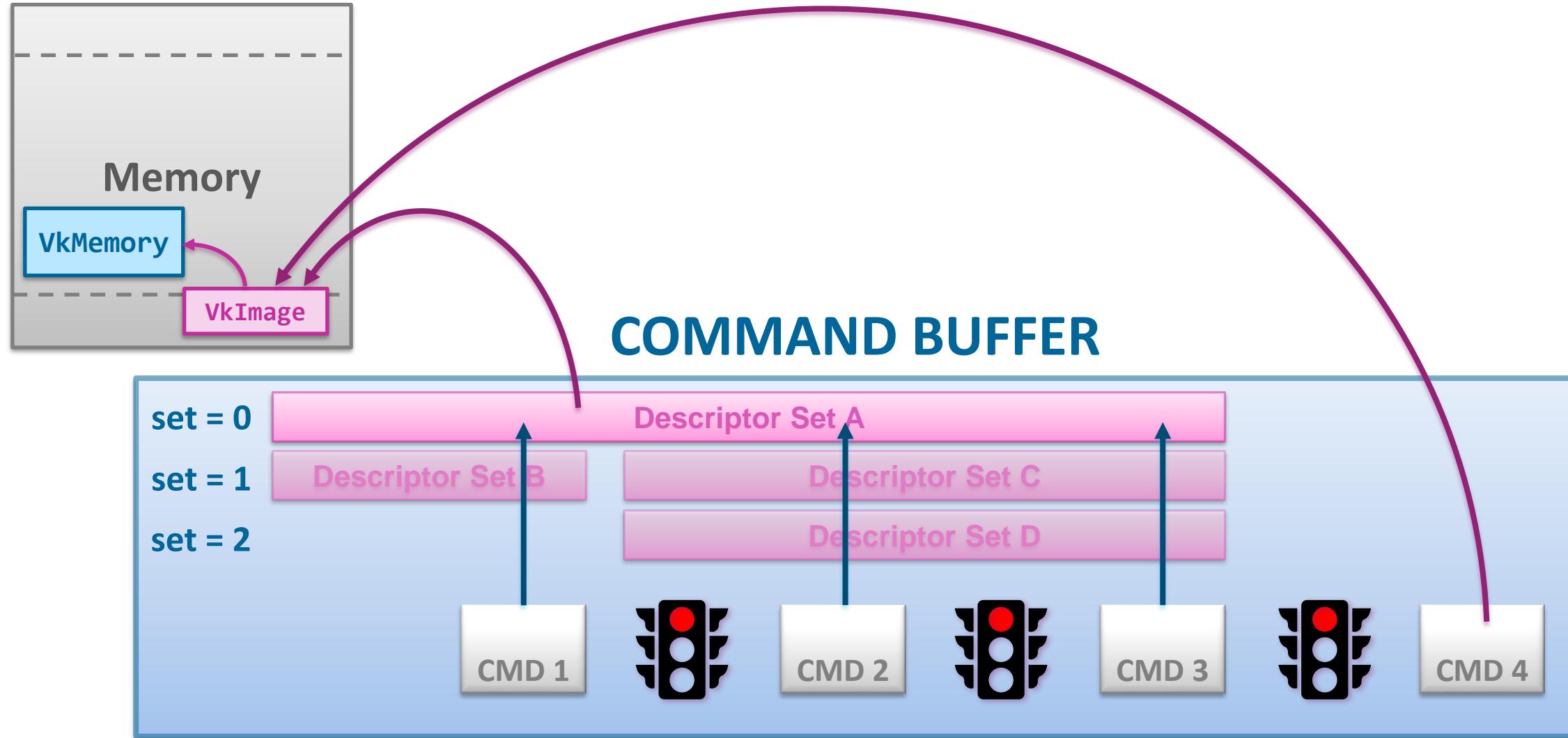
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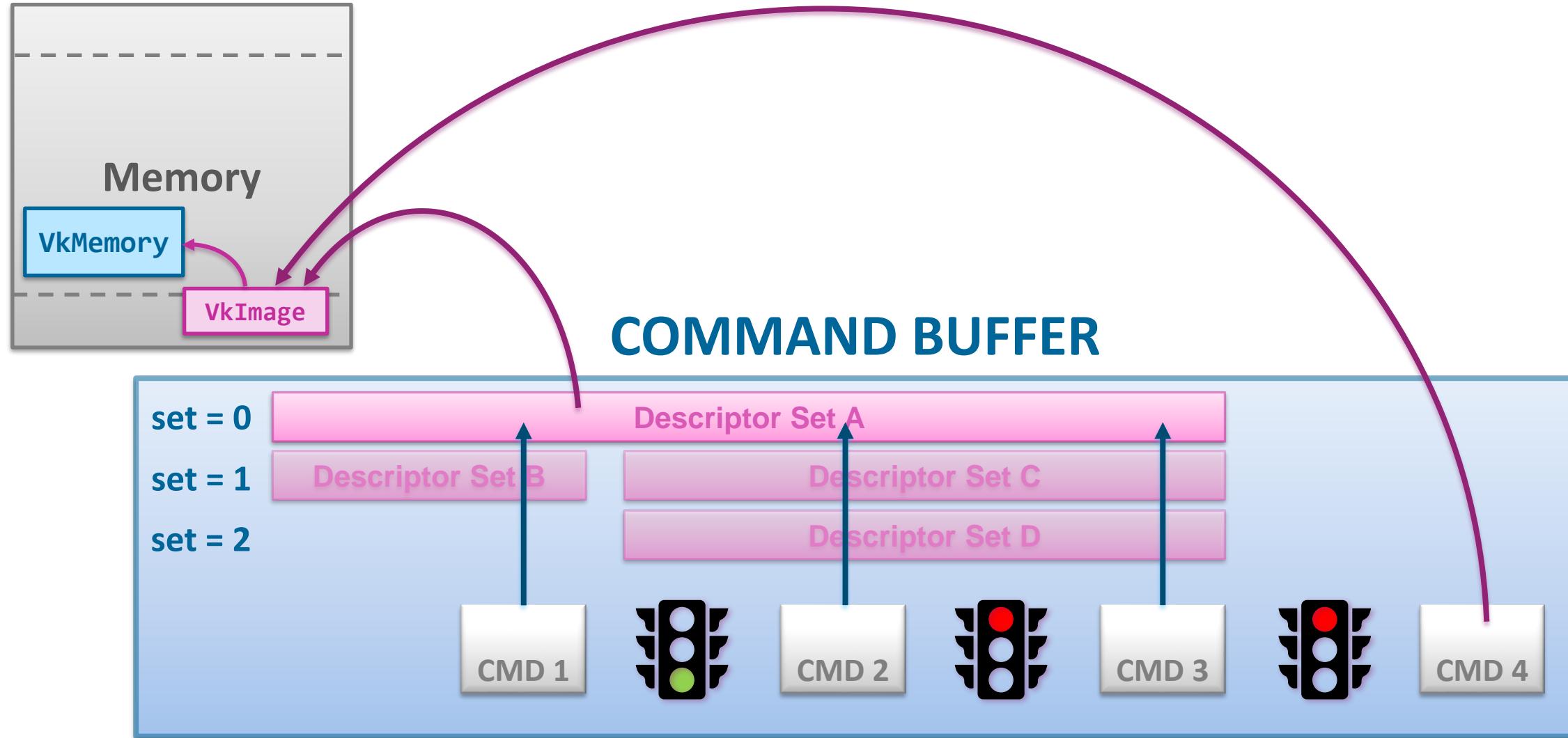
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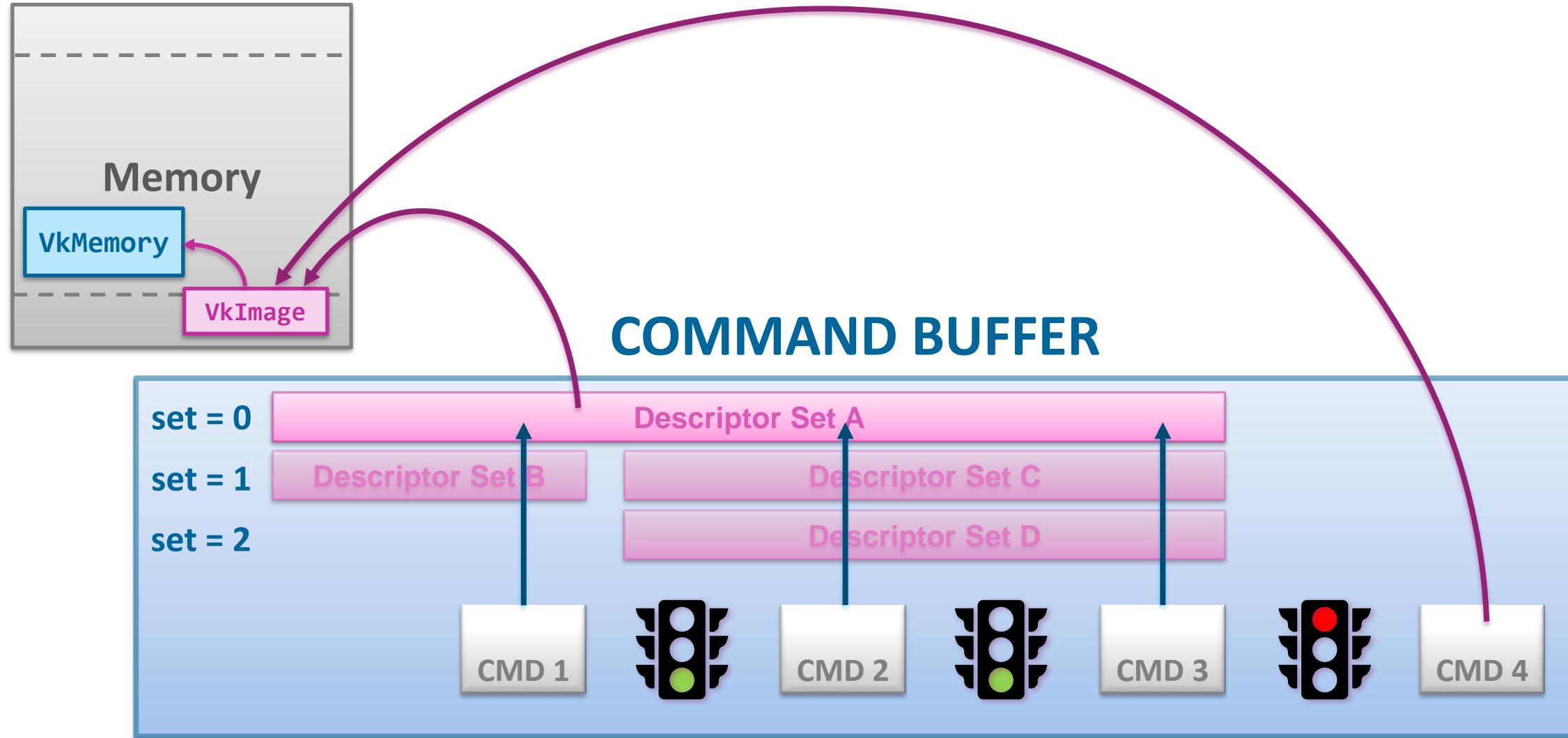
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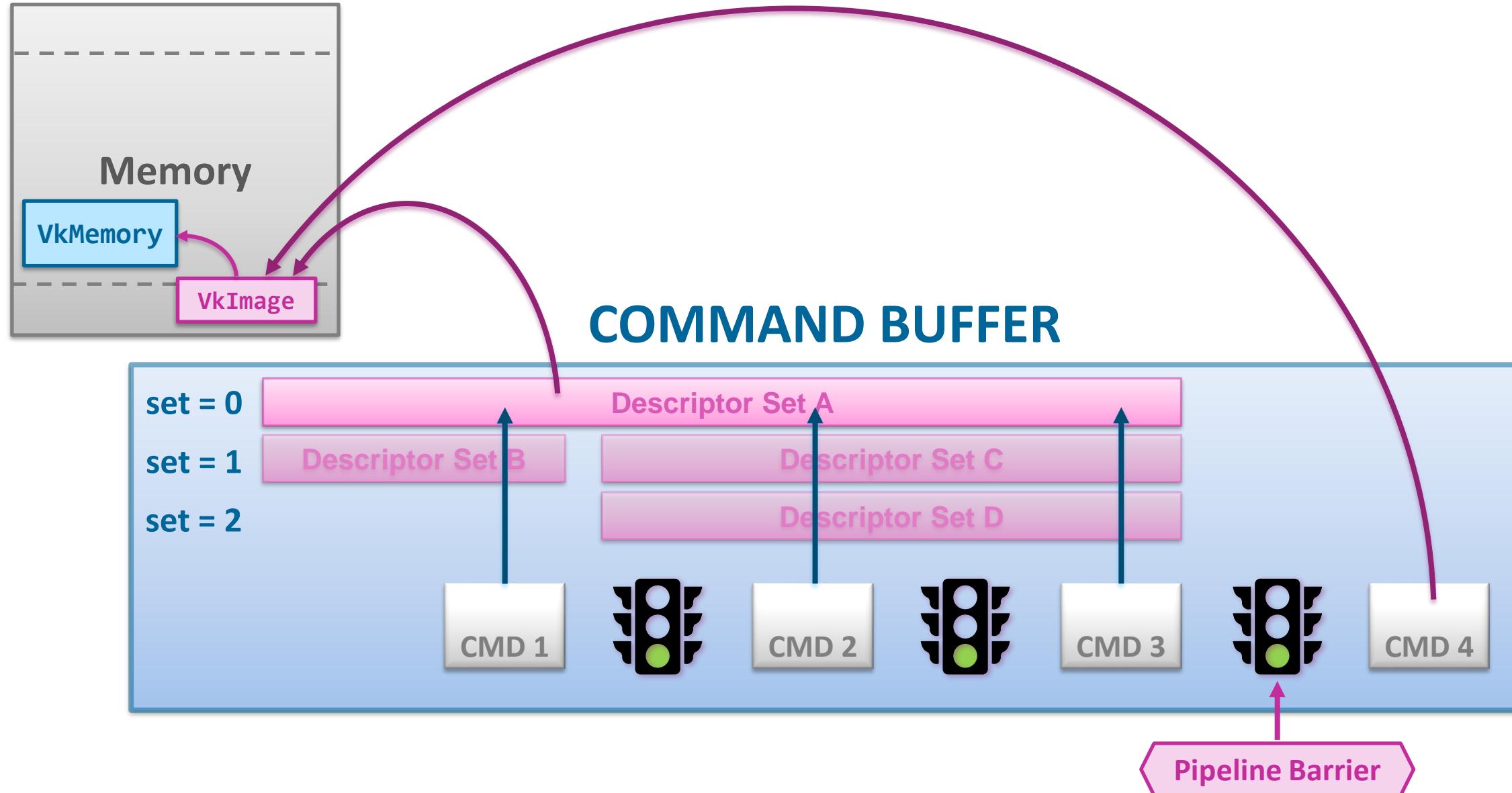
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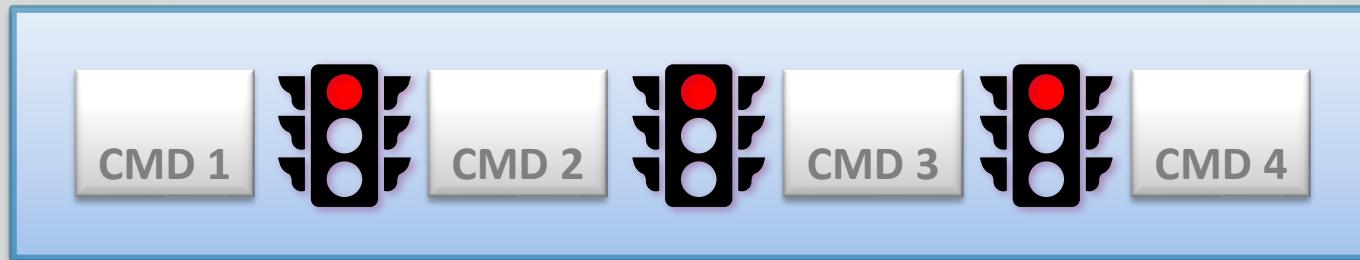


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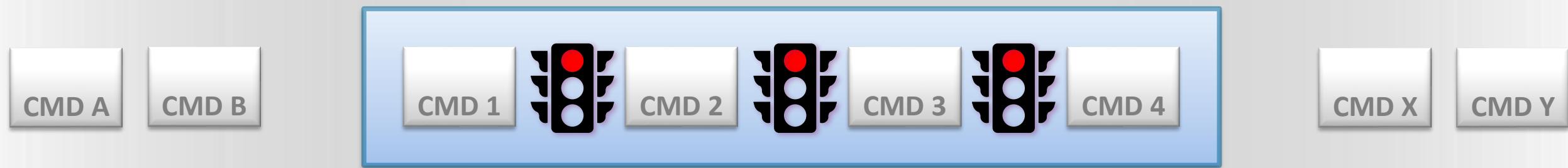
Command Recording and Queue Submission

QUEUE



Command Recording and Queue Submission

QUEUE

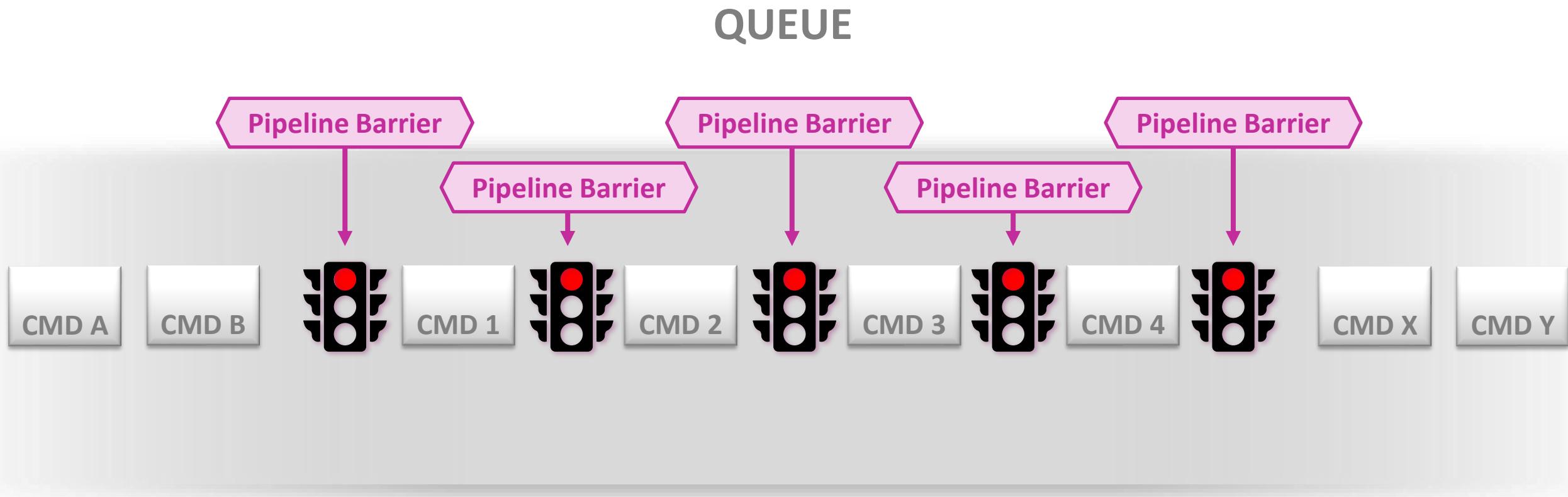


Command Recording and Queue Submission

QUEUE

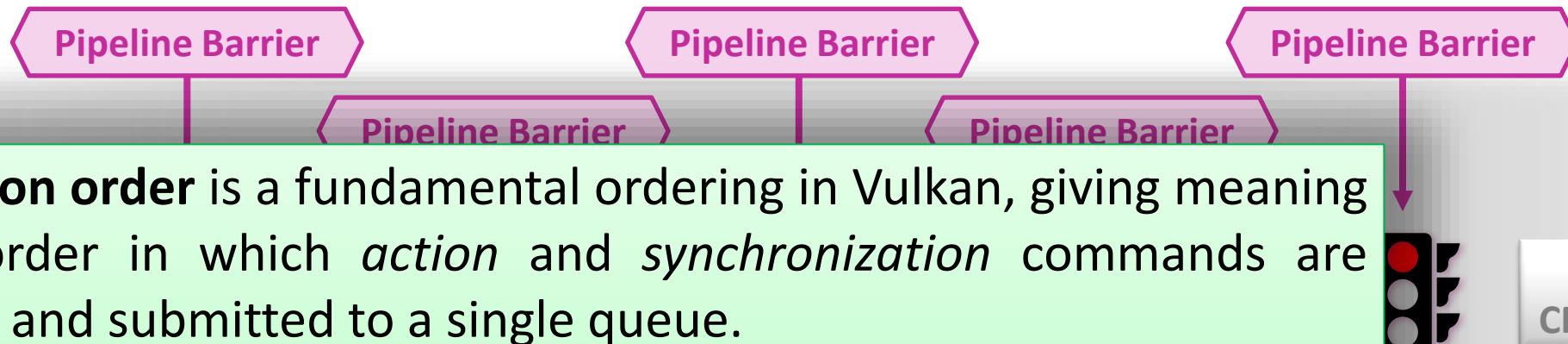


Command Recording and Queue Submission



Command Recording and Queue Submission

QUEUE



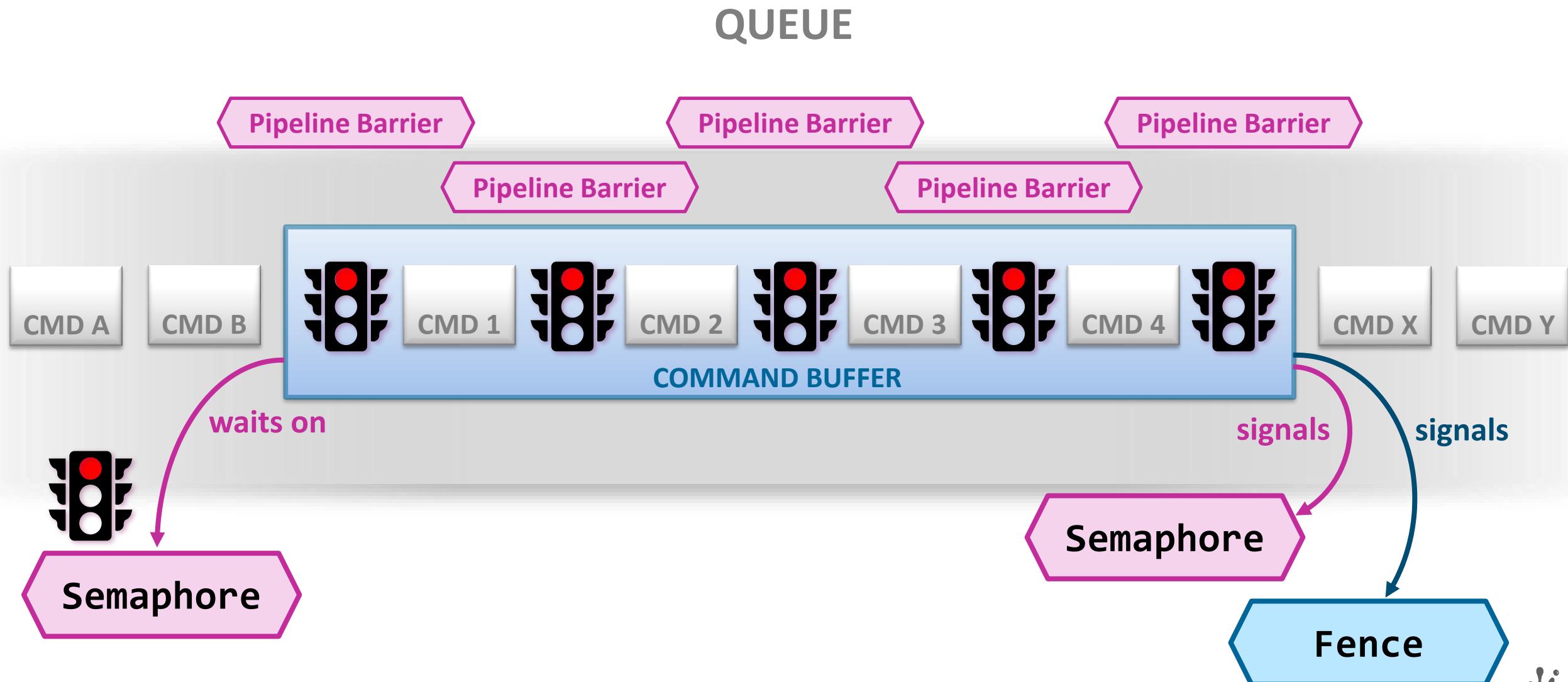
Explicit and implicit ordering guarantees between commands in Vulkan all work on the premise that this ordering is meaningful.

This order does not itself define any execution or memory dependencies; synchronization commands and other orderings within the API use this ordering to define their scopes.

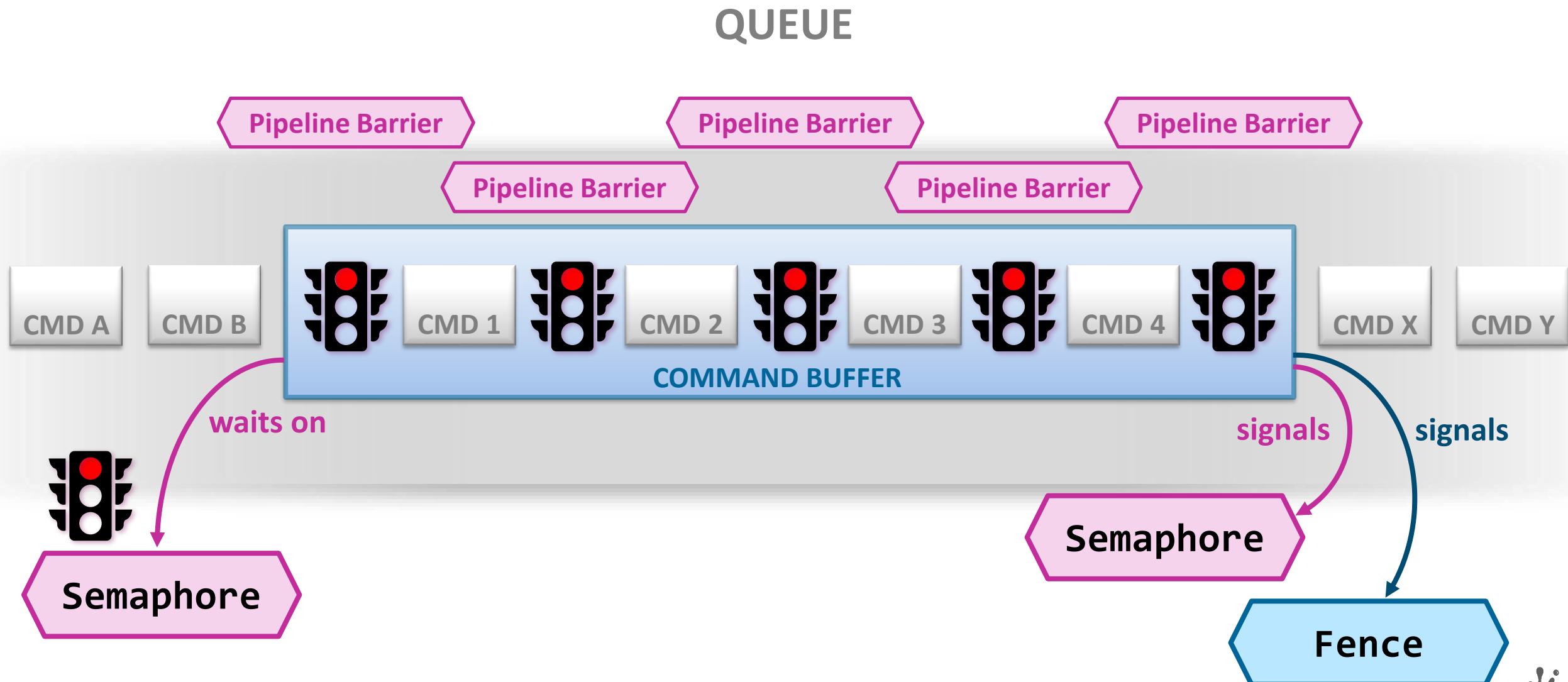
The Khronos Group. Vulkan 1.3.209 Specification



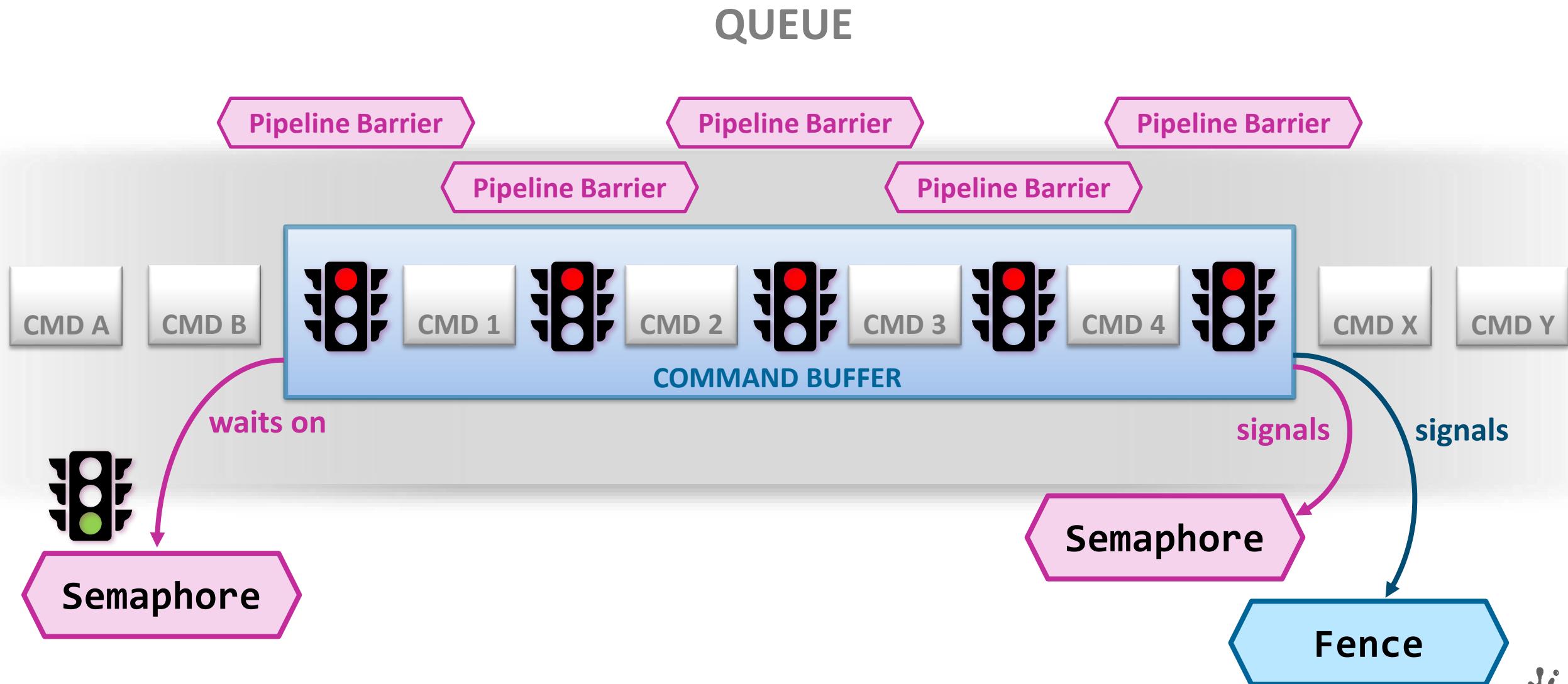
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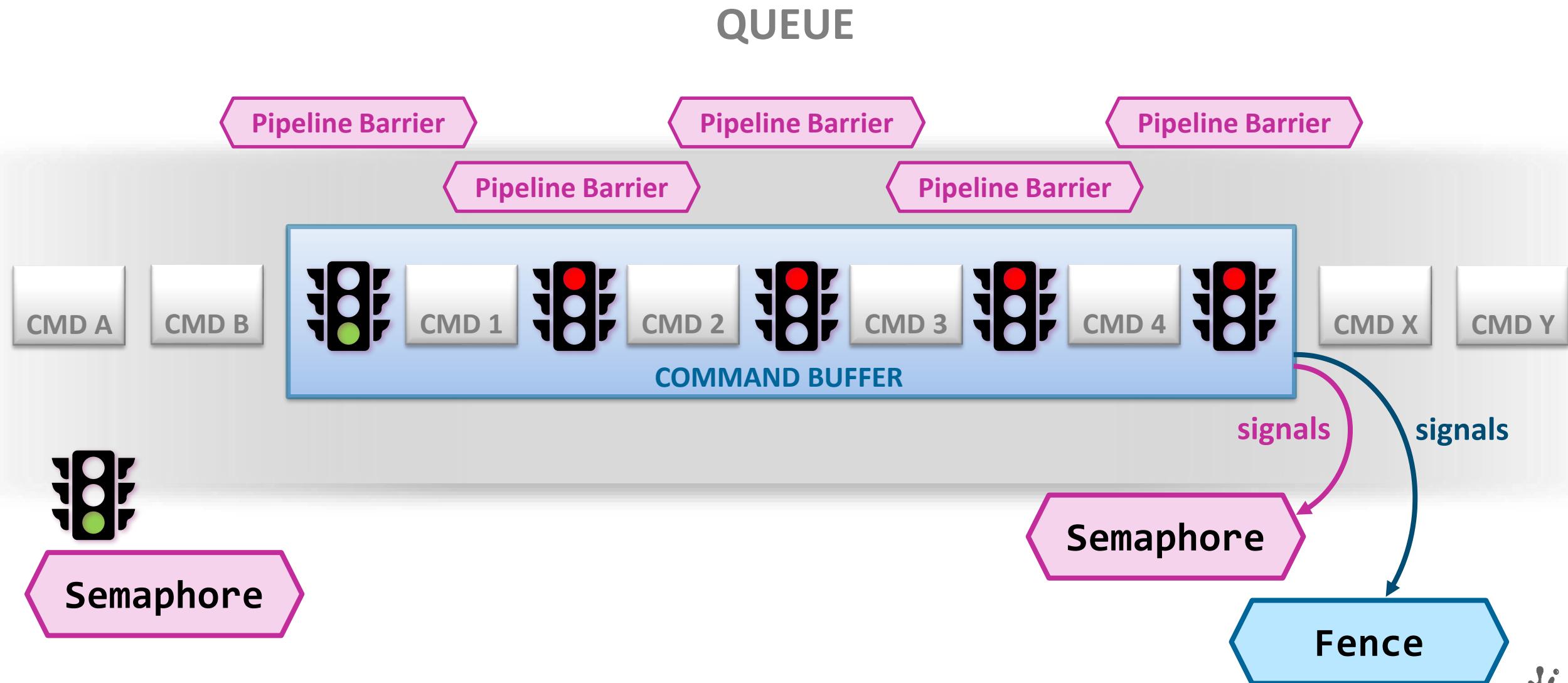
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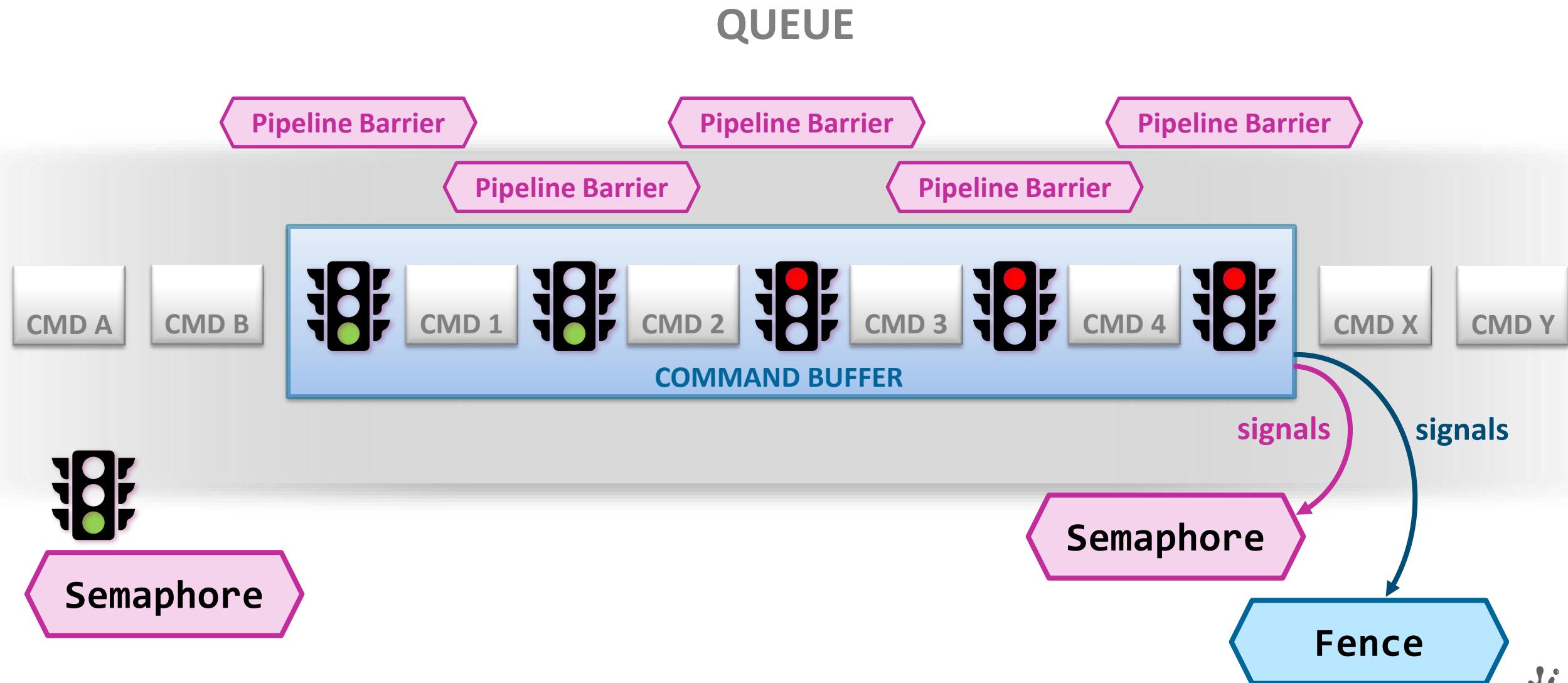
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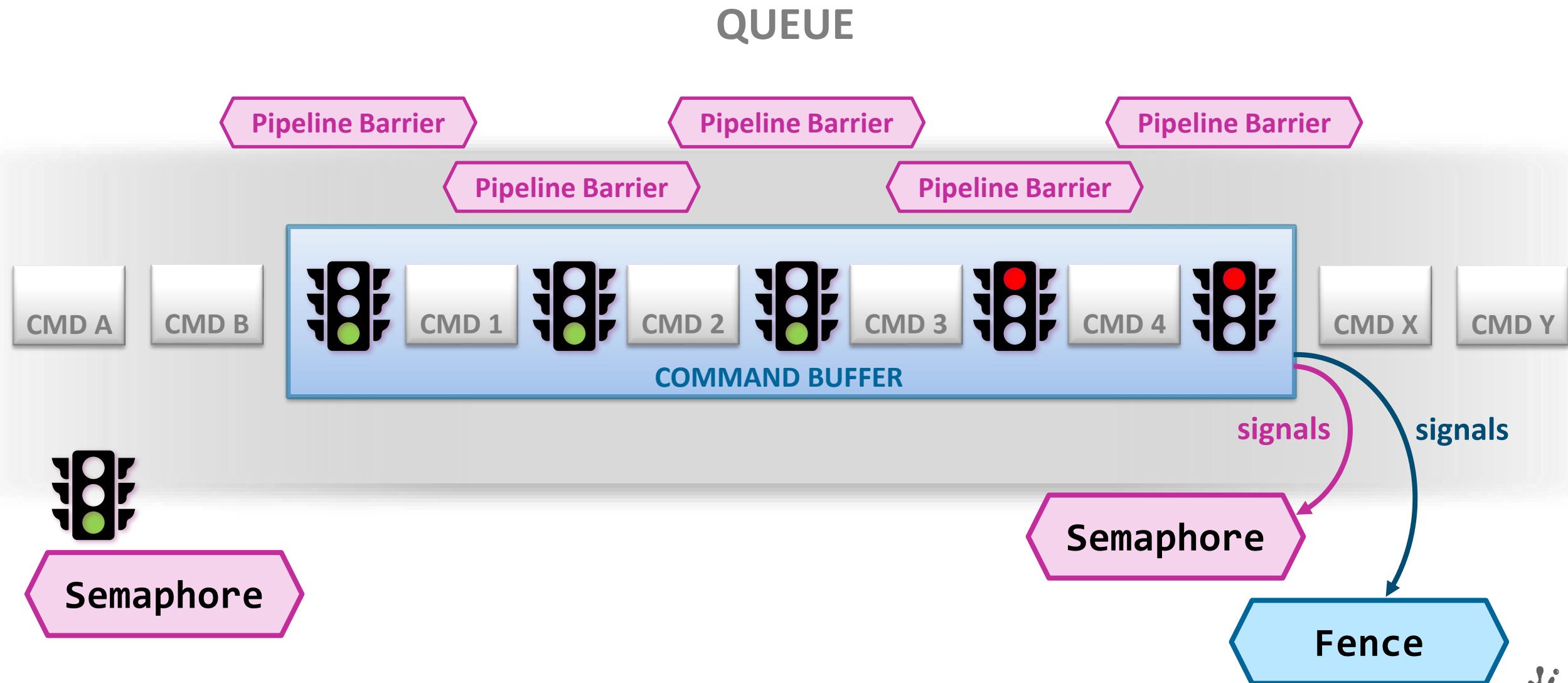
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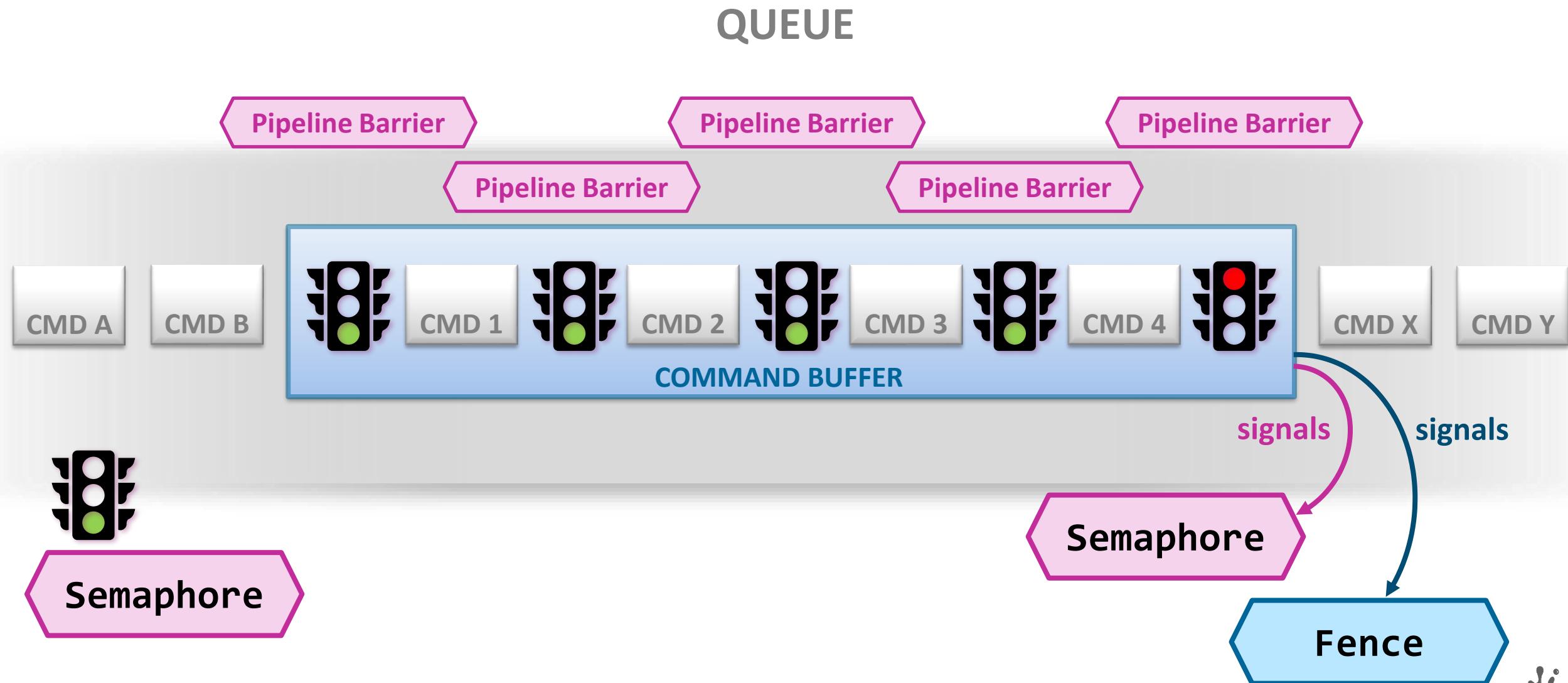
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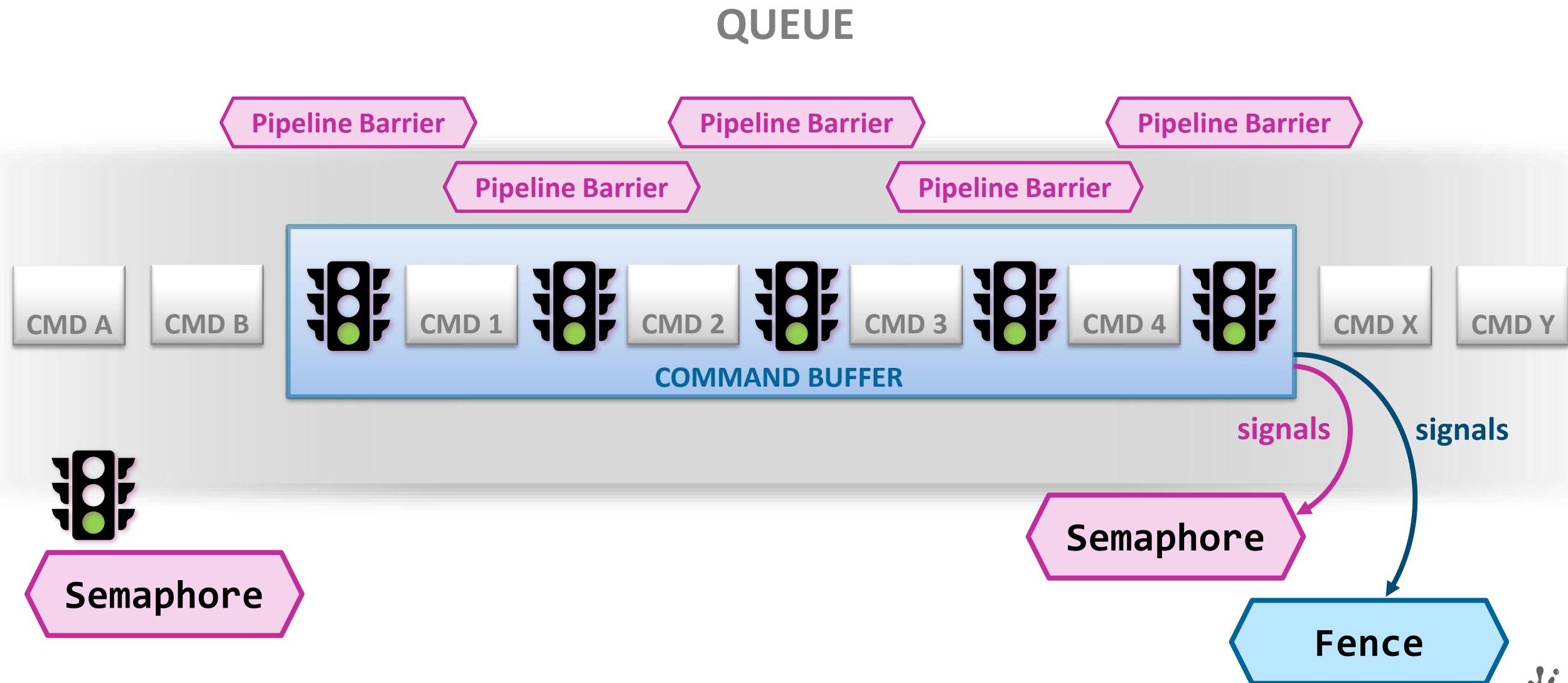
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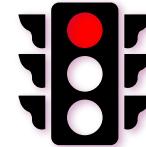
Command Recording and Queue Submission

QUEUE

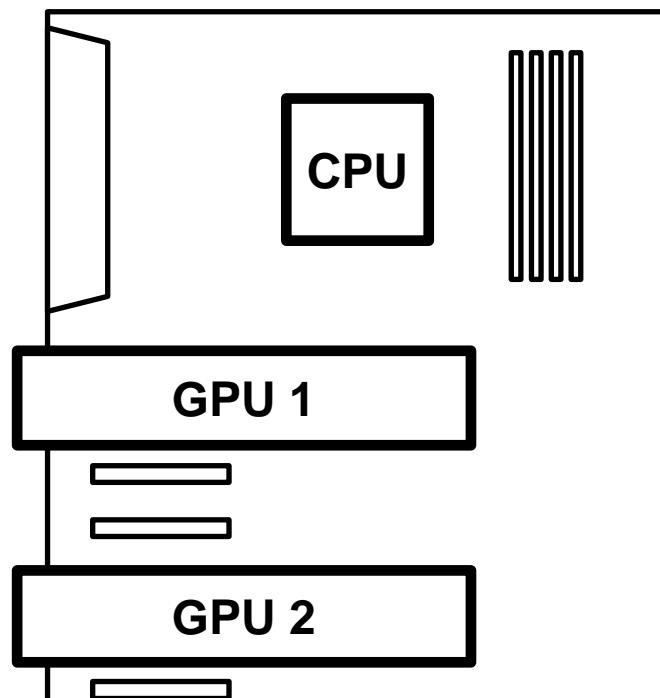
COMMAND BUFFER



Semaphore

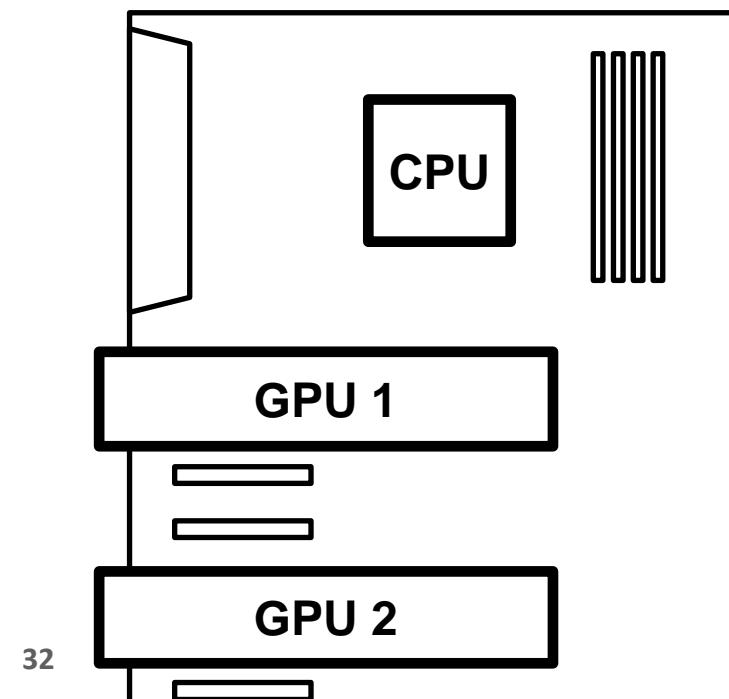
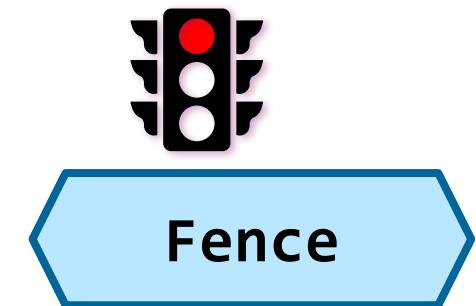
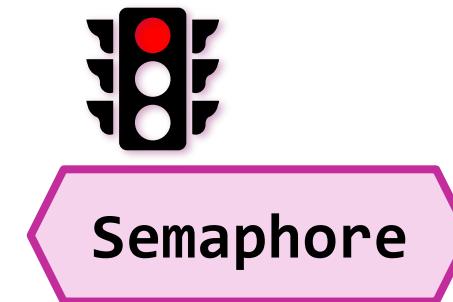


Fence



Command Recording and Queue Submission

QUEUE



Command Recording and Queue Submission

QUEUE

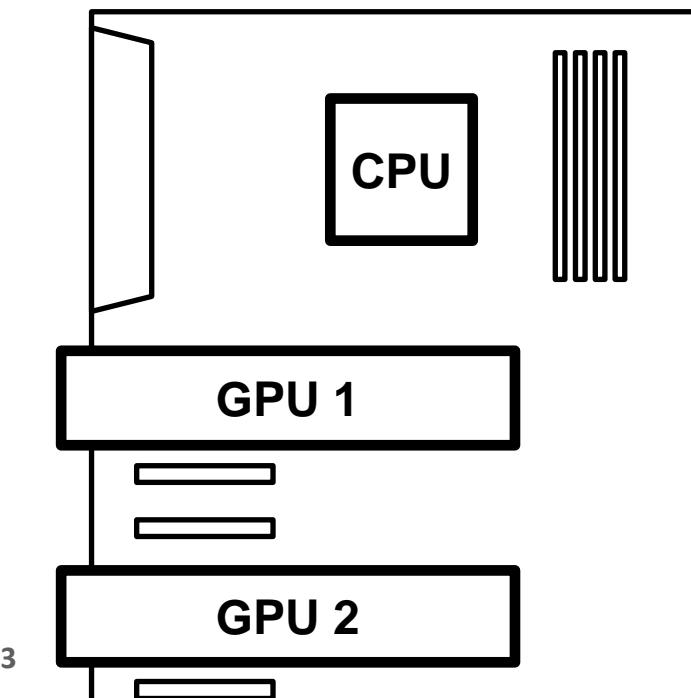
BATCH



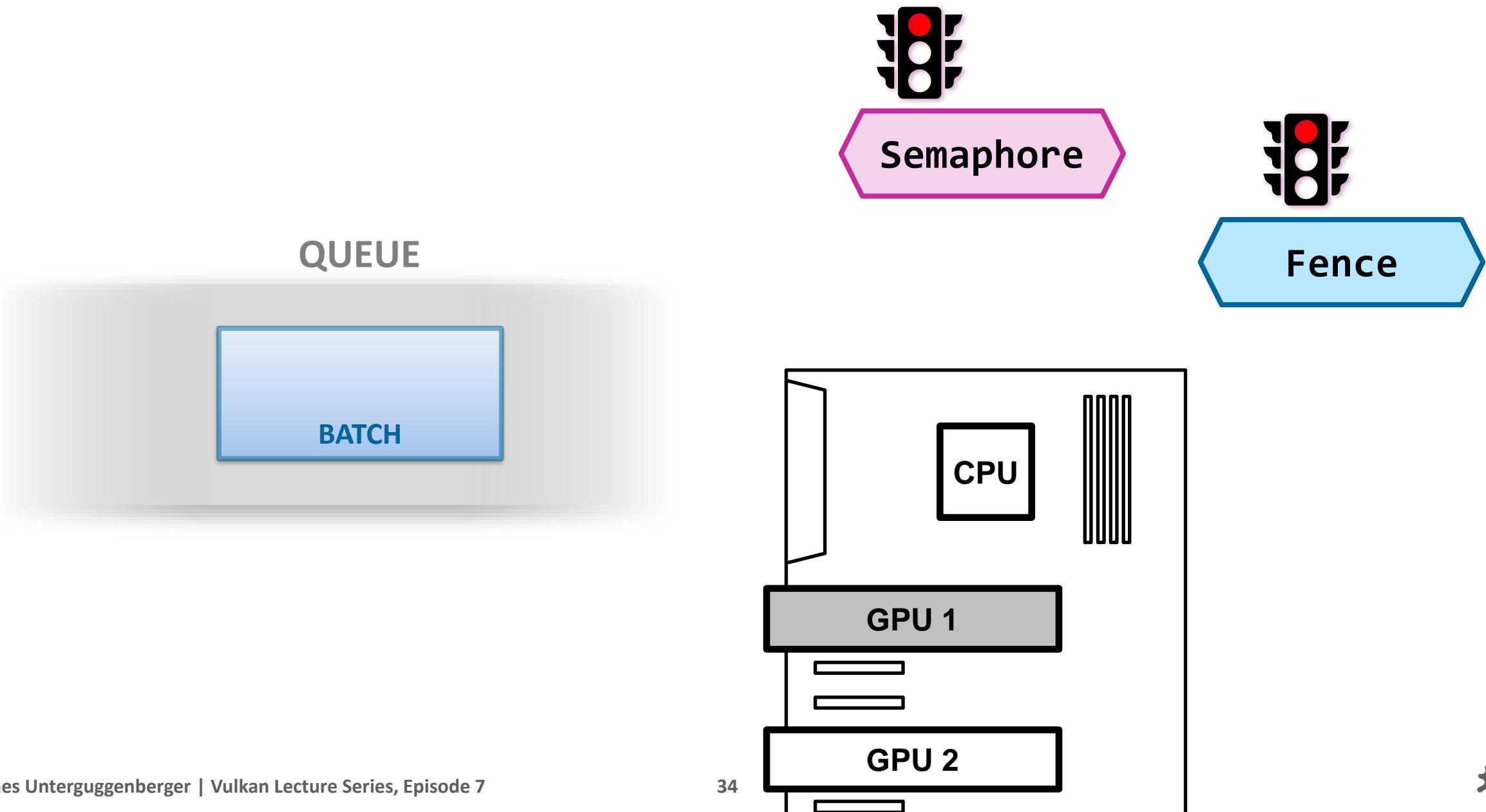
Semaphore



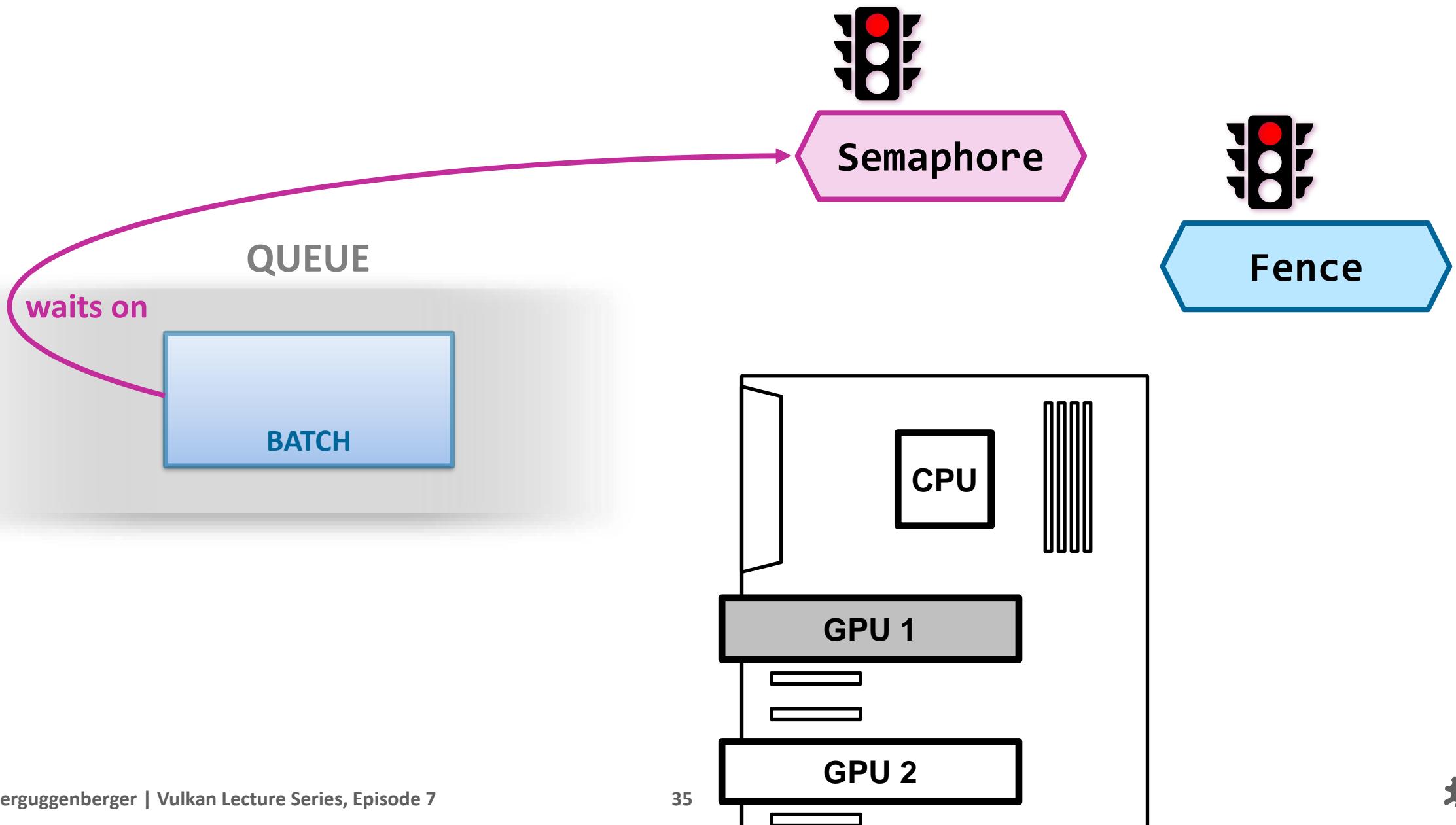
Fence



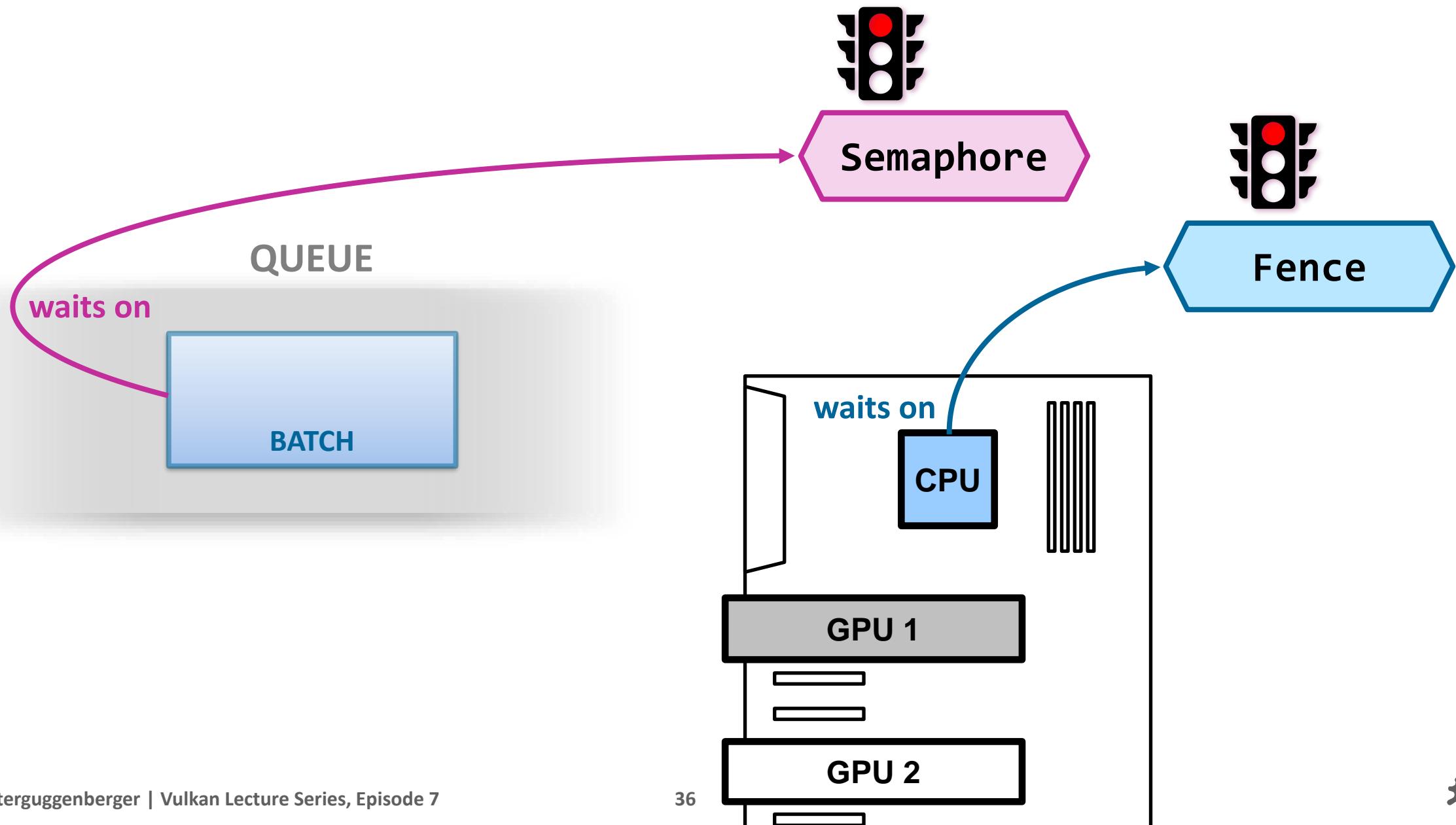
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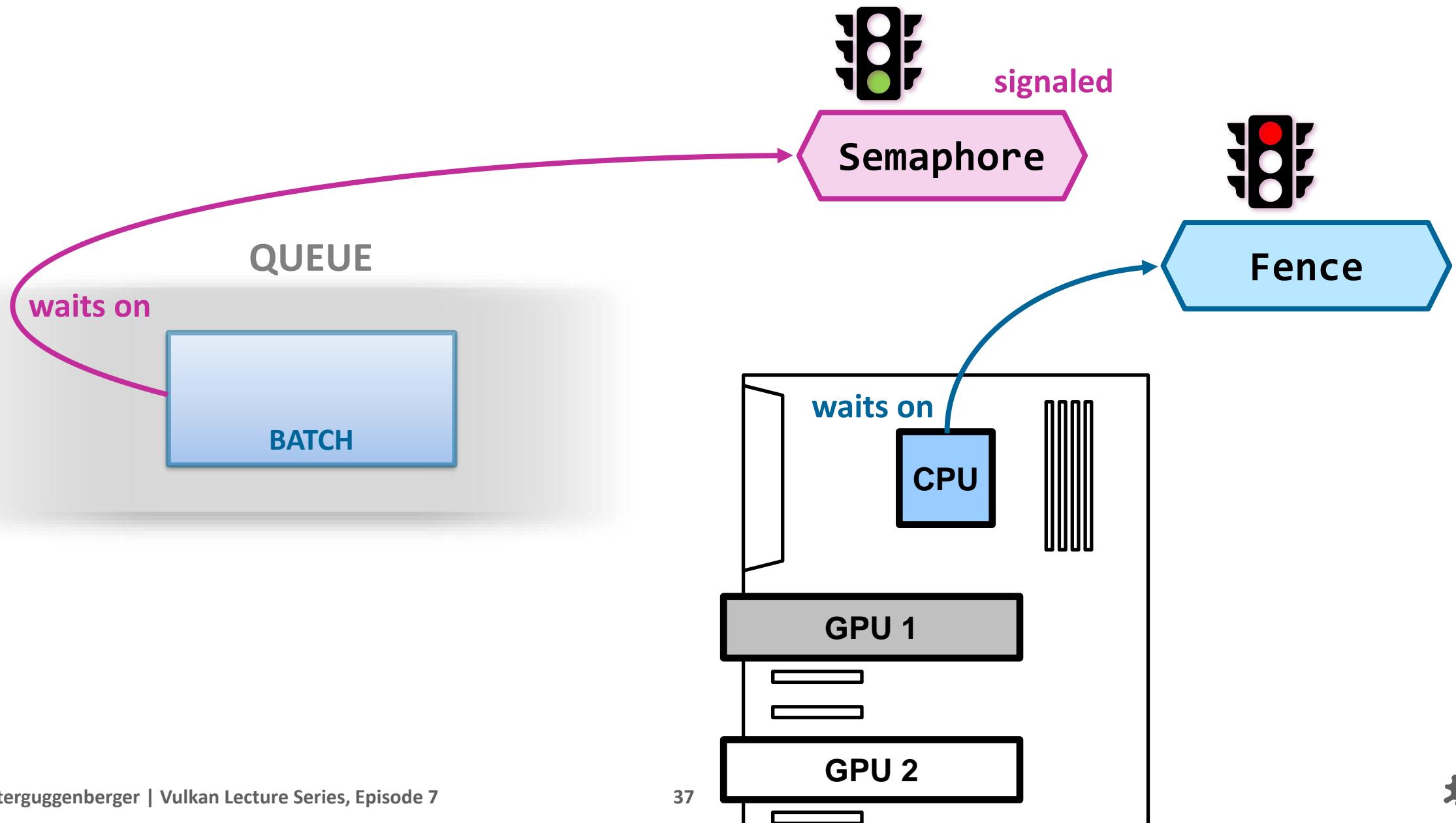
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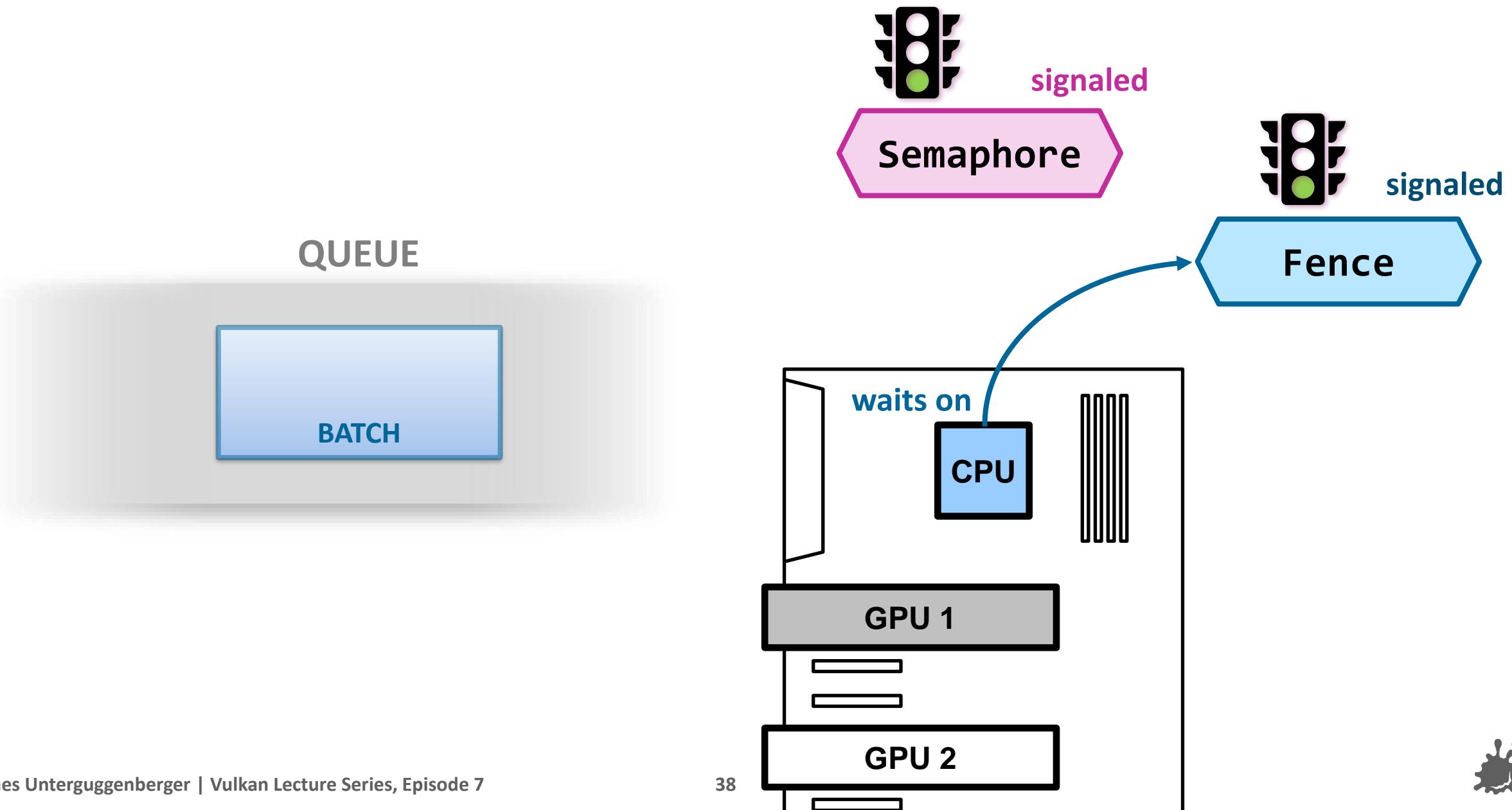
Command Recording and Queue Submission



Command Recording and Queue Submission



Command Recording and Queue Submission



Overview of Synchronization Methods

- Wait Idle Operations
- Fences
- Semaphores
 - Binary Semaphores
 - Timeline Semaphores
- Pipeline Barriers
 - Execution Barriers
 - Memory Barriers
- Render Pass Subpass Dependencies
- Events

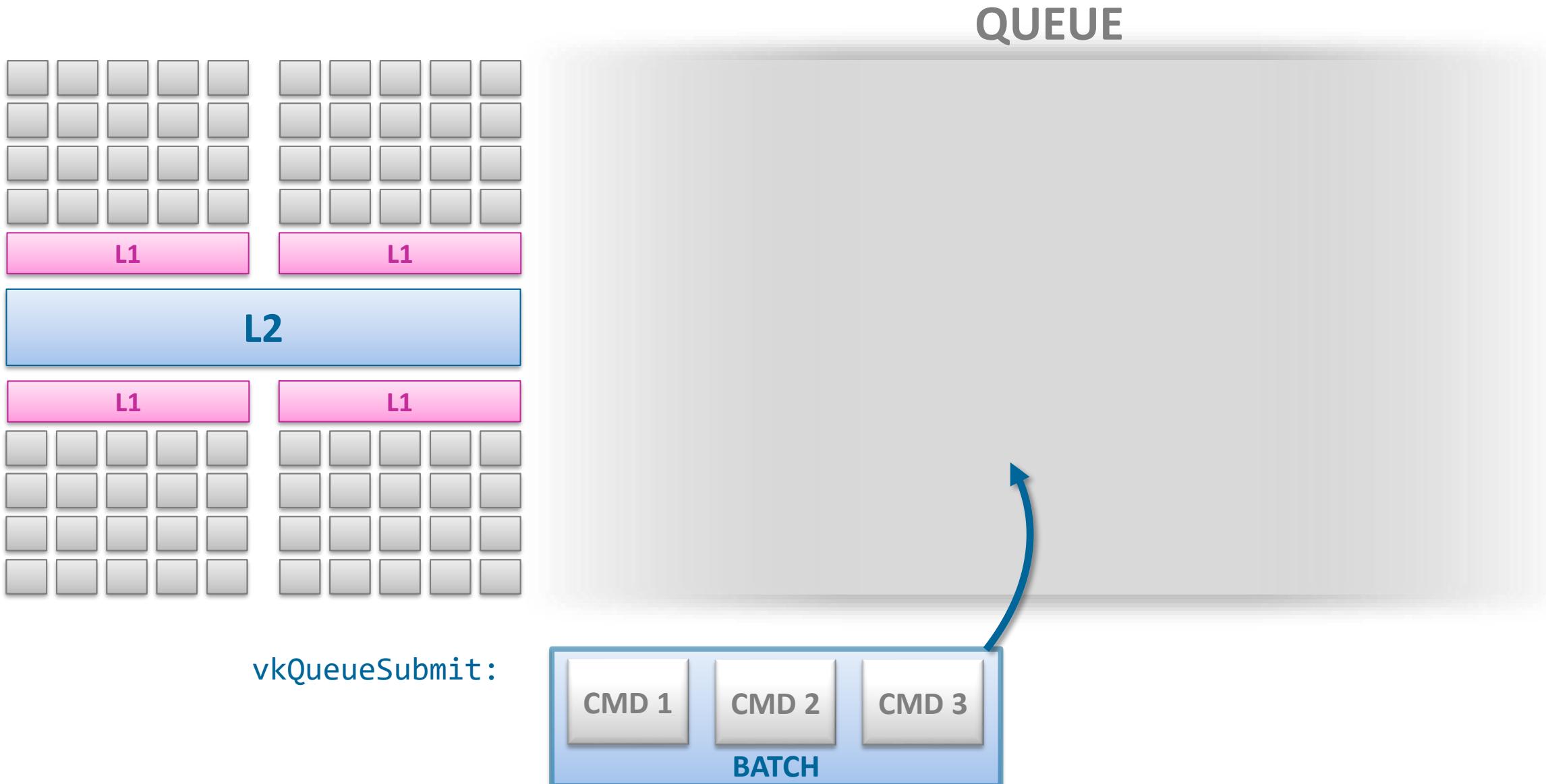


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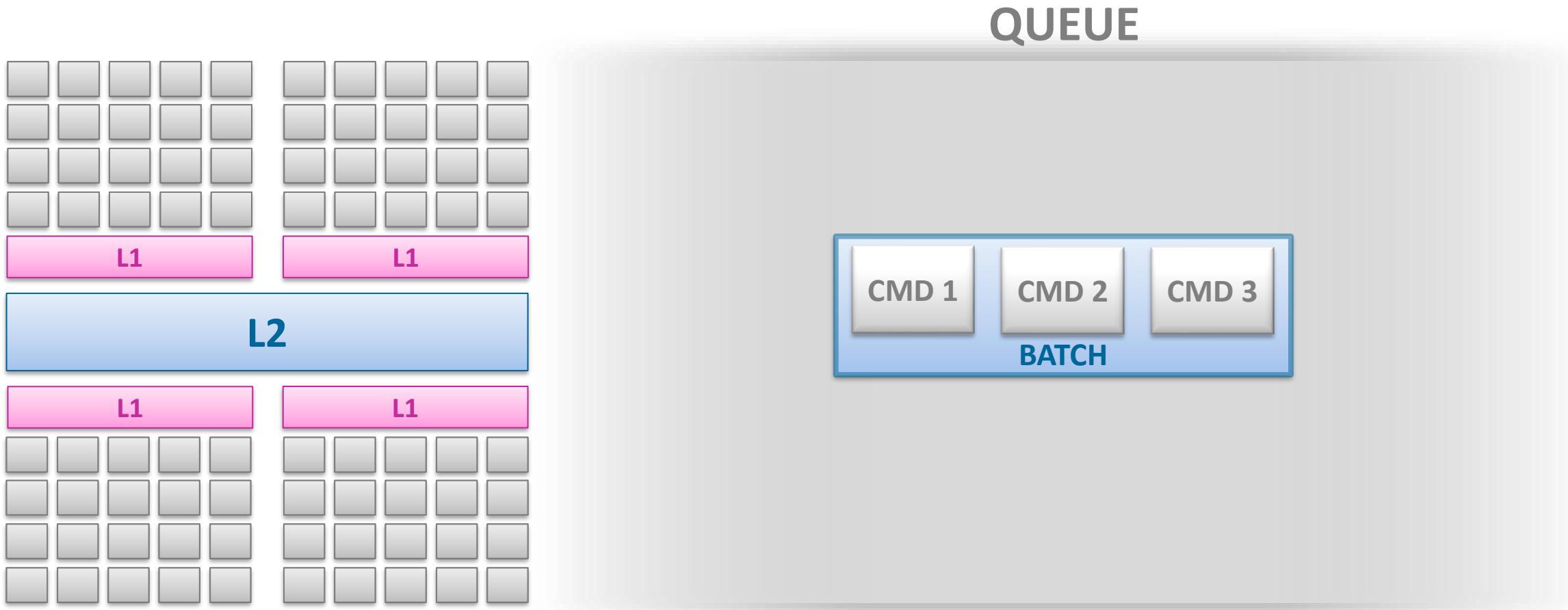
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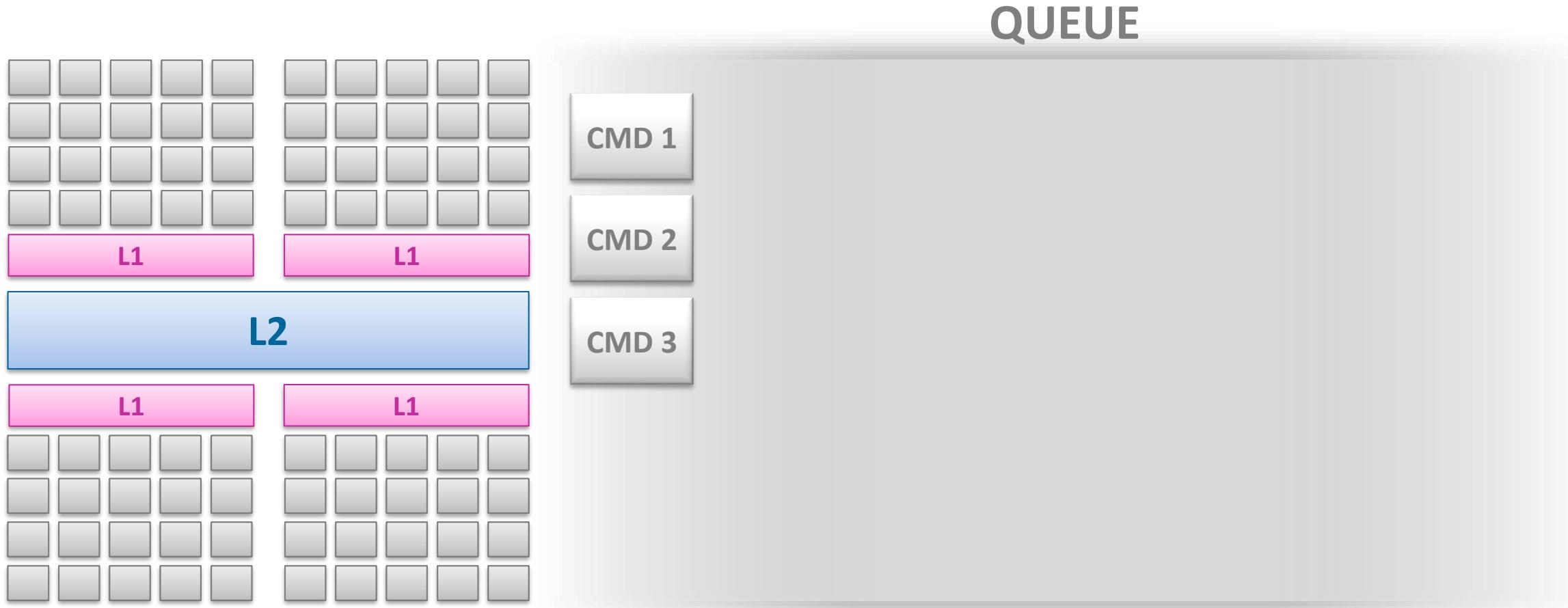
Wait Idle Operations



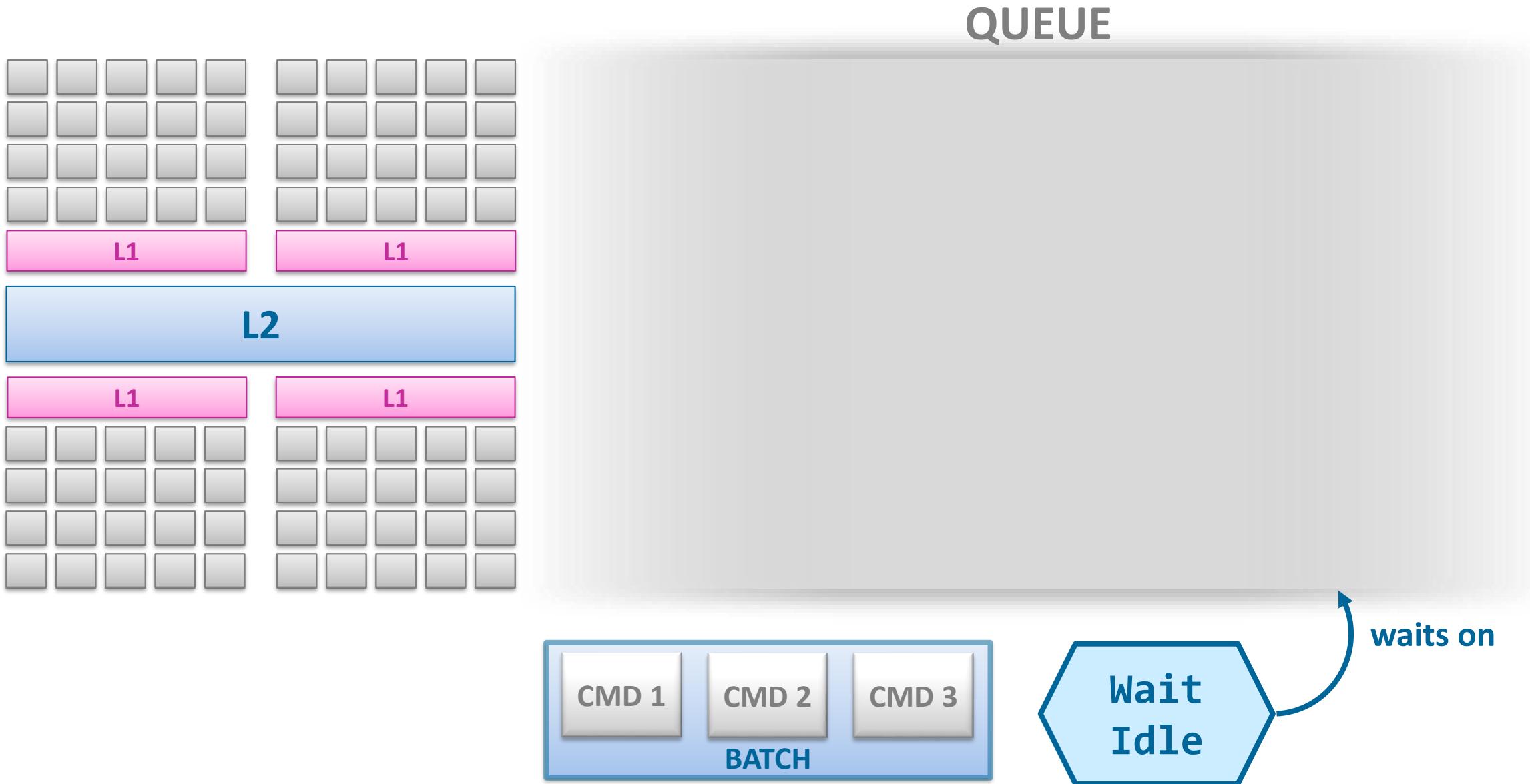
Wait Idle Operations



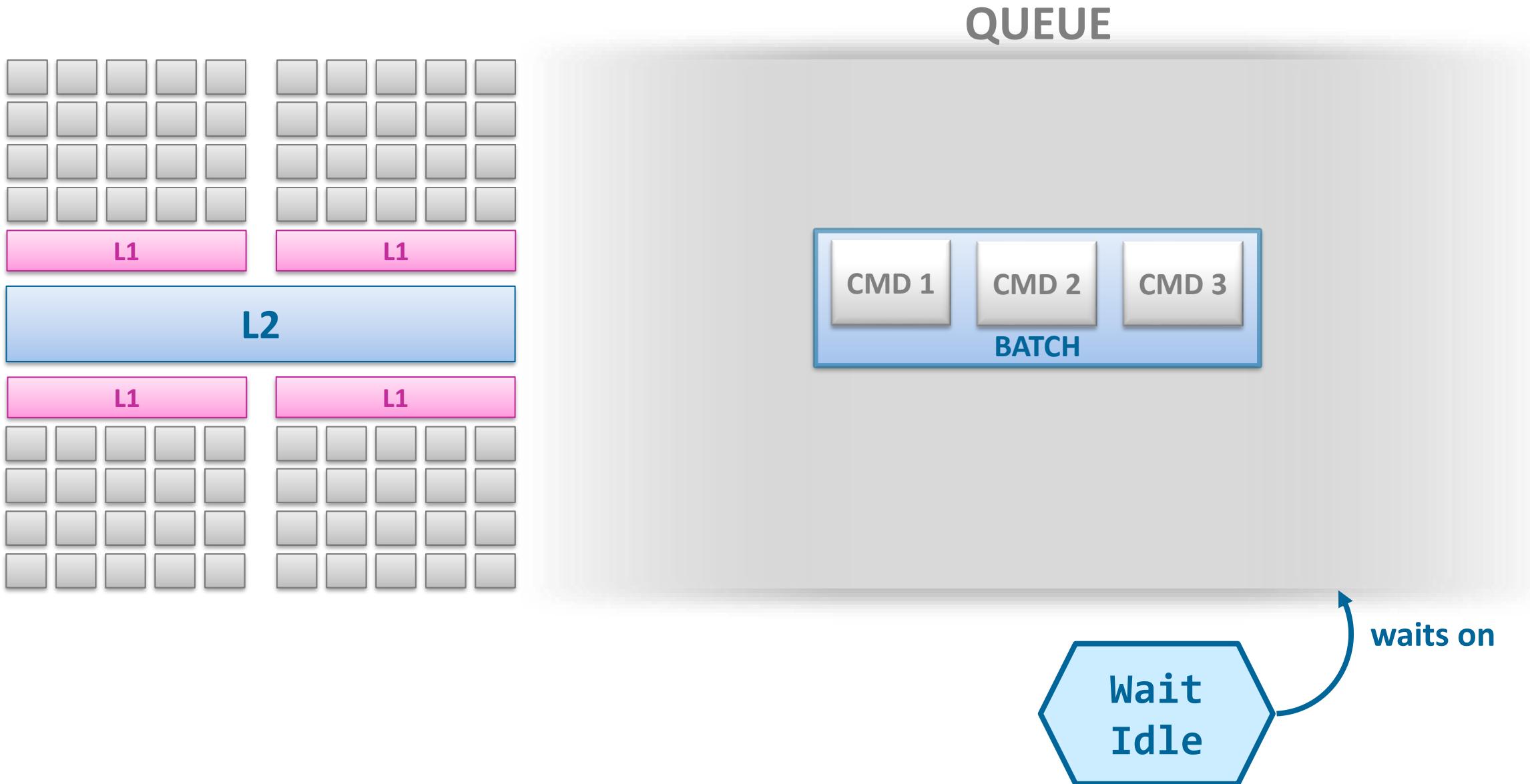
Wait Idle Operations



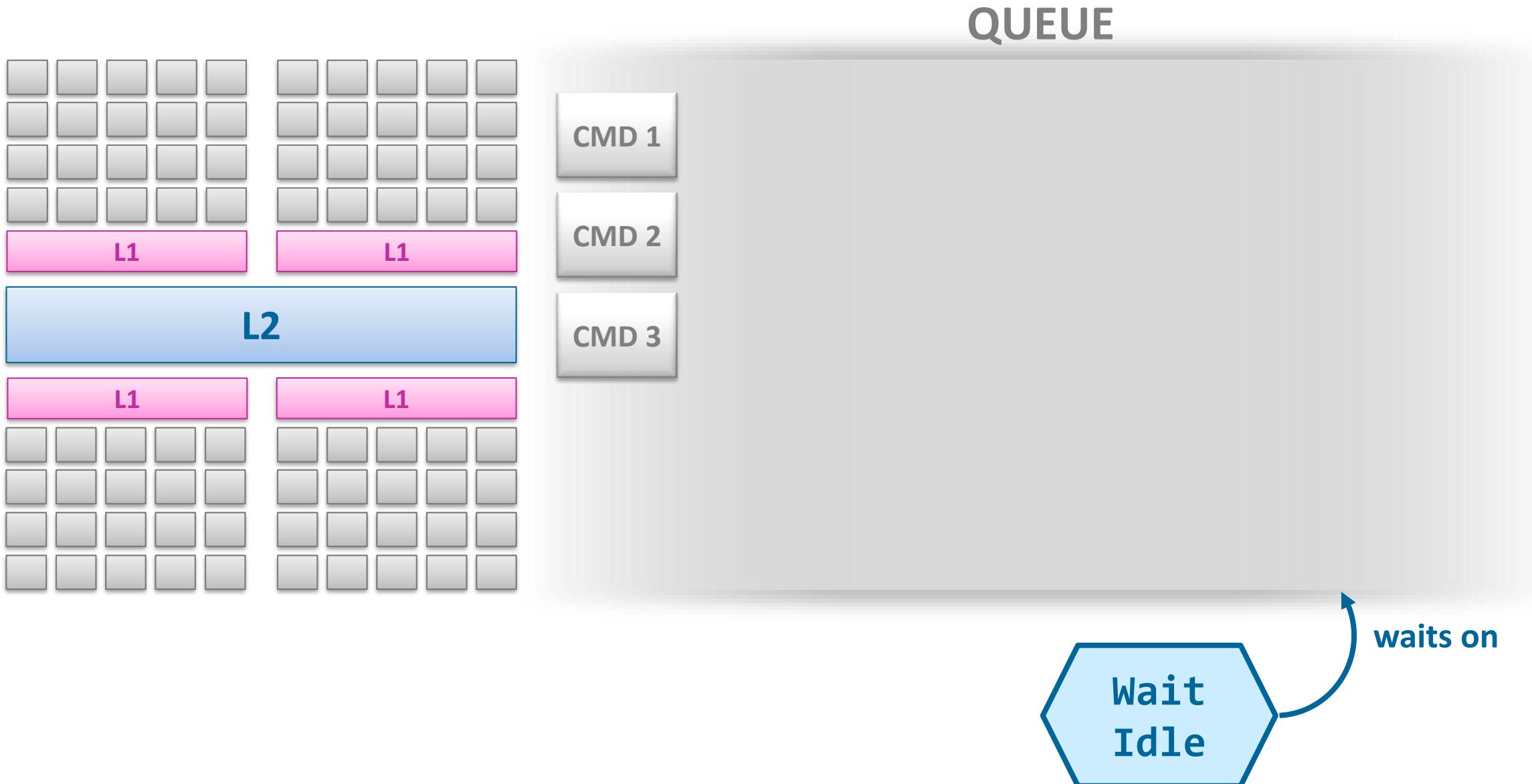
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Wait Idle Operations

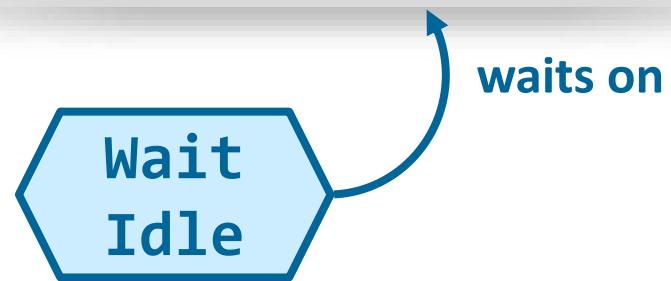


Wait Idle Operations

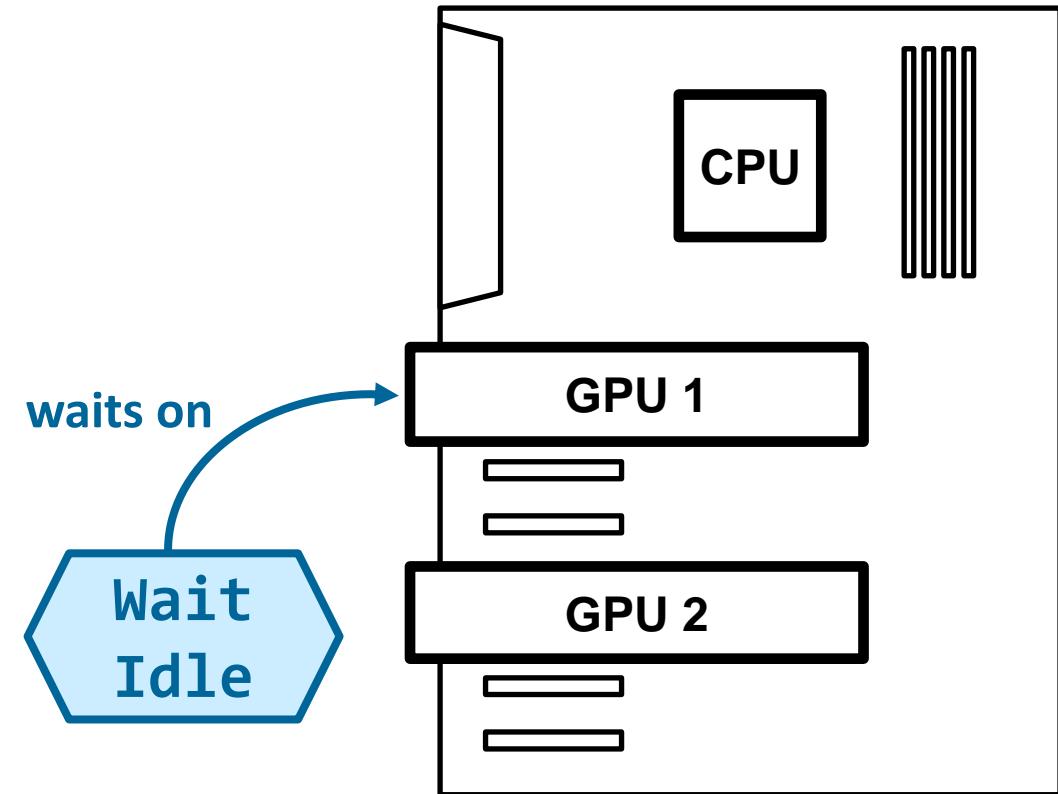


Wait Idle Operations

QUEUE



```
VkQueue queue;  
vkQueueWaitIdle(queue);
```



```
VkDevice device;  
vkDeviceWaitIdle(device);
```



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Overview of Synchronization Methods

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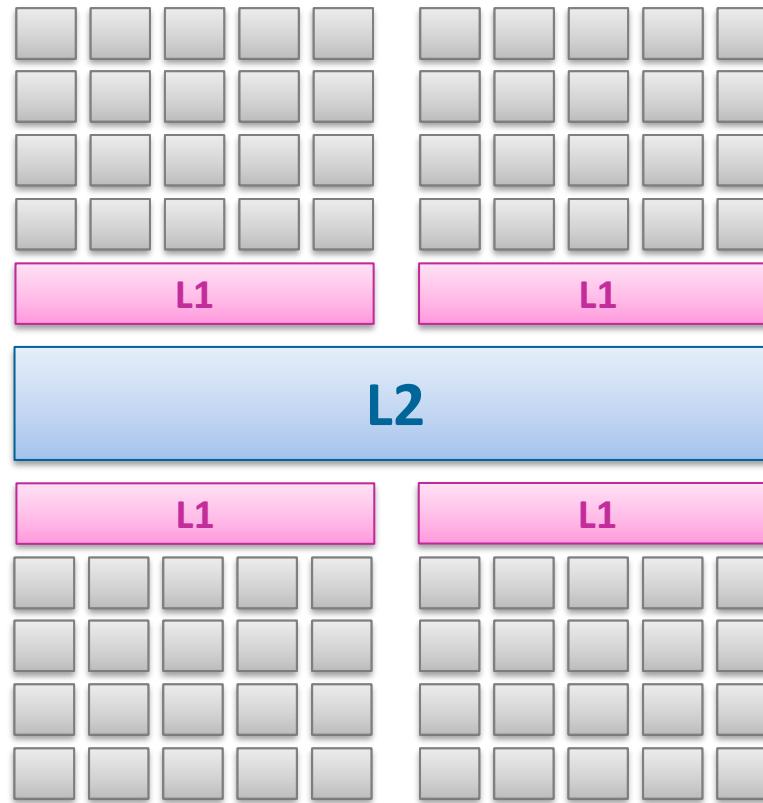
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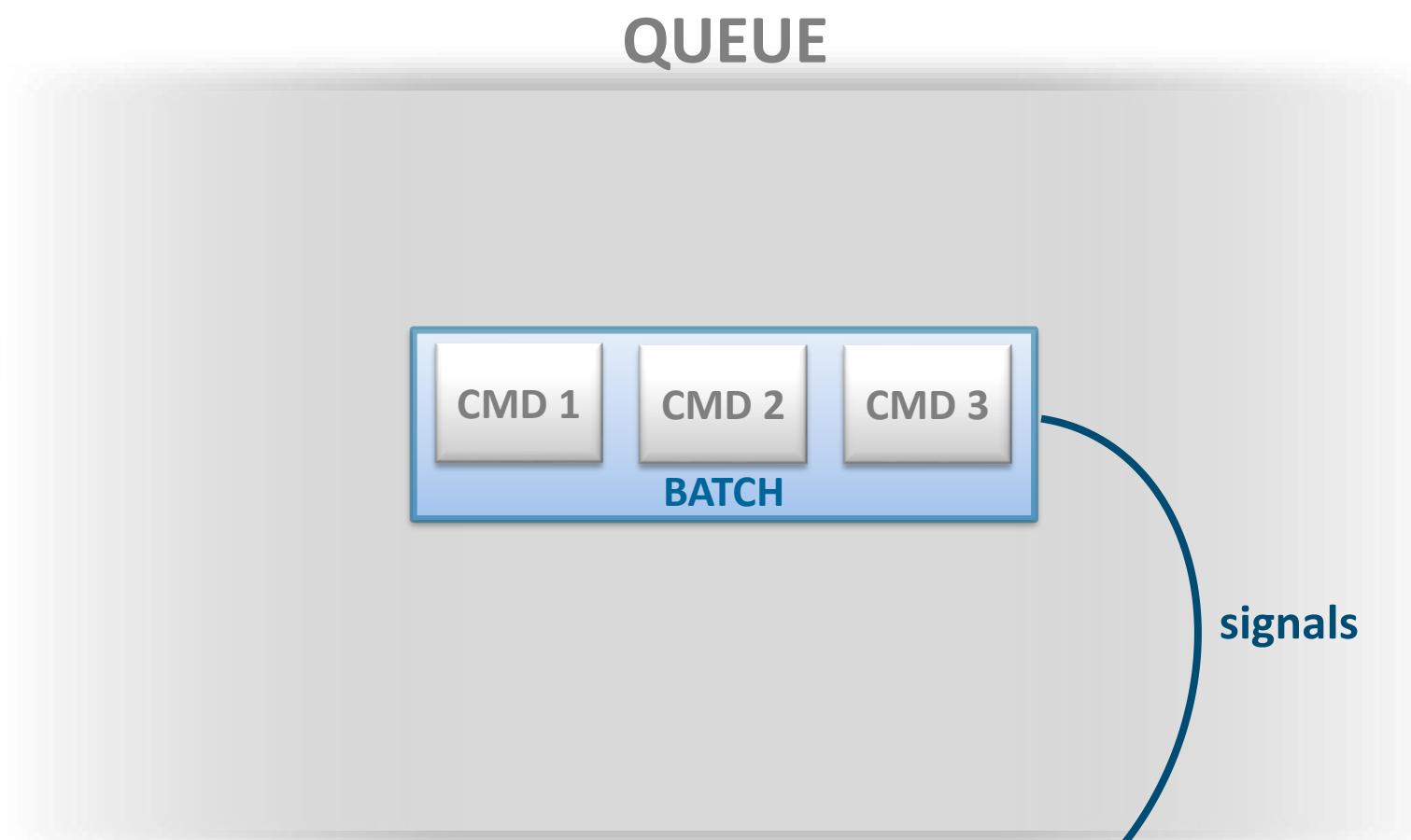
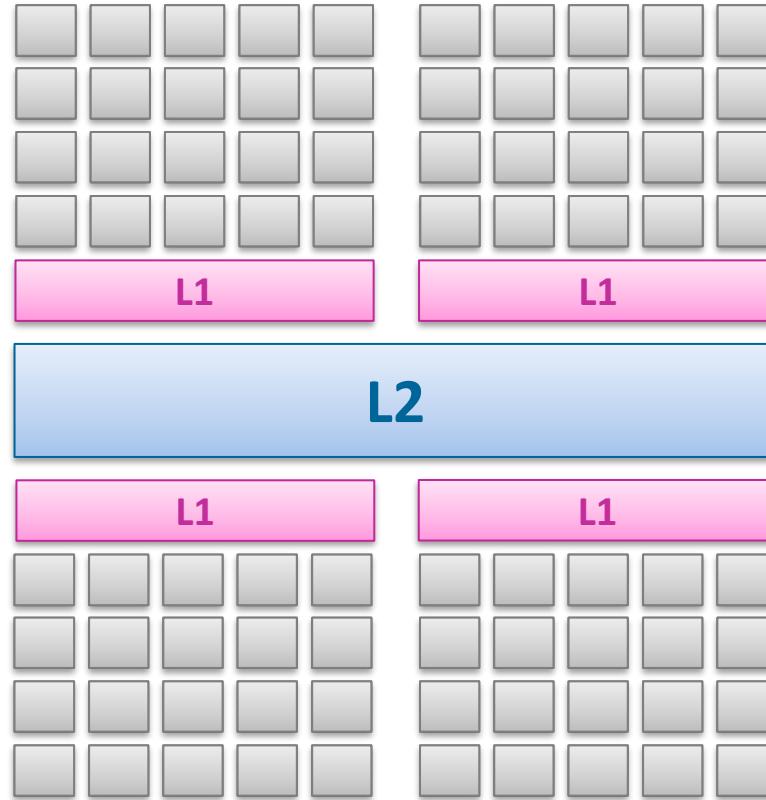


Fences

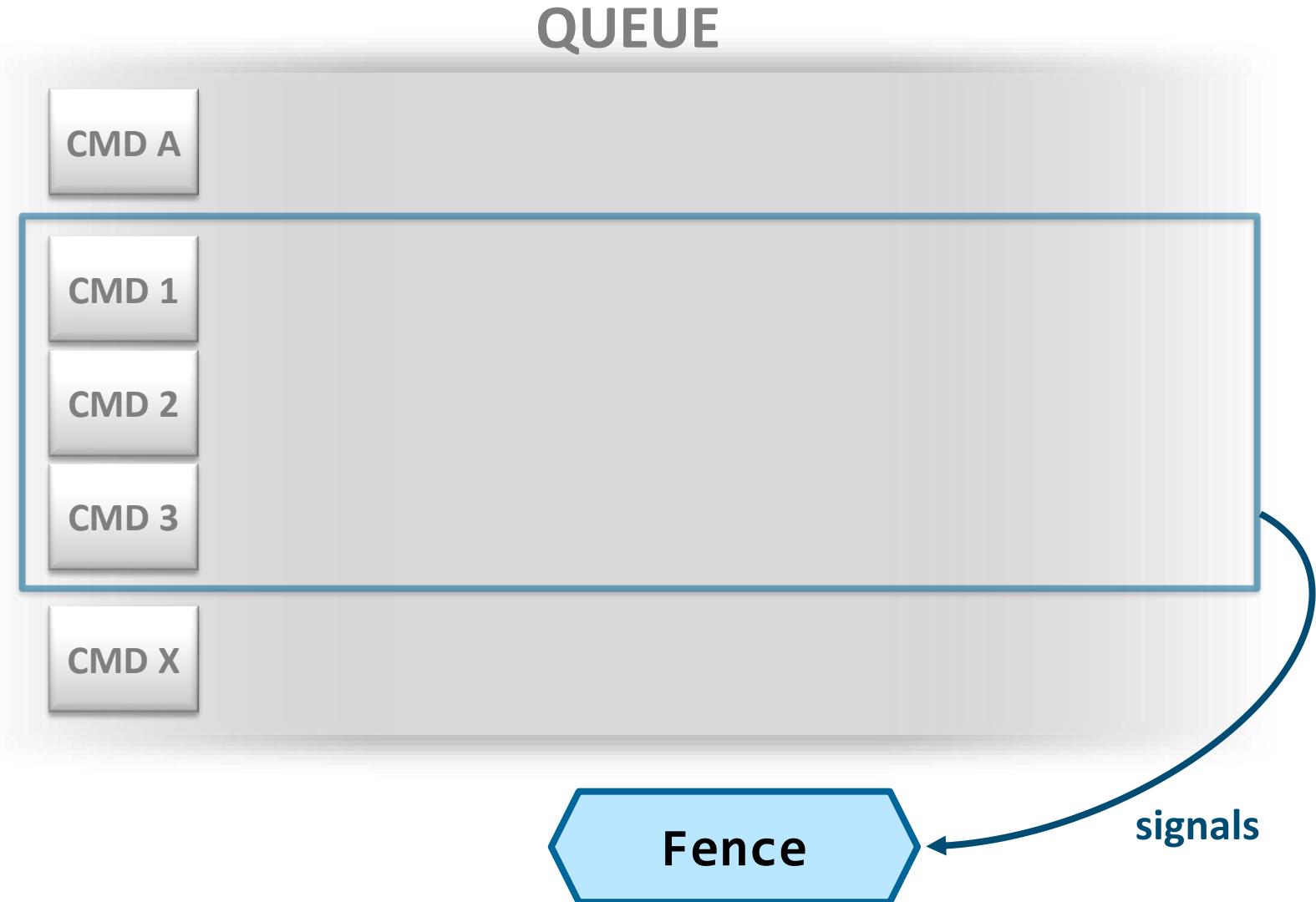
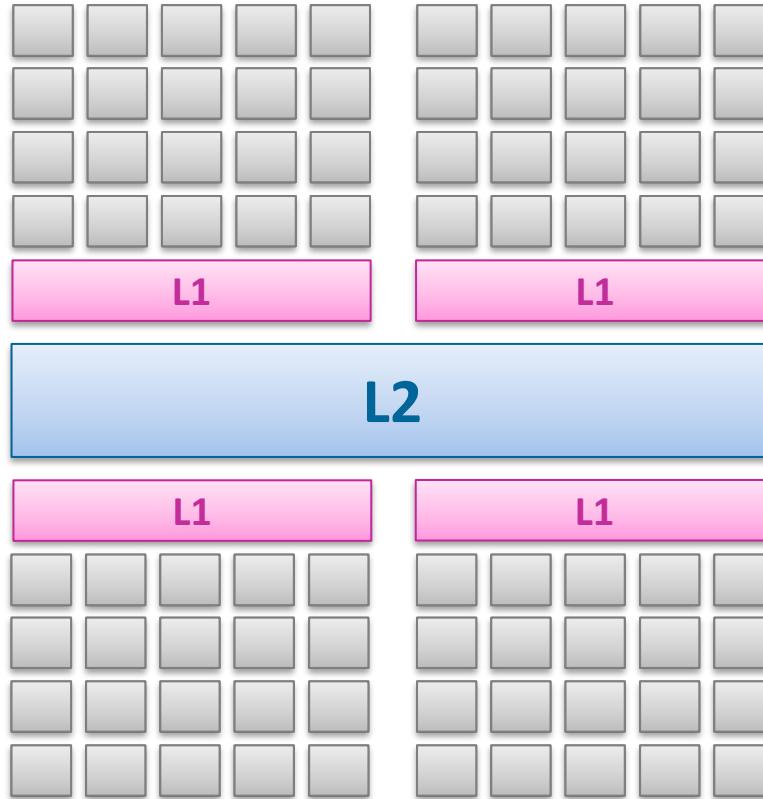
QUEUE



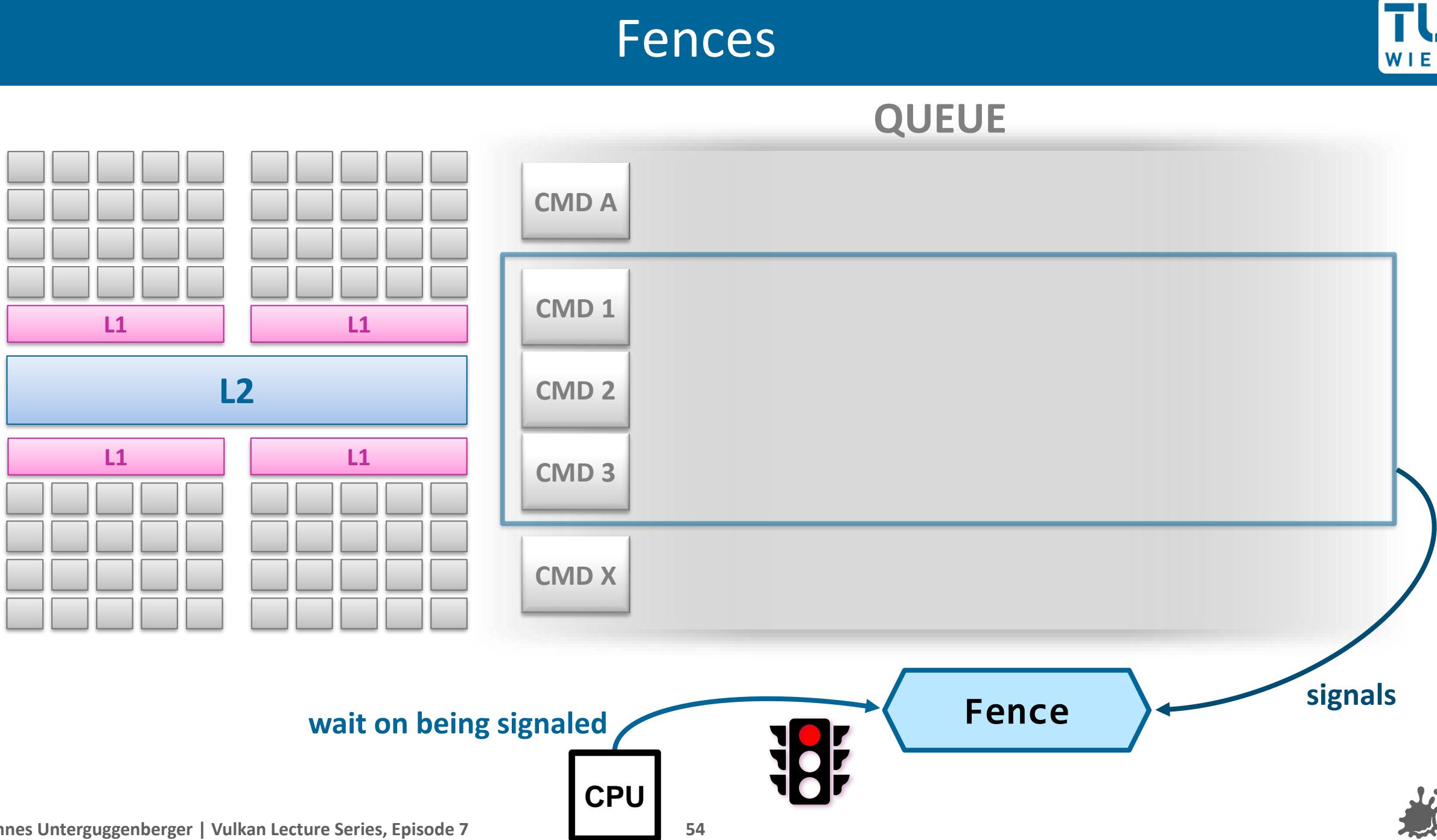
Fences



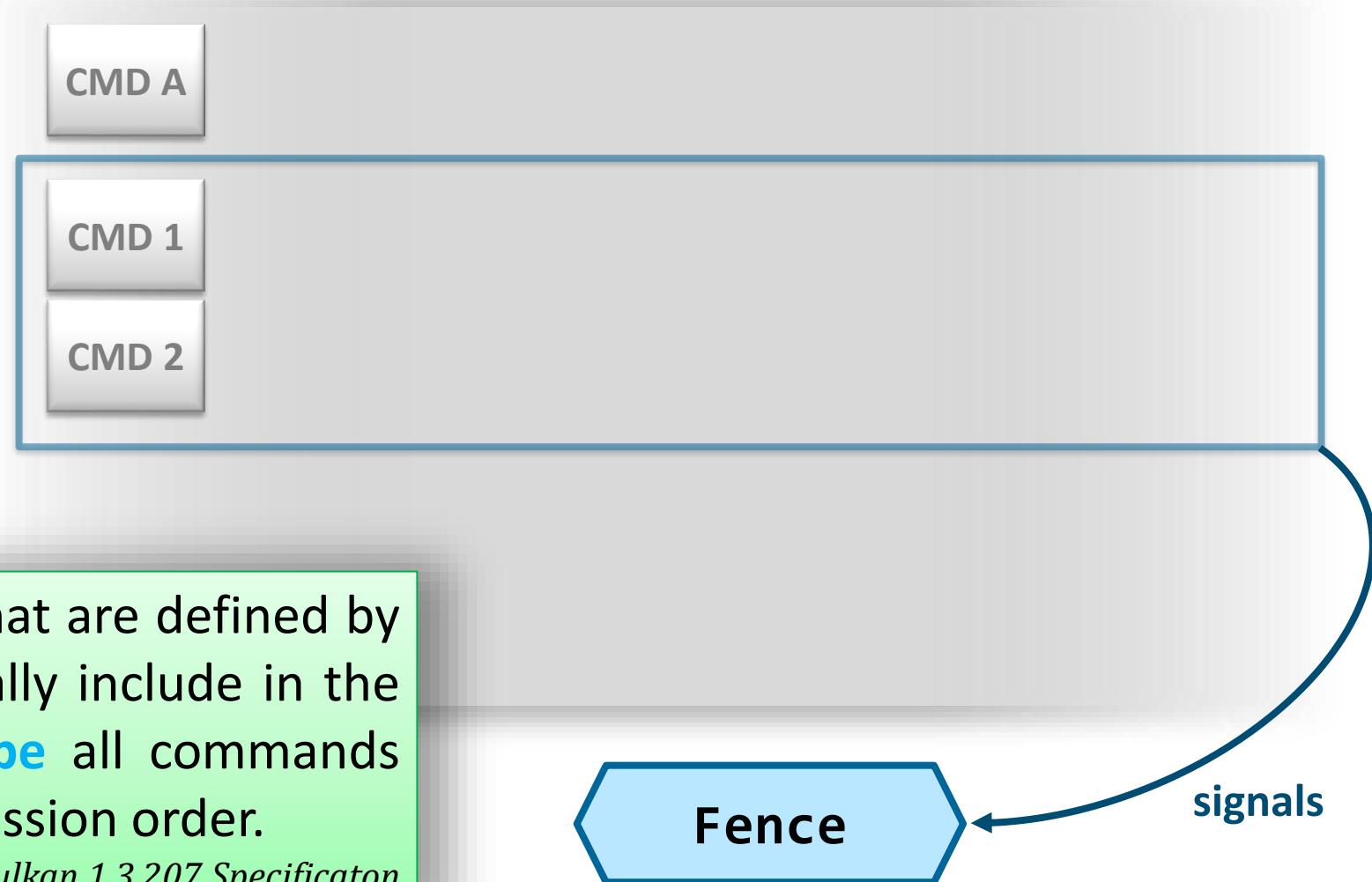
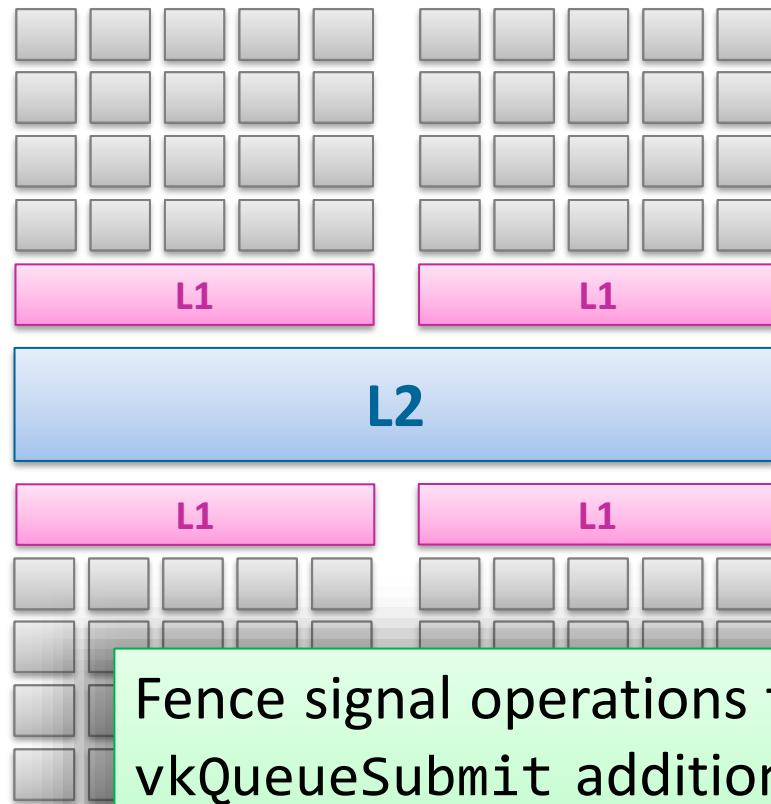
Fences



Fences



Fences



Fence signal operations that are defined by `vkQueueSubmit` additionally include in the **first synchronization scope** all commands that occur earlier in submission order.

The Khronos Group. Vulkan 1.3.207 Specification



The **synchronization scopes** define which other operations a synchronization command is able to create execution dependencies with.
[...]

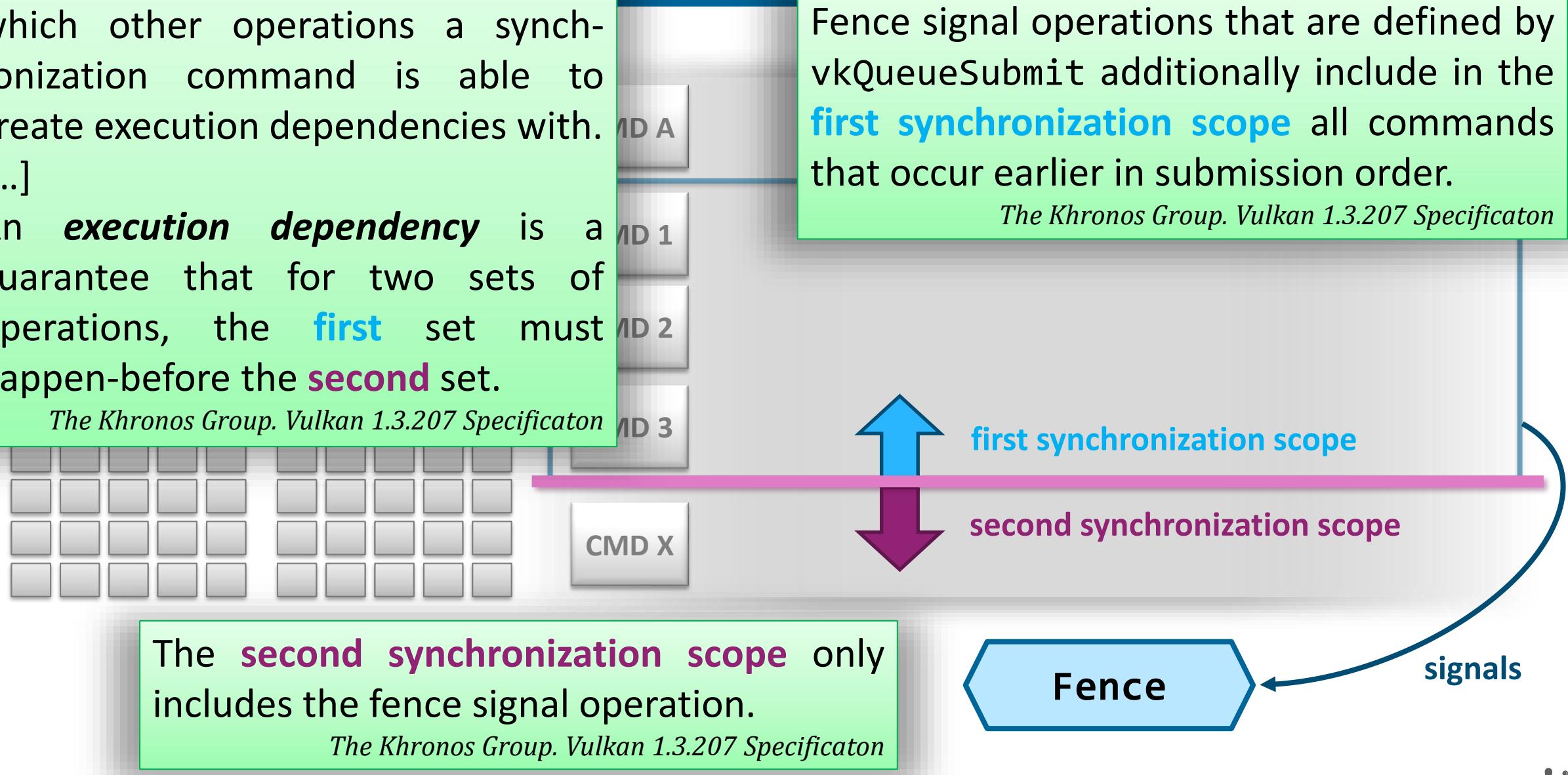
An **execution dependency** is a guarantee that for two sets of operations, the **first** set must happen-before the **second** set.

The Khronos Group. Vulkan 1.3.207 Specification

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- Render Pass Subpass Dependencies
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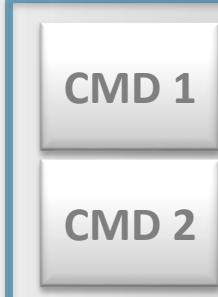
Overview of Synchronization Methods

- Wait Idle Operations (device -> host sync)
- Fences (device -> host sync)
- Semaphores
 - Binary Semaphores
 - Timeline Semaphores
- Pipeline Barriers
 - Execution Barriers
 - Memory Barriers
- Render Pass Subpass Dependencies
- Events



Semaphores

QUEUE 1:



waits on



Semaphore

signals

QUEUE 2:



Semaphores

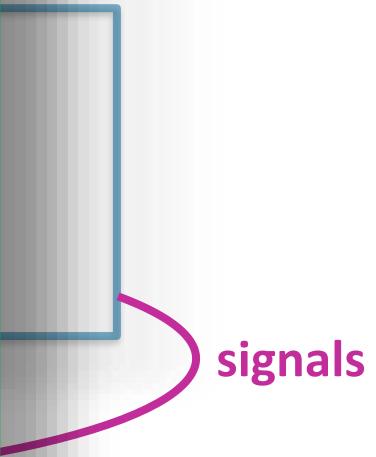
A binary semaphore **must** be signaled, or have an associated semaphore signal operation that is pending execution.

Any semaphore signal operations on which the pending binary semaphore signal operation depends **must** also be completed or pending execution.

There **must** be no other queue waiting on the same binary semaphore when the operation executes.

The Khronos Group. Vulkan 1.3.207 Specification

QUEUE 2:



Overview of Synchronization Methods

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Overview of Synchronization Methods

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- Fences (device -> host sync)
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Overview of Synchronization Methods

- Wait Idle Operations (device -> host sync)
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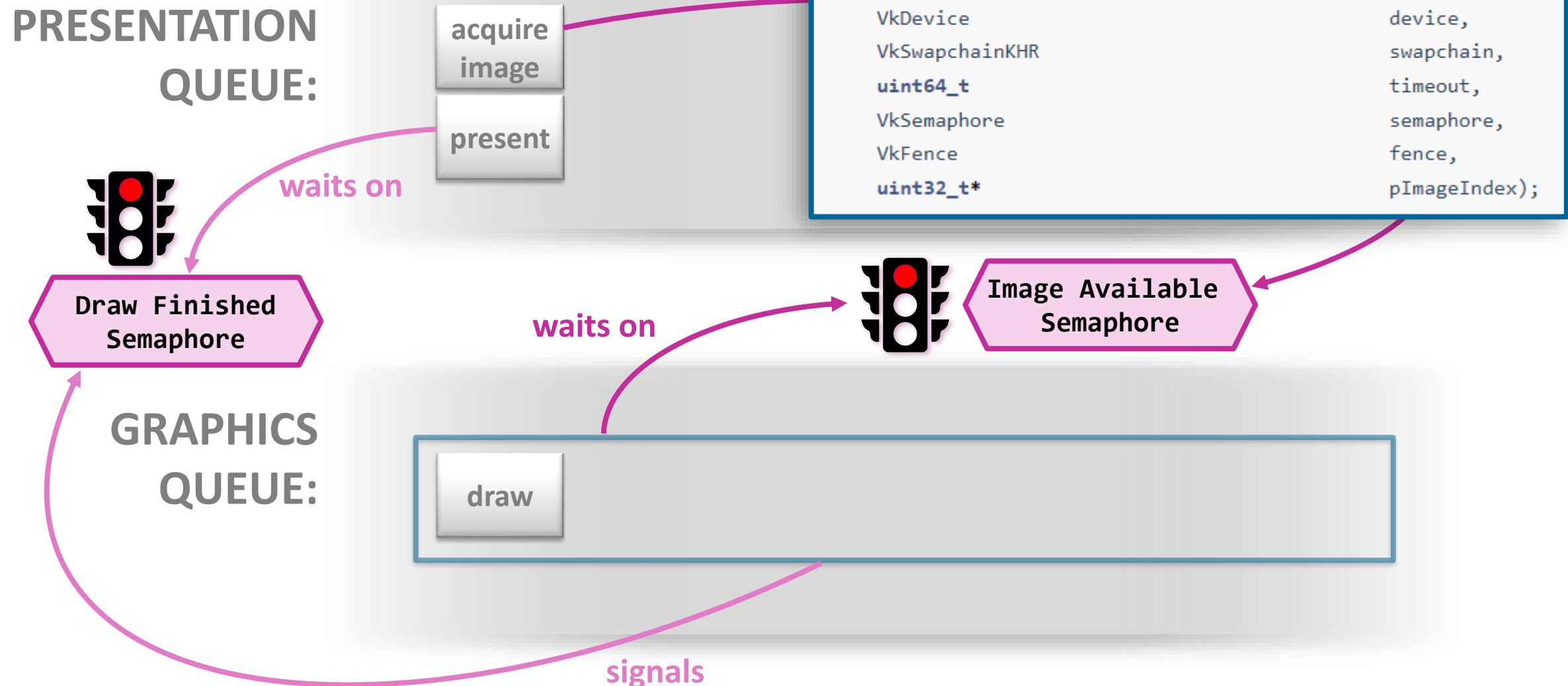
Overview of Synchronization Methods

- Wait Idle Operations (device -> host sync)
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Binary Semaphores

PRESENTATION QUEUE:



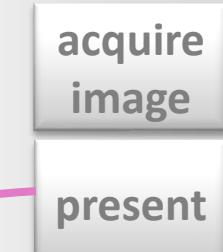
Binary Semaphores

PRESENTATION QUEUE:



Draw Finished Semaphore

GRAPHICS QUEUE:



waits on

waits on

draw

signals

```
// Provided by VK_KHR_swapchain
VkResult vkAcquireNextImageKHR(
    VkDevice device,
    VkSwapchainKHR swapchain,
    uint64_t timeout,
    VkSemaphore semaphore,
    VkFence fence,
    uint32_t* pImageIndex);
```

```
// Provided by VK_VERSION_1_3
typedef struct VkSubmitInfo2 {
    VkStructureType sType;
    const void* pNext;
    VkSubmitFlags flags;
    uint32_t waitSemaphoreCount;
    const VkSemaphoreSubmitInfo* pWaitSemaphoreInfos;
    uint32_t commandBufferCount;
    const VkCommandBufferSubmitInfo* pCommandBufferInfos;
    uint32_t signalSemaphoreCount;
    const VkSemaphoreSubmitInfo* pSignalSemaphoreInfos;
} VkSubmitInfo2;
```

Binary Semaphores

PRESENTATION QUEUE:

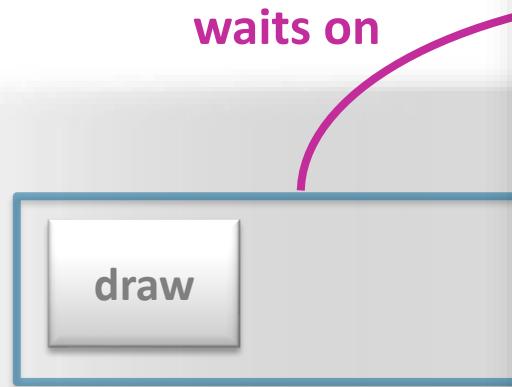


Draw Finished Semaphore

GRAPHICS QUEUE:



waits on



waits on

signals

```
// Provided by VK_KHR_swapchain
VkResult vkAcquireNextImageKHR(
    VkDevice device,
    VkSwapchainKHR swapchain,
    uint64_t timeout,
    VkSemaphore semaphore,
    VkFence fence,
    uint32_t* pImageIndex);
```

```
// Provided by VK_VERSION_1_3
typedef struct VkSubmitInfo2 {
    VkStructureType sType;
    const void* pNext;
    VkSubmitFlags flags;
    uint32_t waitSemaphoreInfoCount;
    const VkSemaphoreSubmitInfo* pWaitSemaphoreInfos;
    uint32_t commandBufferInfoCount;
    const VkCommandBufferSubmitInfo* pCommandBufferInfos;
    uint32_t signalSemaphoreInfoCount;
    const VkSemaphoreSubmitInfo* pSignalSemaphoreInfos;
} VkSubmitInfo2;
```

Binary Semaphores

PRESENTATION QUEUE:



Draw Finished
Semaphore

GRAPHICS QUEUE:

acquire
image
present

waits on

waits on

draw

signals

```
// Provided by VK_KHR_swapchain
VkResult vkAcquireNextImageKHR(
    VkDevice device,
    VkSwapchainKHR swapchain,
    uint64_t timeout,
    VkSemaphore semaphore,
    VkFence fence,
    uint32_t* pImageIndex);
```

```
// Provided by VK_VERSION_1_3
typedef struct VkSubmitInfo2 {
    VkStructureType sType;
    const void* pNext;
    VkSubmitFlags flags;
    uint32_t waitSemaphoreInfoCount;
    const VkSemaphoreSubmitInfo* pWaitSemaphoreInfos;
    uint32_t commandBufferInfoCount;
    const VkCommandBufferSubmitInfo* pCommandBufferInfos;
    uint32_t signalSemaphoreInfoCount;
    const VkSemaphoreSubmitInfo* pSignalSemaphoreInfos;
} VkSubmitInfo2;
```

Binary Semaphores

PRESERATION QUEUE:



waits on

acquire
image
present

Draw_Finished

```
// Provided by VK_KHR_swapchain
typedef struct VkPresentInfoKHR {
    VkStructureType           sType;
    const void*               pNext;
    uint32_t                  waitSemaphoreCount;
    const VkSemaphore*         pWaitSemaphores;
    uint32_t                  swapchainCount;
    const VkSwapchainKHR*     pSwapchains;
    const uint32_t*            pImageIndices;
    VkResult*                 pResults;
} VkPresentInfoKHR;
```

```
// Provided by VK_KHR_swapchain
VkResult vkAcquireNextImageKHR(
    VkDevice                         device,
    VkSwapchainKHR                   swapchain,
    uint64_t                          timeout,
    VkSemaphore                      semaphore,
    VkFence                          fence,
    uint32_t*                        pImageIndex);
```

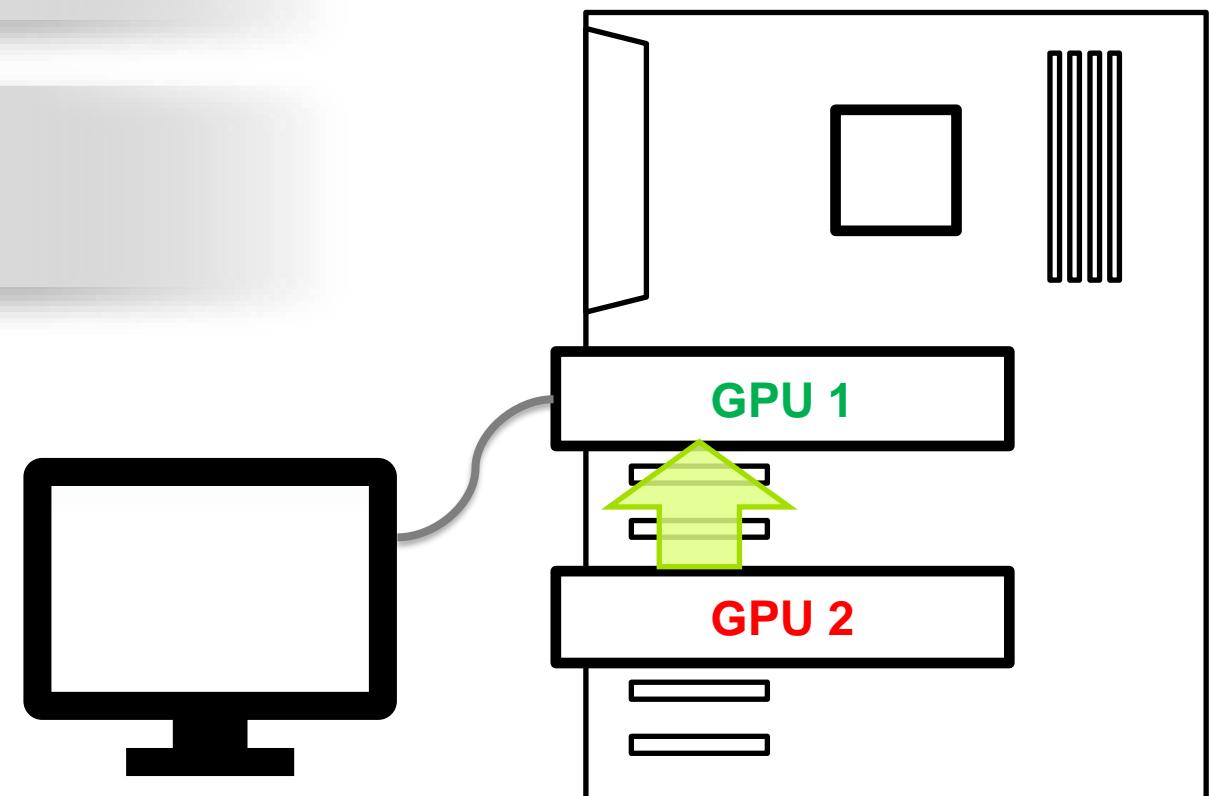
```
// Provided by VK_VERSION_1_3
typedef struct VkSubmitInfo2 {
    VkStructureType           sType;
    const void*               pNext;
    VkSubmitFlags              flags;
    uint32_t                  waitSemaphoreInfoCount;
    const VkSemaphoreSubmitInfo* pWaitSemaphoreInfos;
    uint32_t                  commandBufferInfoCount;
    const VkCommandBufferSubmitInfo* pCommandBufferInfos;
    uint32_t                  signalSemaphoreInfoCount;
    const VkSemaphoreSubmitInfo* pSignalSemaphoreInfos;
} VkSubmitInfo2;
```

PRESENTATION QUEUE:

GRAPHICS QUEUE:

present

draw



PRESENTATION QUEUE:

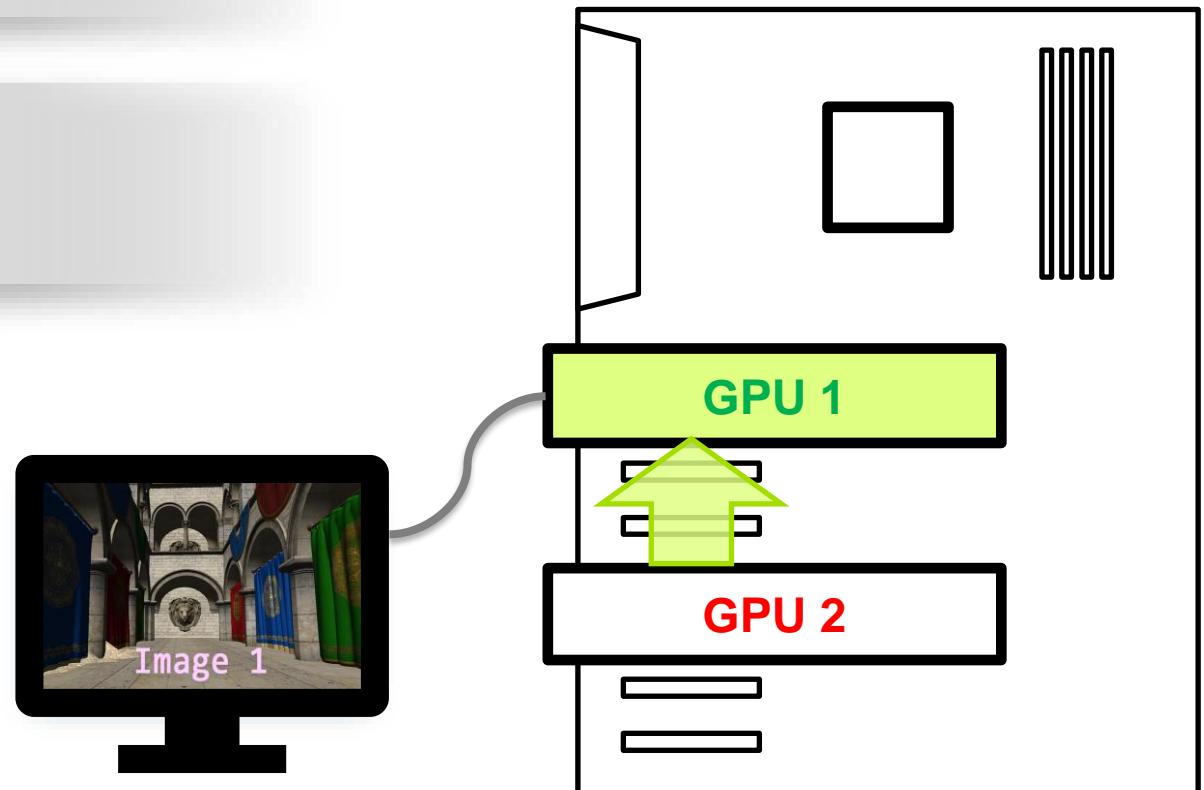
All elements of the pWaitSemaphores member of pPresentInfo **must** be created with a VkSemaphoreType of VK_SEMAPHORE_TYPE_BINARY.

The Khronos Group. Vulkan 1.3.207 Specicaton

All elements of the pWaitSemaphores member of pPresentInfo **must** be semaphores that are signaled, or have semaphore signal operations previously submitted for execution.

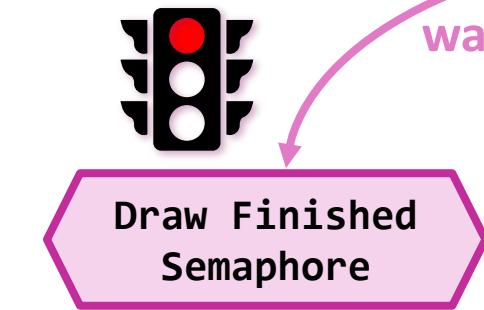
The Khronos Group. Vulkan 1.3.207 Specicaton

present

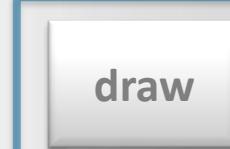
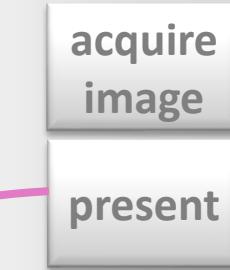


Binary Semaphores

PRESENTATION
QUEUE:



GRAPHICS
QUEUE:



waits on

signals



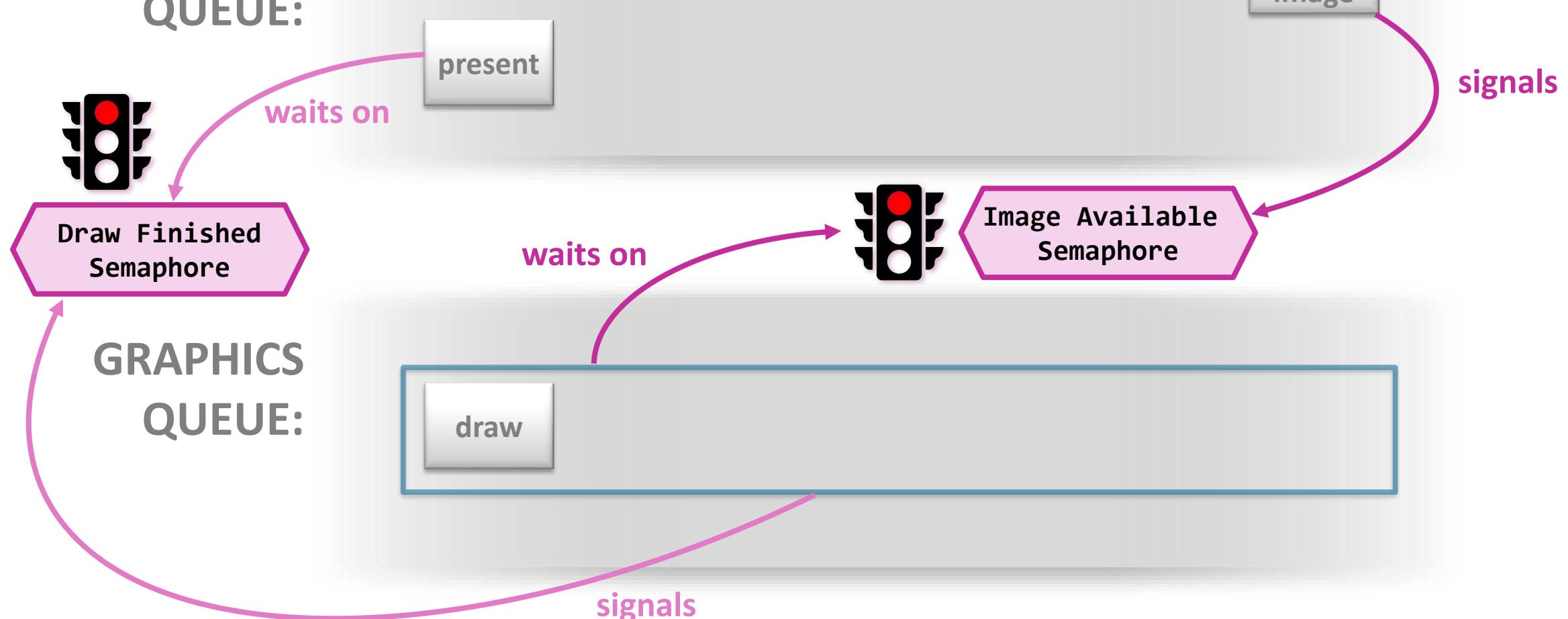
waits on

signals



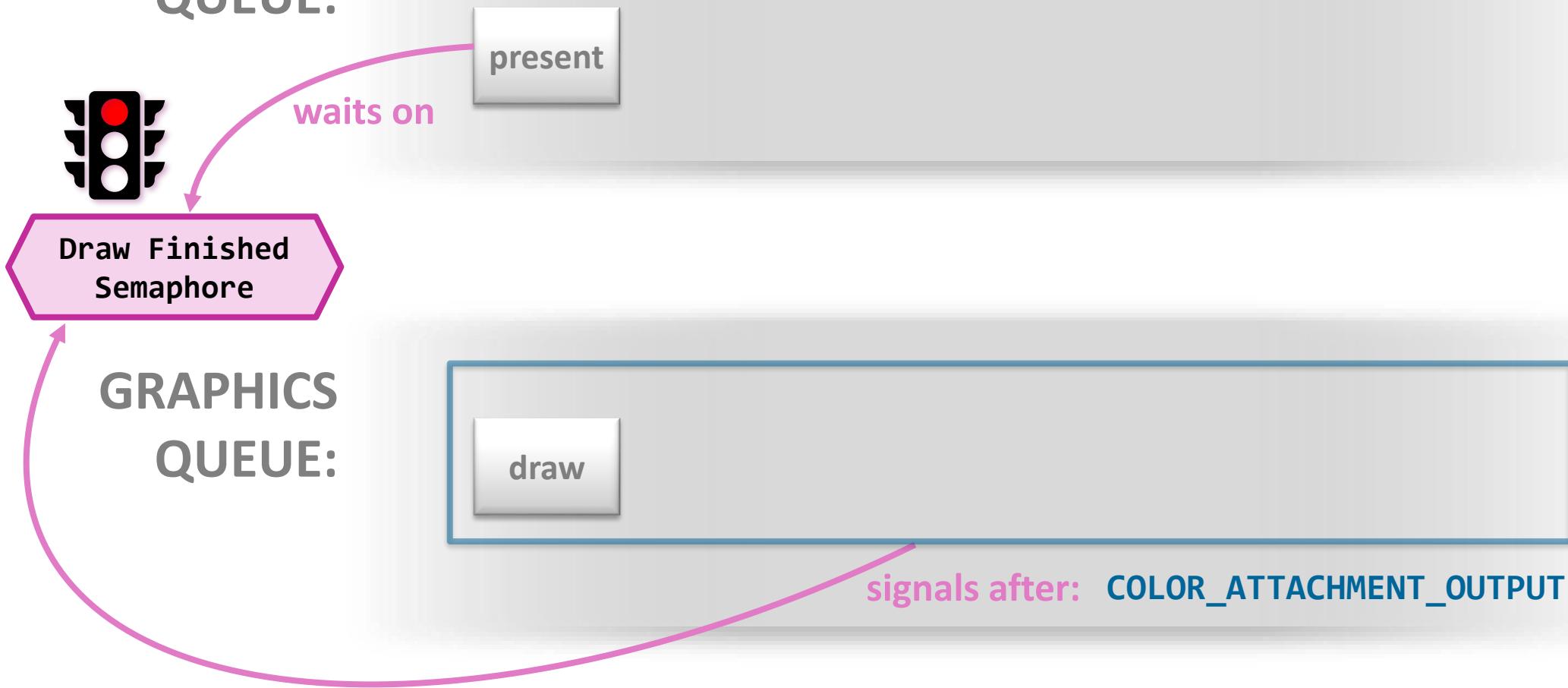
Binary Semaphores

PRESENTATION
QUEUE:



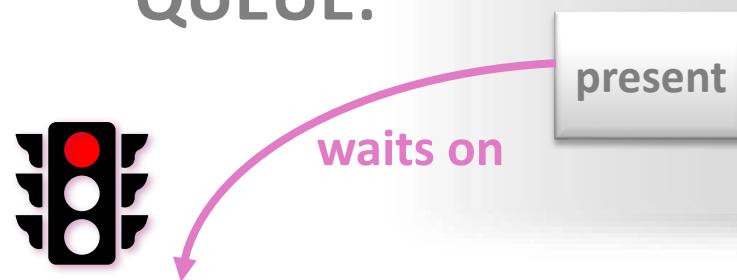
Binary Semaphores

PRESENTATION QUEUE:



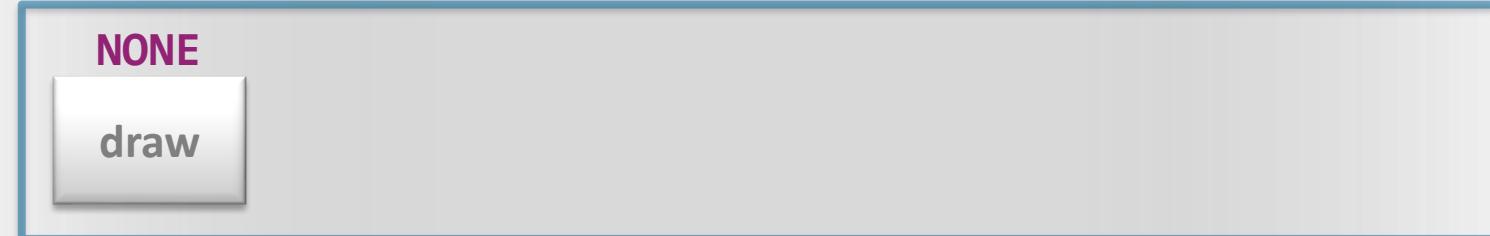
Binary Semaphores

PRESENTATION QUEUE:



Draw Finished
Semaphore

GRAPHICS QUEUE:

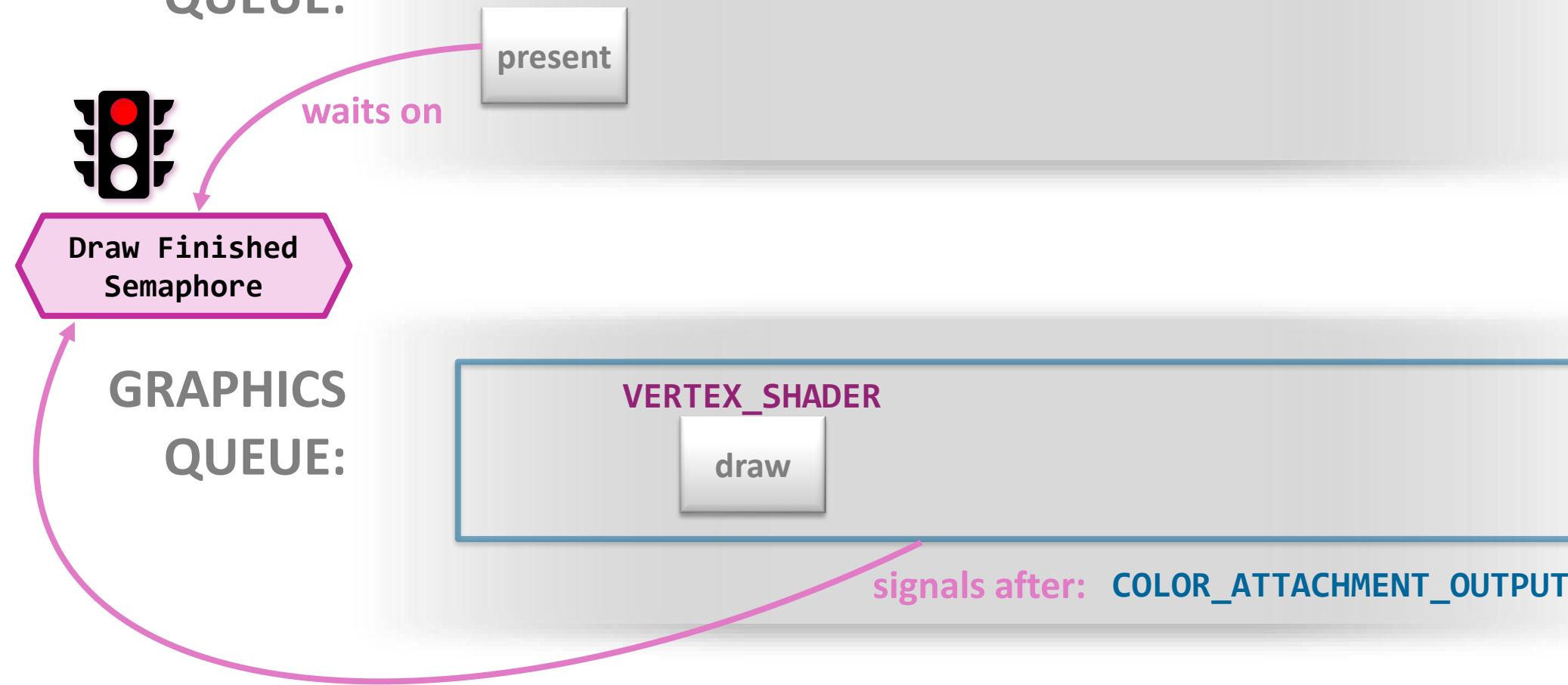


signals after: COLOR_ATTACHMENT_OUTPUT



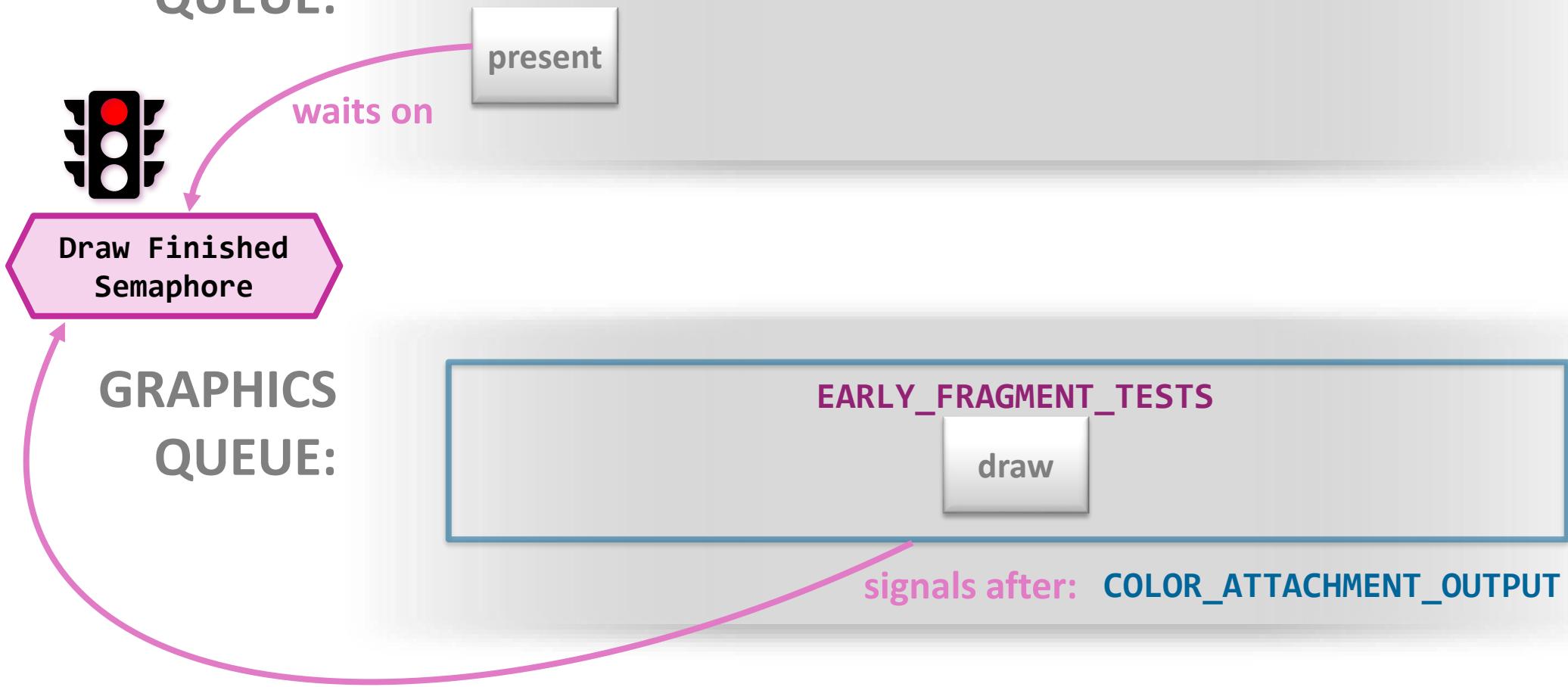
Binary Semaphores

PRESENTATION QUEUE:



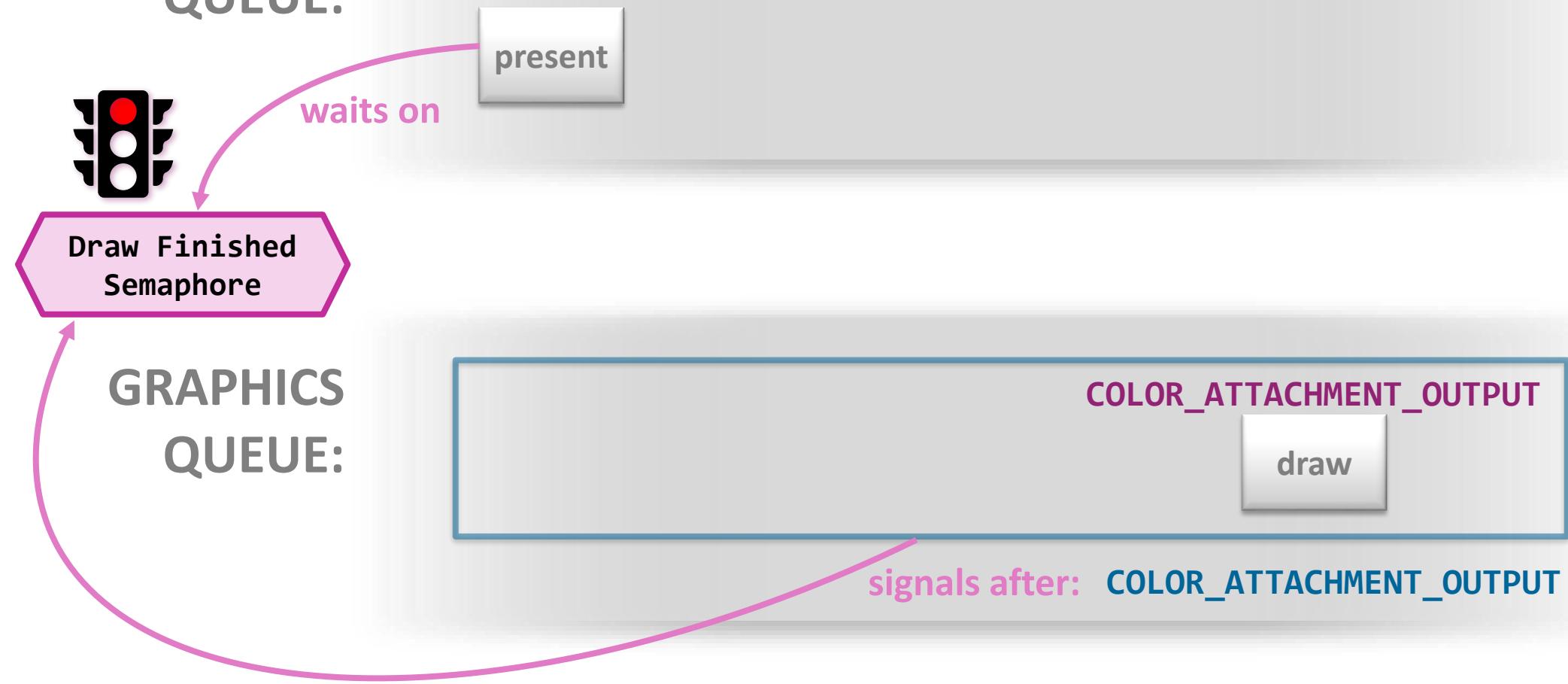
Binary Semaphores

PRESENTATION QUEUE:



Binary Semaphores

PRESENTATION QUEUE:



Binary Semaphores

PRESENTATION
QUEUE:



waits on

present

Draw Finished
Semaphore

GRAPHICS
QUEUE:

draw

signals after: COLOR_ATTACHMENT_OUTPUT



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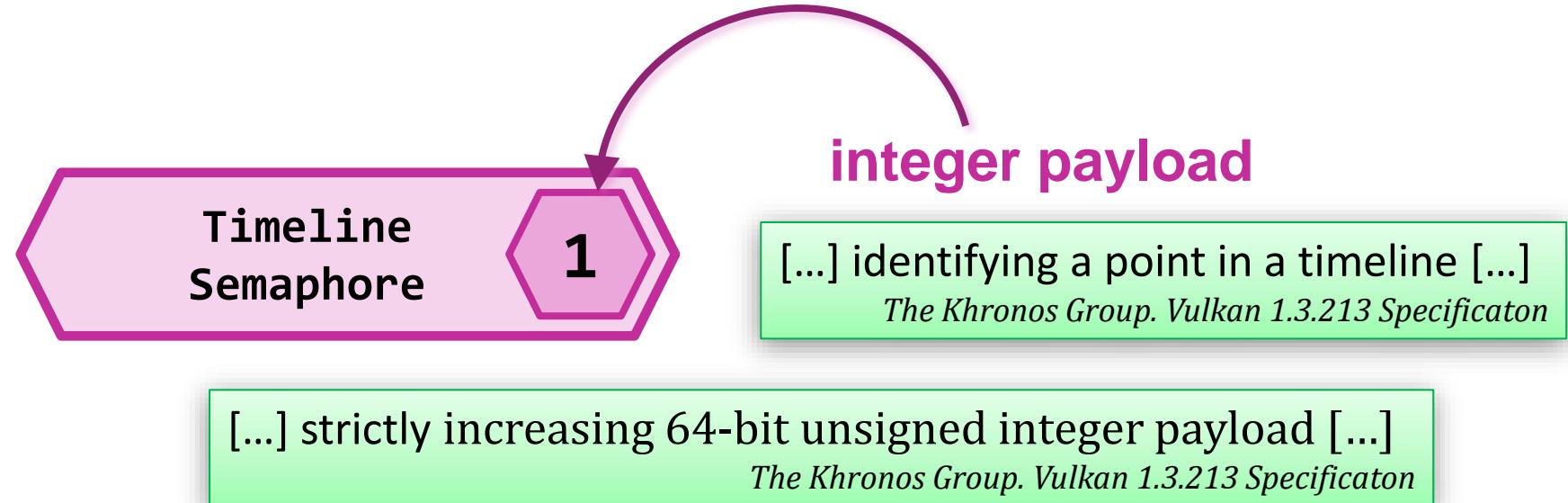


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Timeline Semaphores



Such timeline semaphores support the following operations:

- Host query - A host operation that allows querying the payload of the timeline semaphore.
- Host wait - A host operation that allows a blocking wait for a timeline semaphore to reach a specified value.
- Host signal - A host operation that allows advancing the timeline semaphore to a specified value.
- Device wait - A device operation that allows waiting for a timeline semaphore to reach a specified value.
- Device signal - A device operation that allows advancing the timeline semaphore to a specified value.

The Khronos Group. Vulkan 1.3.213 Specification



Troubles with Binary Semaphores

PHYSICS

GRAPHICS



Troubles with Binary Semaphores

PHYSICS

GRAPHICS



Troubles with Binary Semaphores

PHYSICS



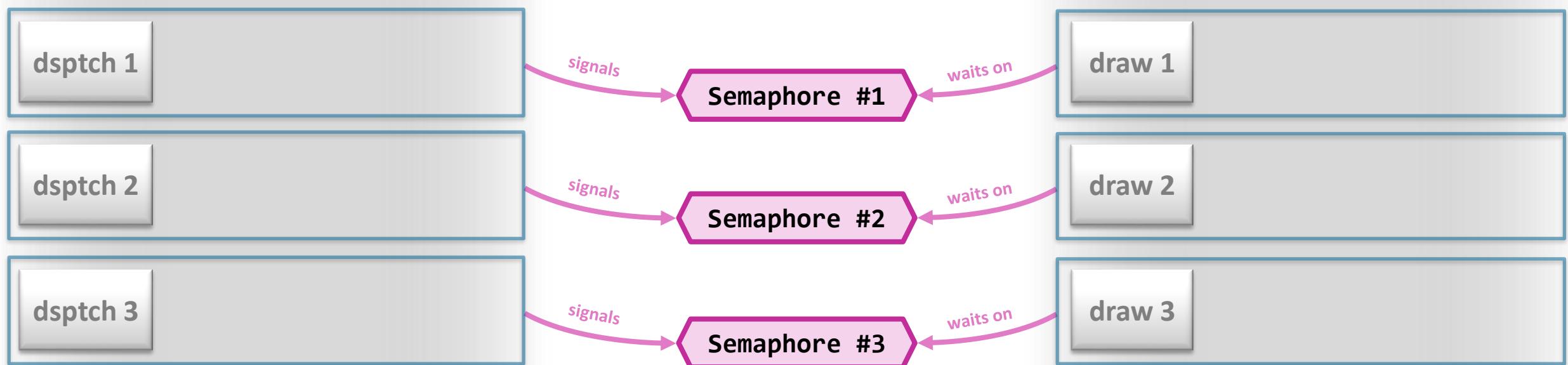
GRAPHICS



Troubles with Binary Semaphores

PHYSICS (60Hz)

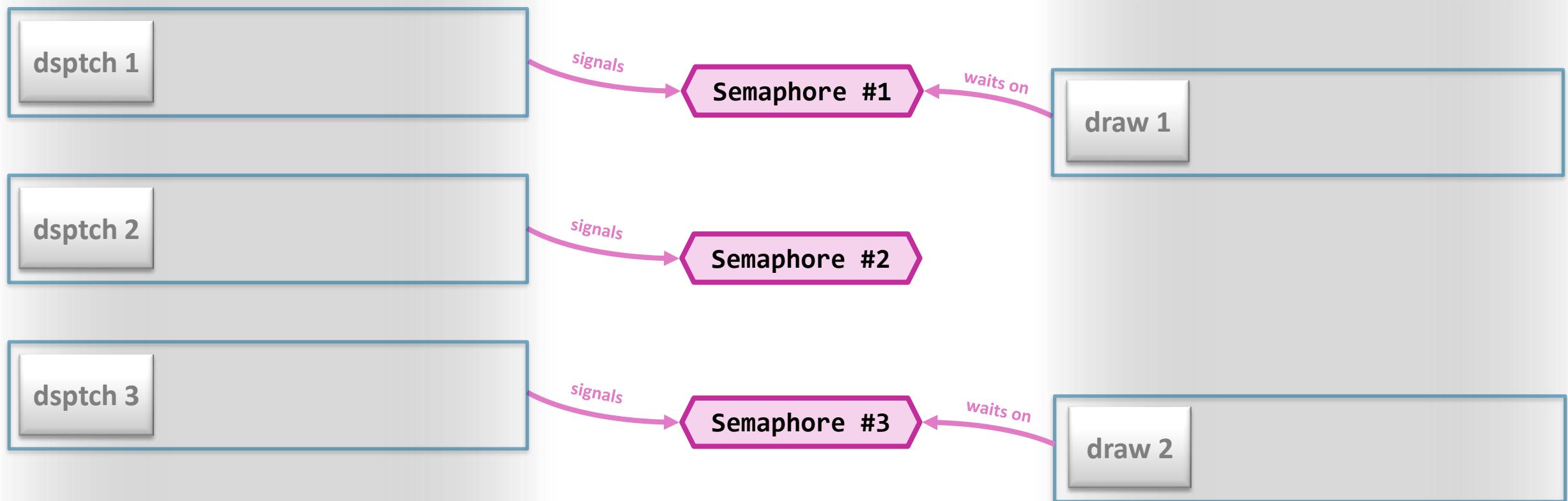
GRAPHICS (max.)



Troubles with Binary Semaphores

PHYSICS (60Hz)

GRAPHICS (max.)



Troubles with Binary Semaphores

PHYSICS (60Hz)

dsptch 1

signals

Semaphore #1

waits on

draw 1

Unlike timeline semaphores, fences or events, the act of waiting for a binary semaphore also unsignals that semaphore. Applications **must** ensure that between two such wait operations, the semaphore is signaled again, with execution dependencies used to ensure these occur in order. Binary semaphore waits and signals should thus occur in discrete 1:1 pairs.

The Khronos Group. Vulkan 1.3.213 Specification

GRAPHICS (max.)

#2

waits on

draw 2

#3

I like to wait on the next

draw 3

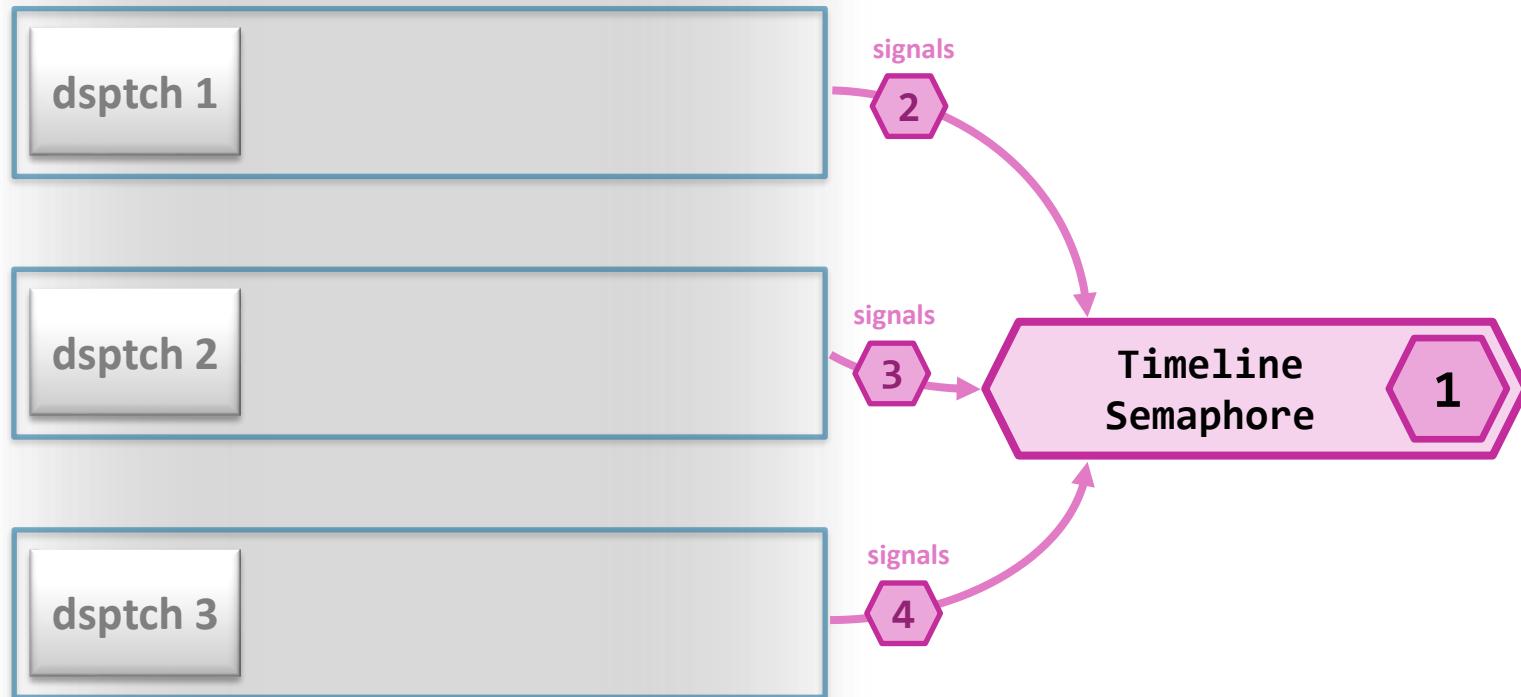
draw 4

draw 5

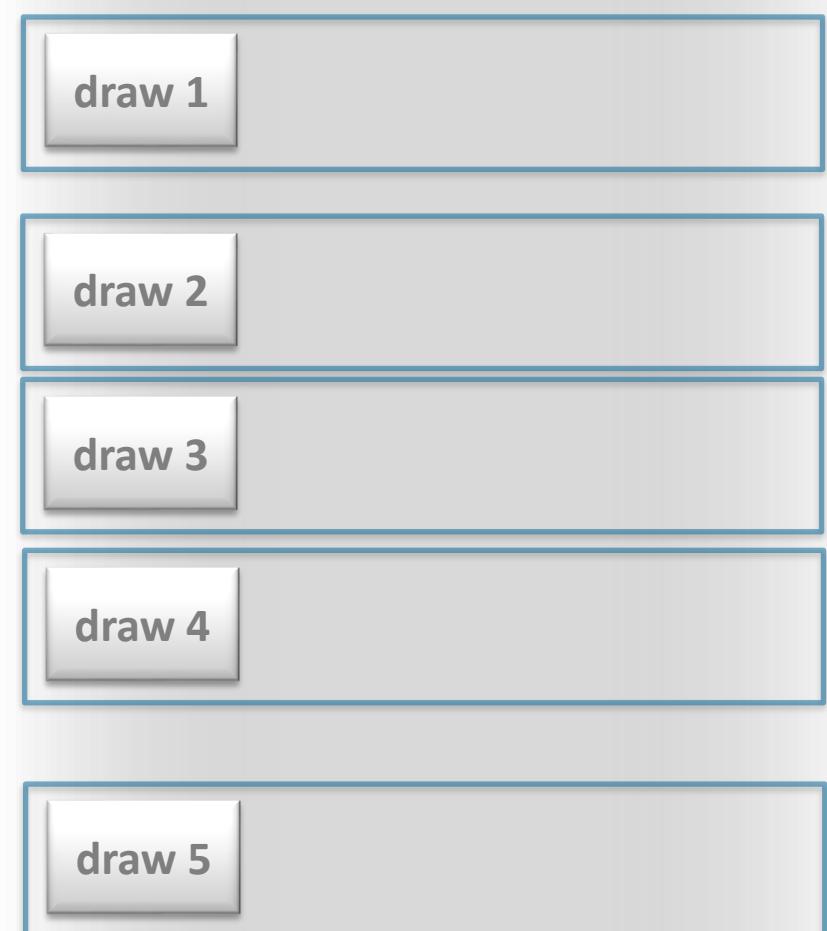


Timeline Semaphores to the Rescue

PHYSICS (60Hz)

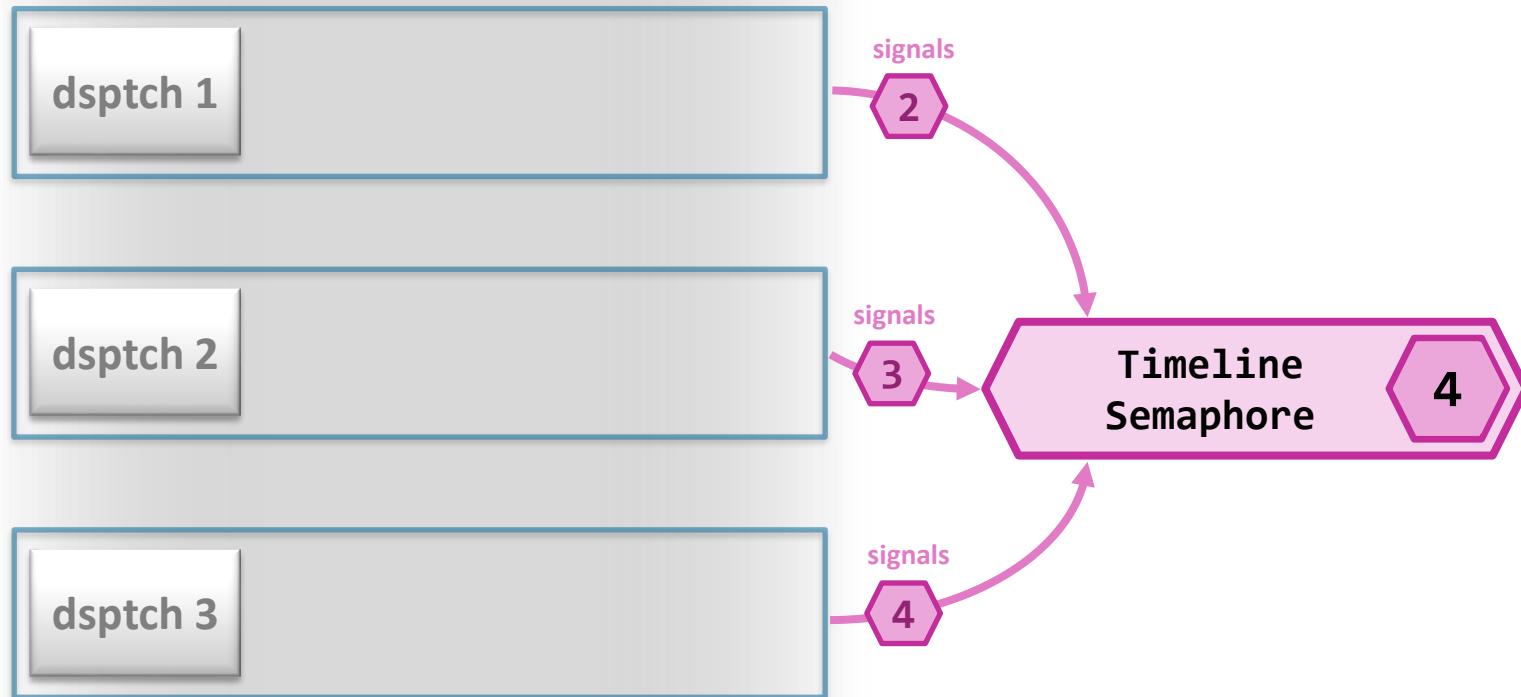


GRAPHICS (max.)

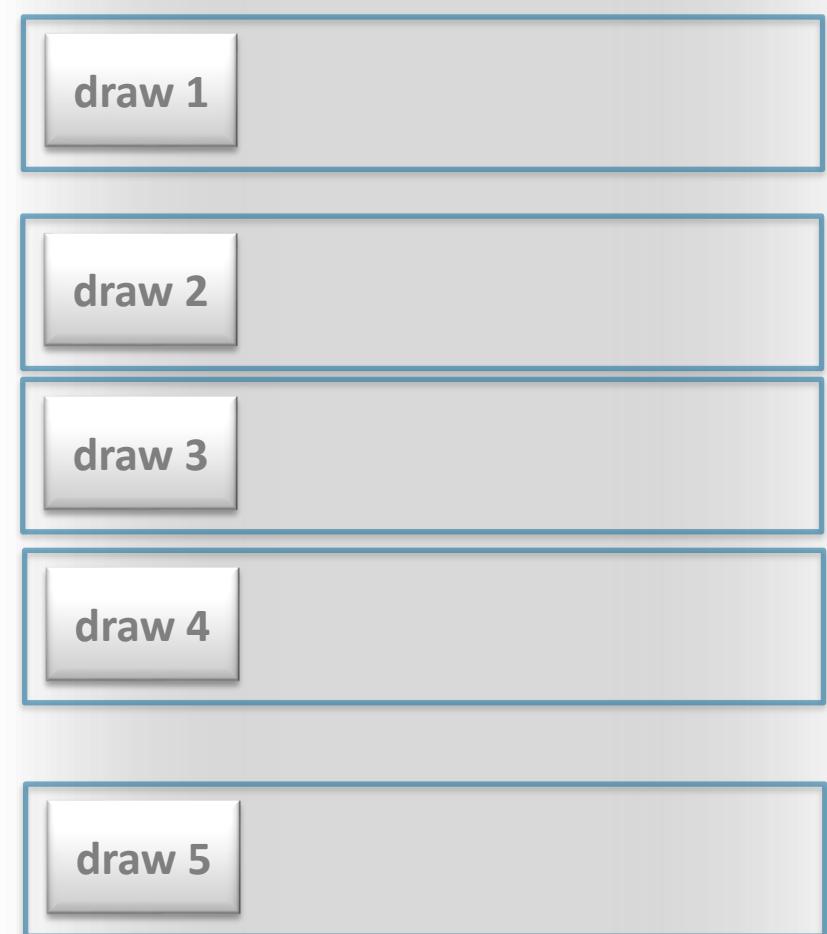


Timeline Semaphores to the Rescue

PHYSICS (60Hz)



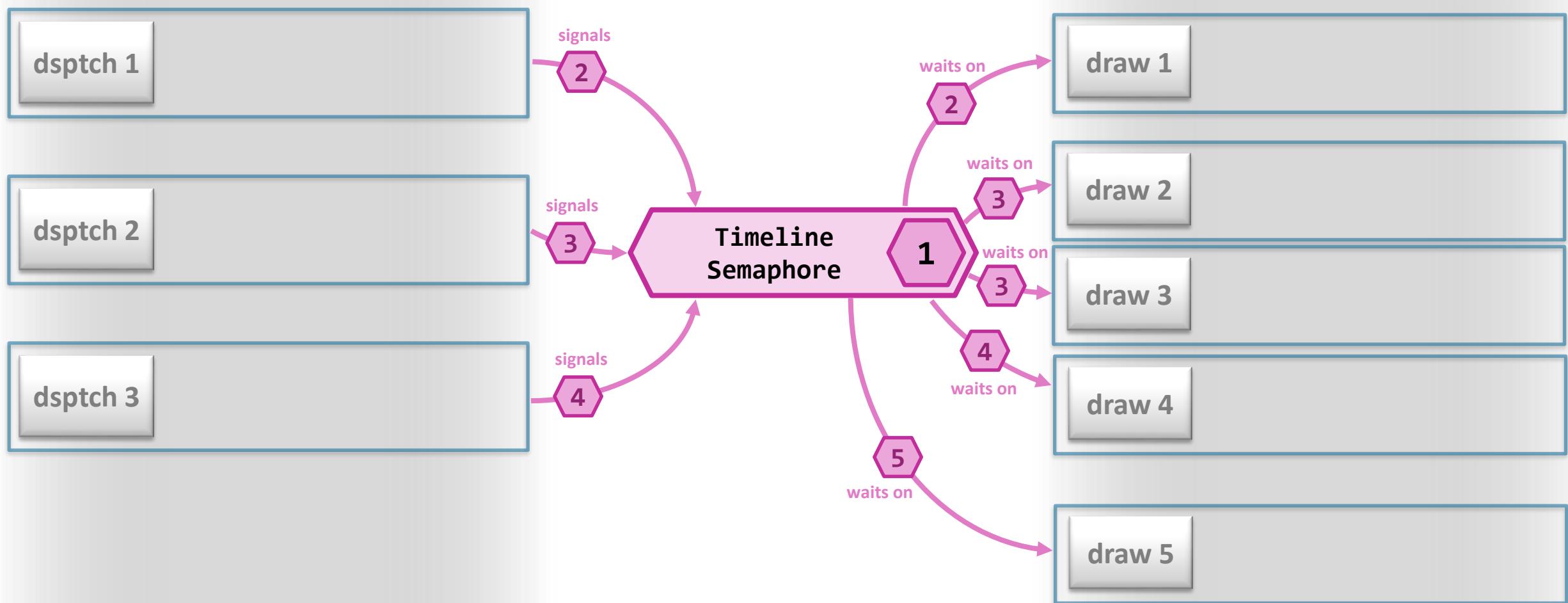
GRAPHICS (max.)



Timeline Semaphores to the Rescue

PHYSICS (60Hz)

GRAPHICS (max.)



Timeline Semaphores to the Rescue

PHYSICS (60Hz)

dsptch 1

signals
2

signals

When using timeline semaphores, wait-before-signal behavior is well-defined and applications **can** submit work via `vkQueueSubmit` defining a timeline semaphore wait operation before submitting a corresponding semaphore signal operation.

The Khronos Group. Vulkan 1.3.213 Specification

GRAPHICS (max.)

draw 1

waits on
2

draw 2

waits on
3

draw 3

waits on
3

draw 4

waits on
4

draw 5

waits on
5Timeline
Semaphore

Timeline Semaphores



- Same type as binary semaphores: `VkSemaphore`
 - But created with `VK_SEMAPHORE_TYPE_TIMELINE`
 - (in contrast to `VK_SEMAPHORE_TYPE_BINARY` for binary semaphores)
- Payload = strictly increasing 64-bit unsigned integer
- Host query: `vkGetSemaphoreCounterValue`
- Host wait: `vkWaitSemaphores`
- Host signal: `vkSignalSemaphore`



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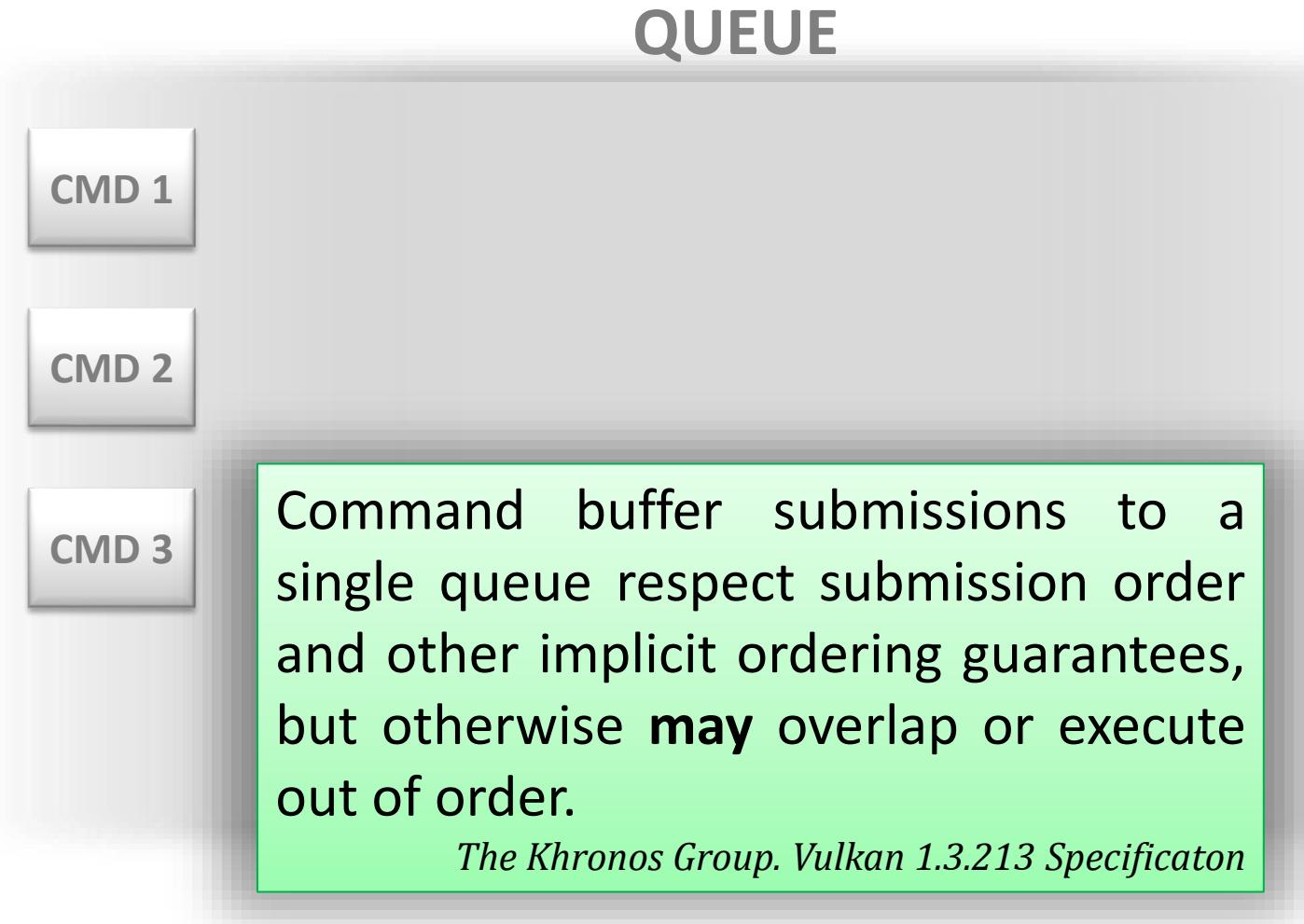
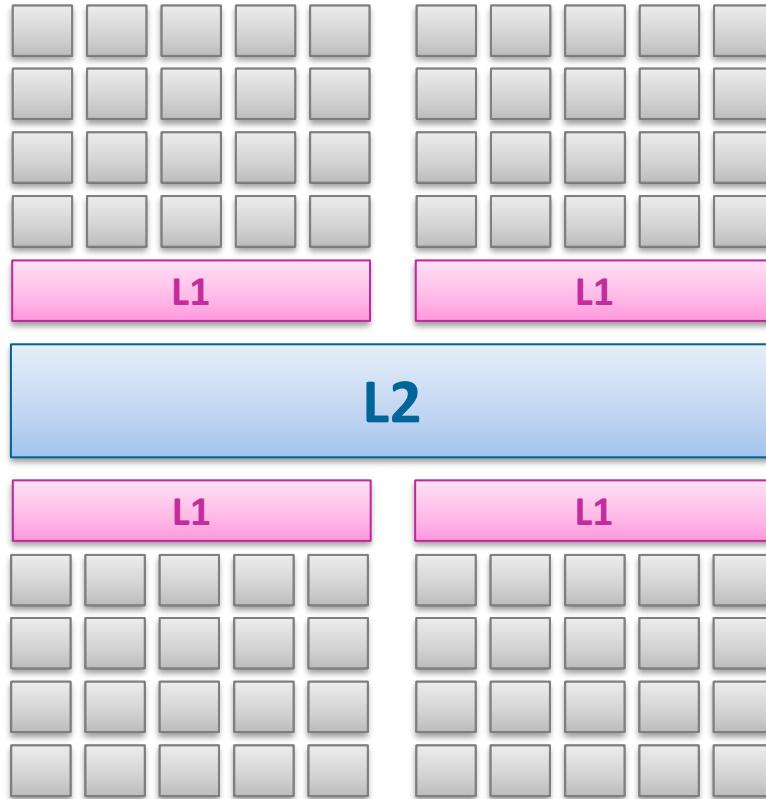


Overview of Synchronization Methods

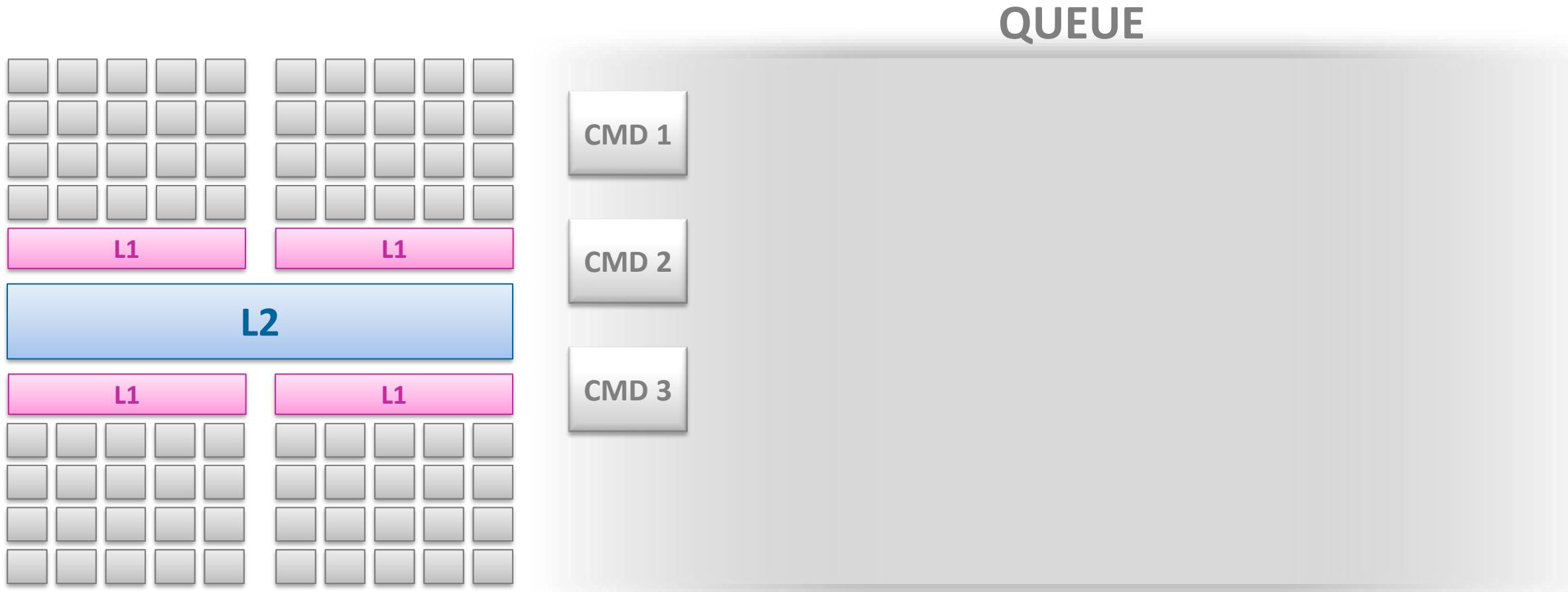
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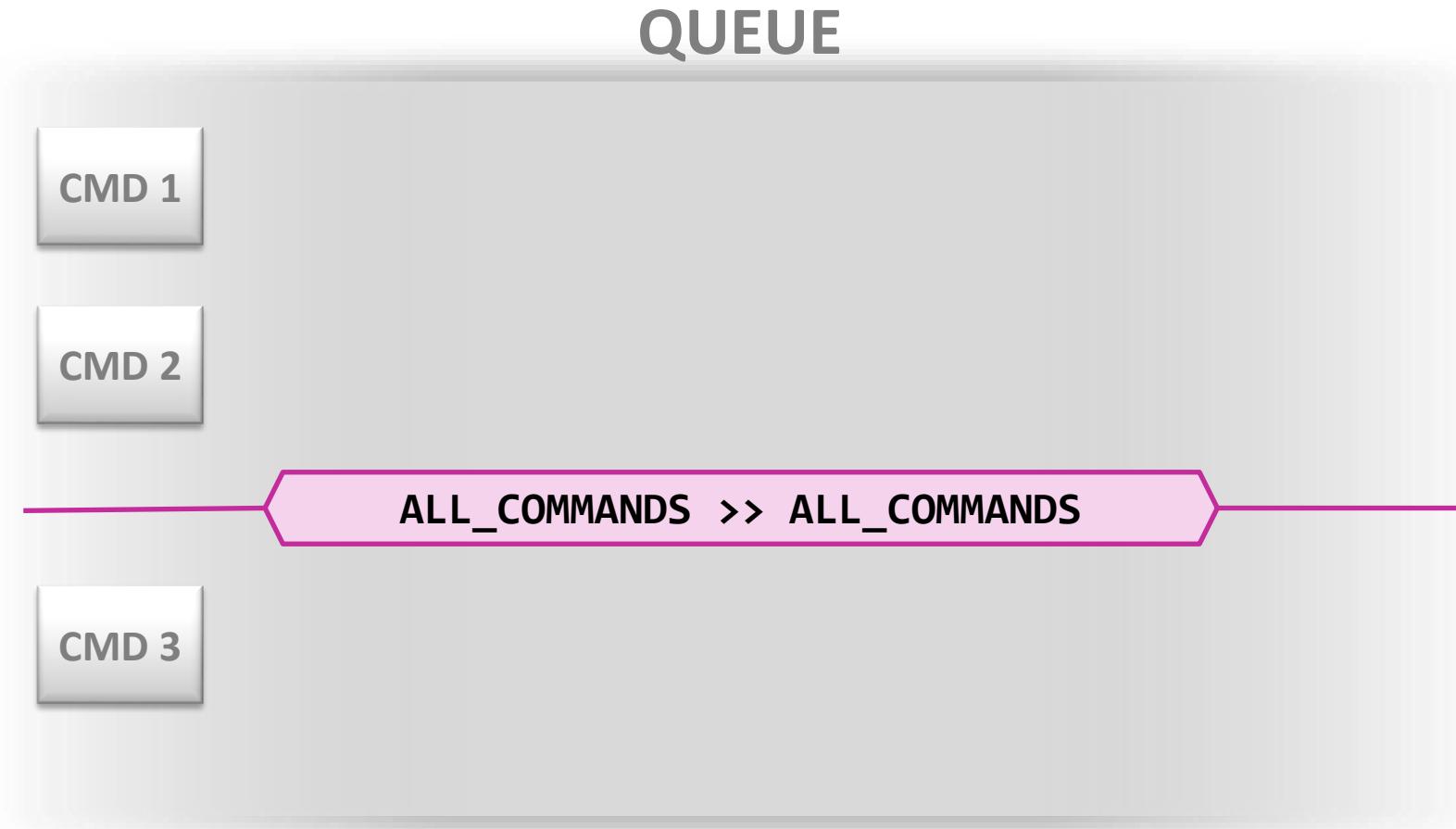
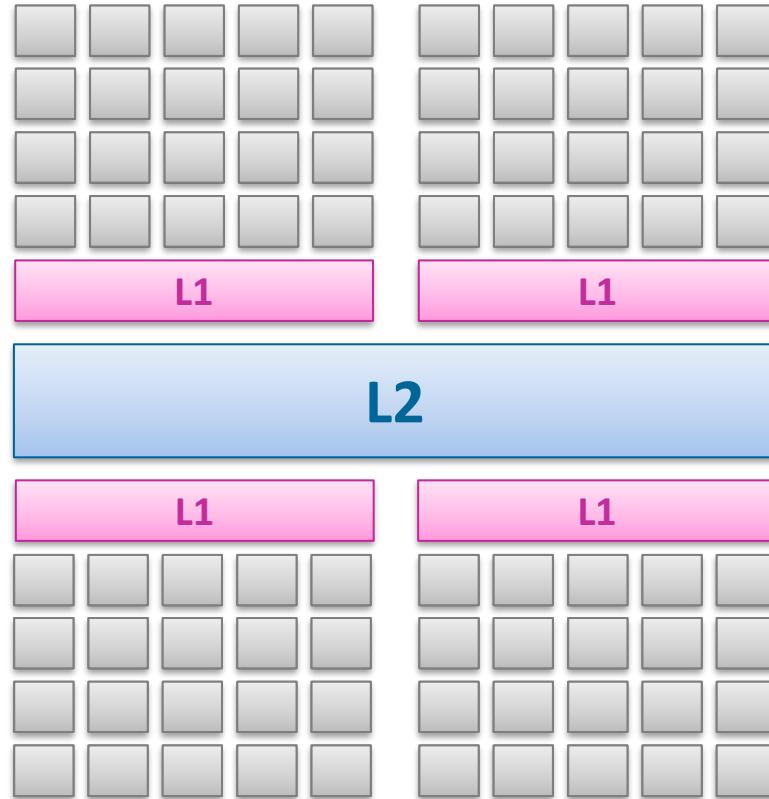
Pipeline Execution Barriers



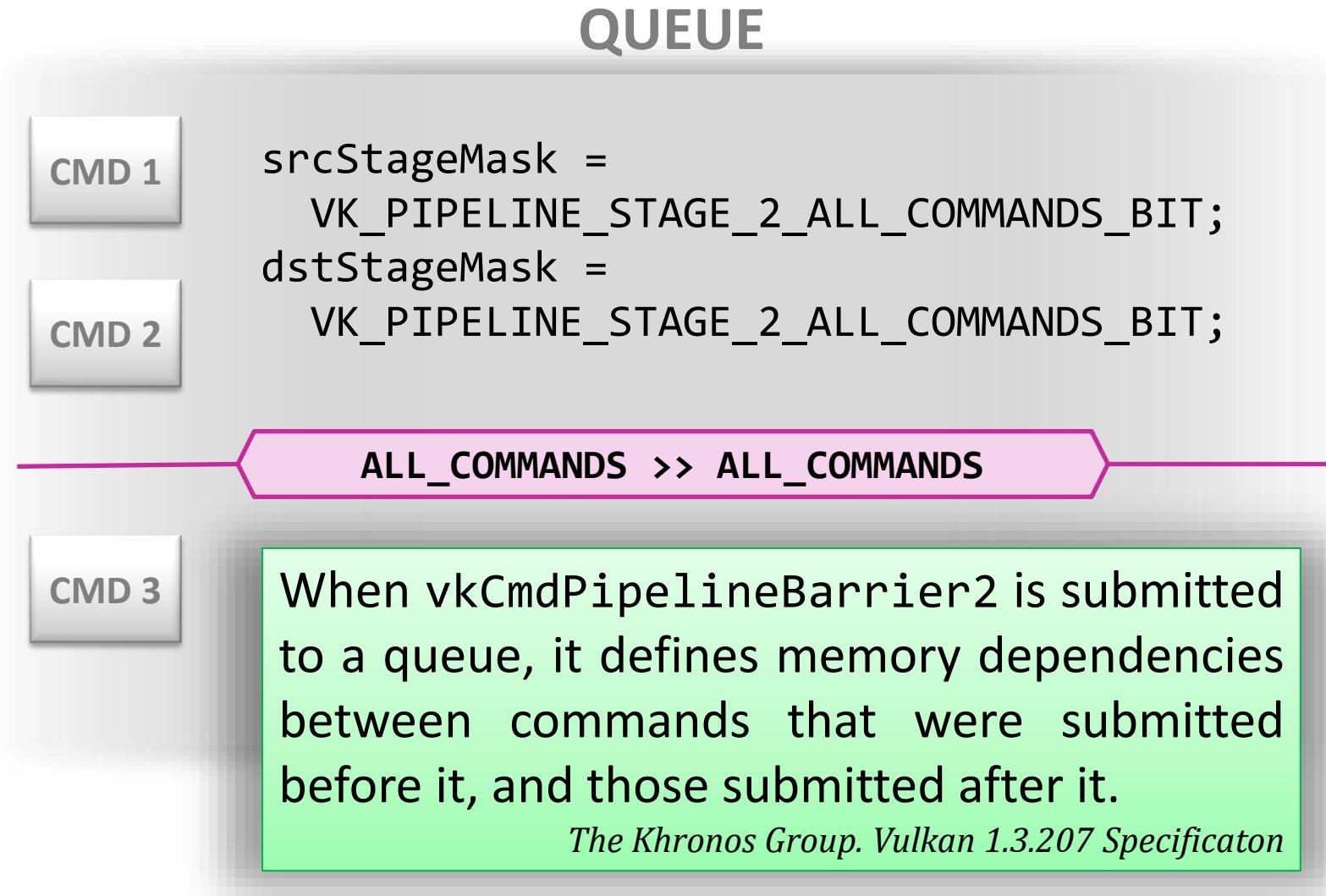
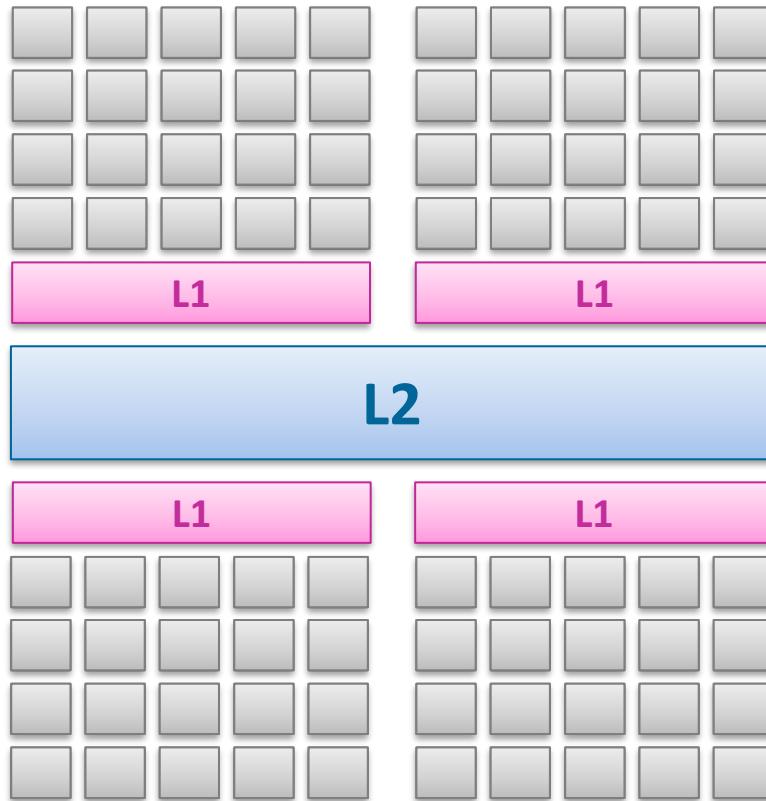
Pipeline Execution Barriers



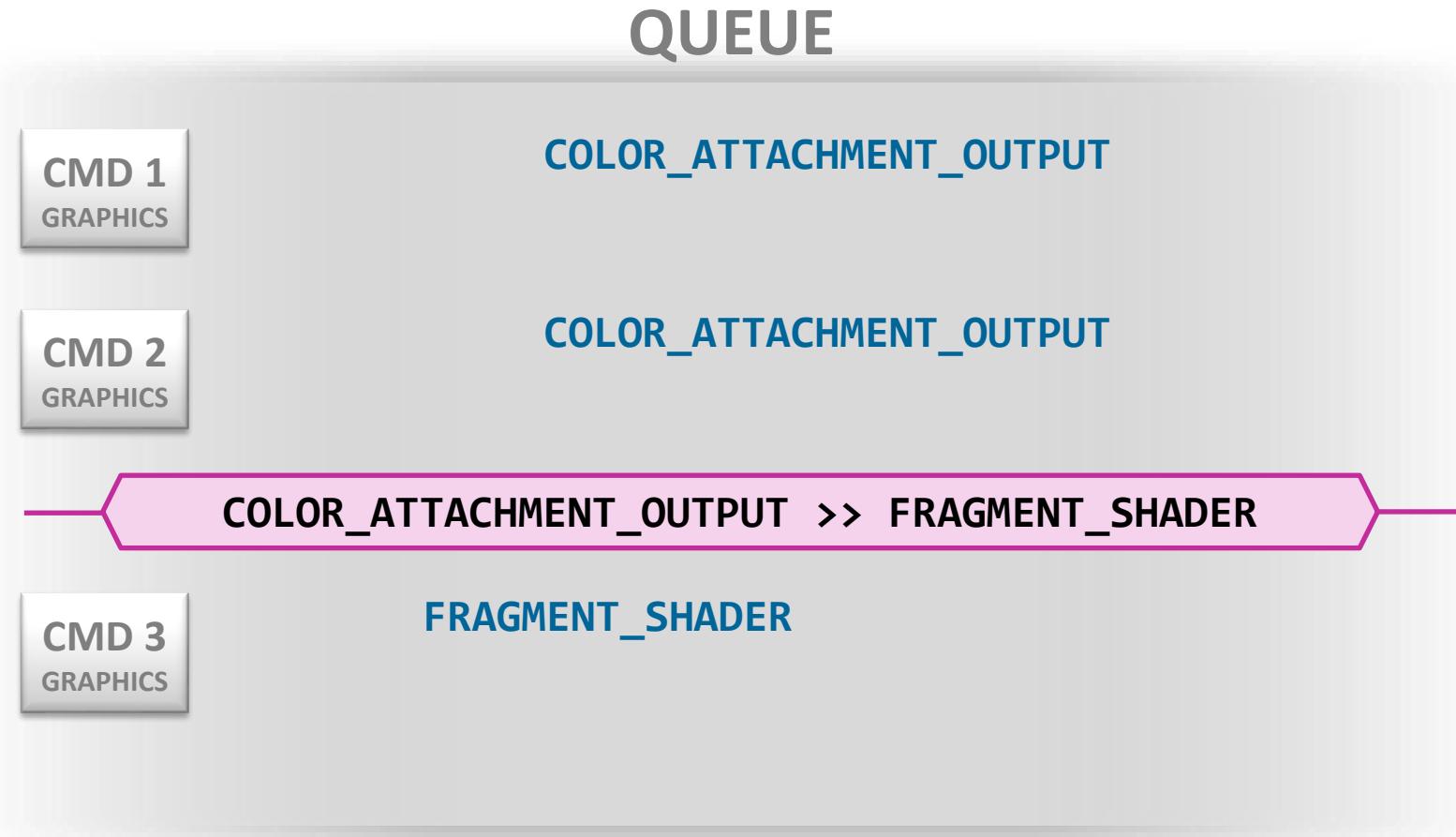
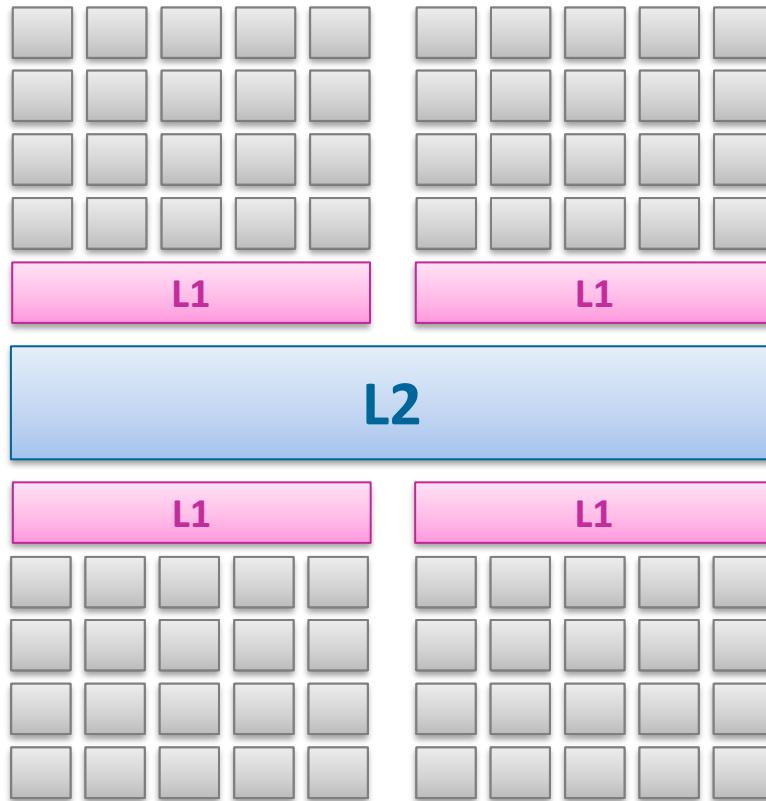
Pipeline Execution Barriers



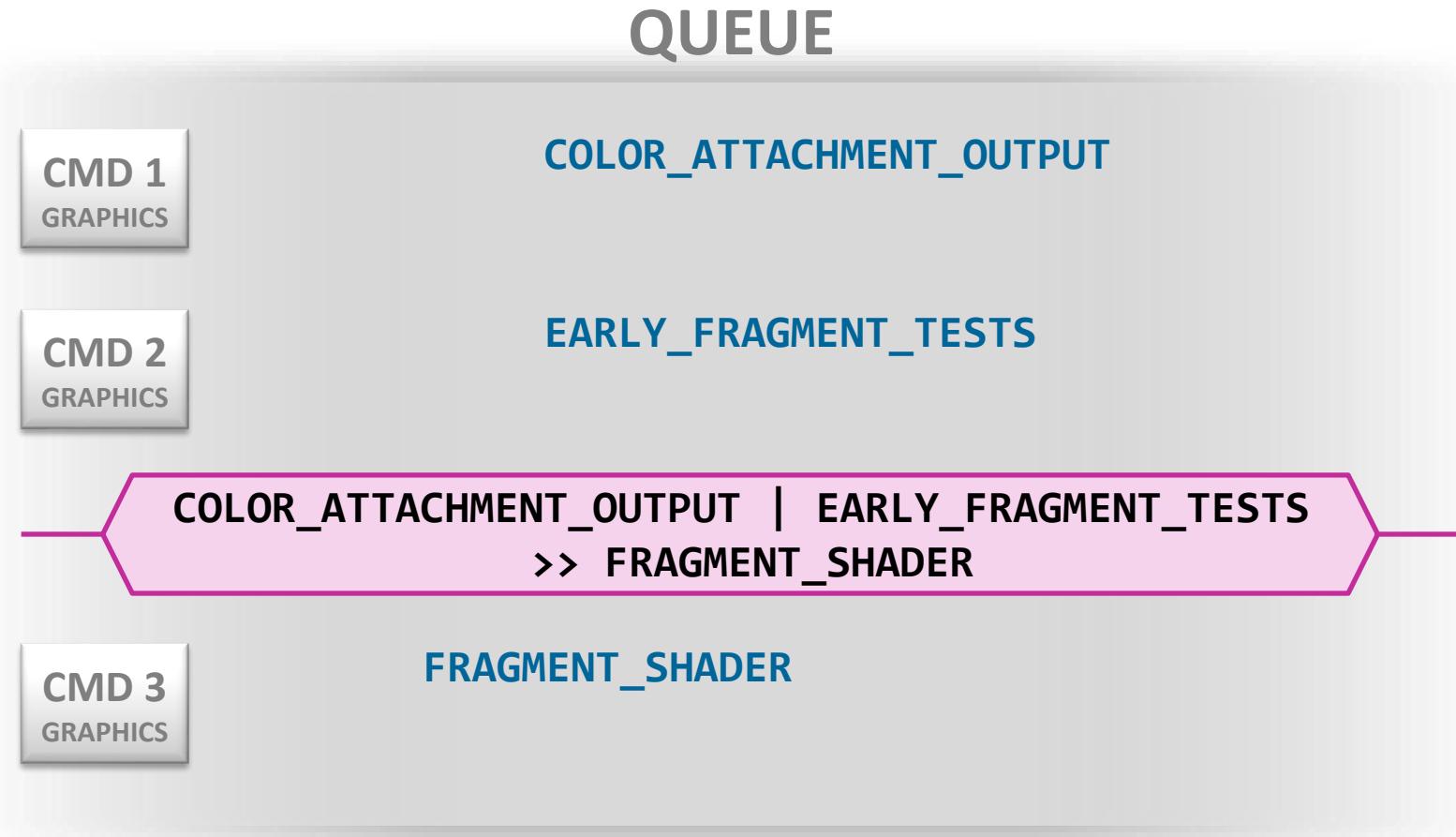
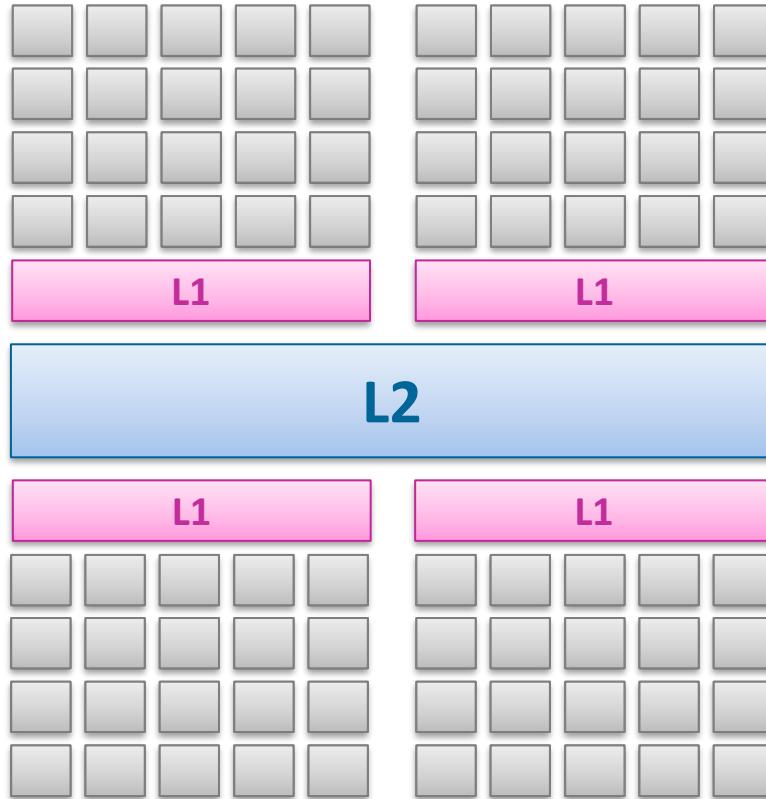
Pipeline Execution Barriers



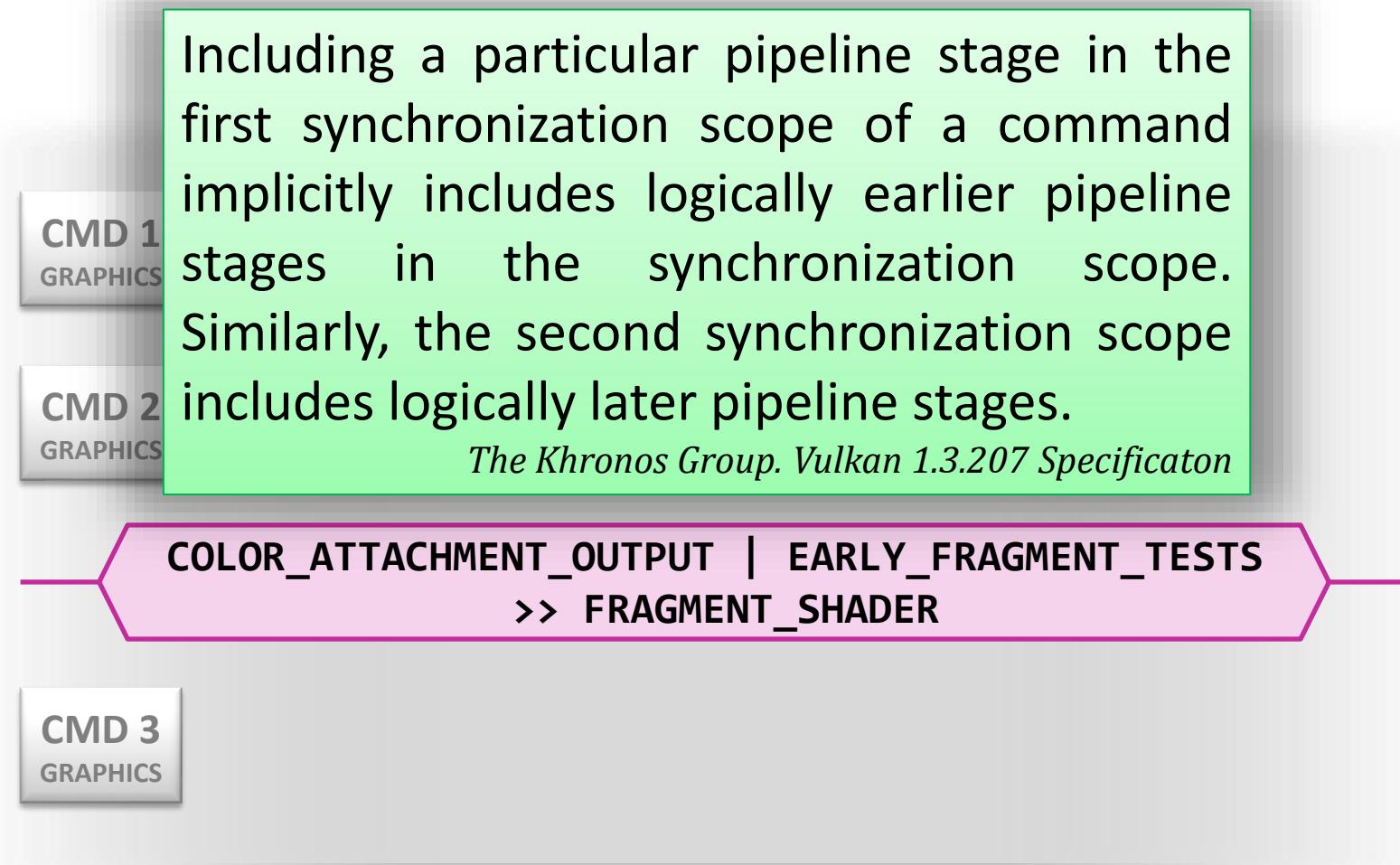
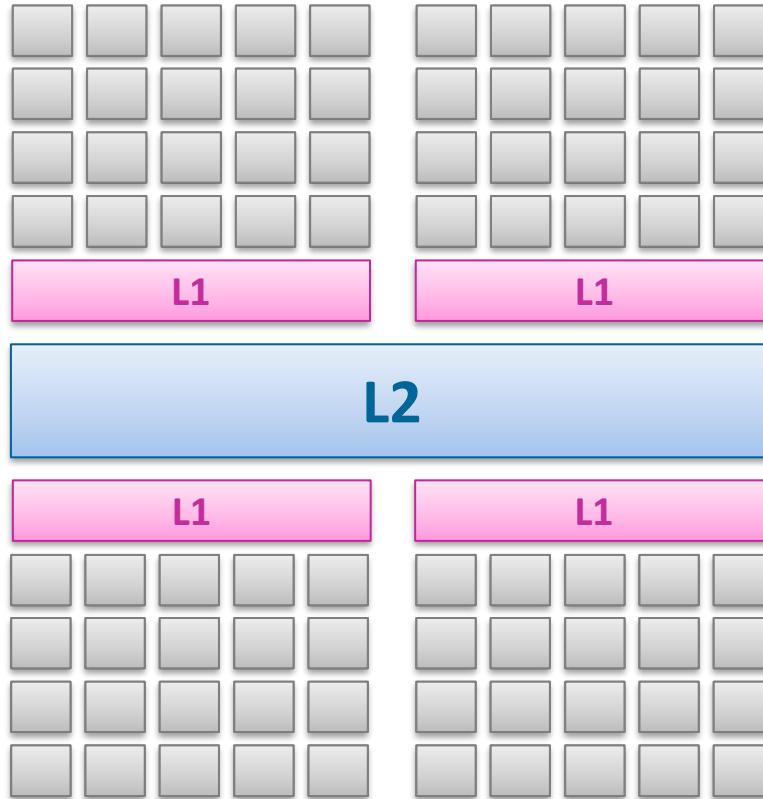
Pipeline Execution Barriers



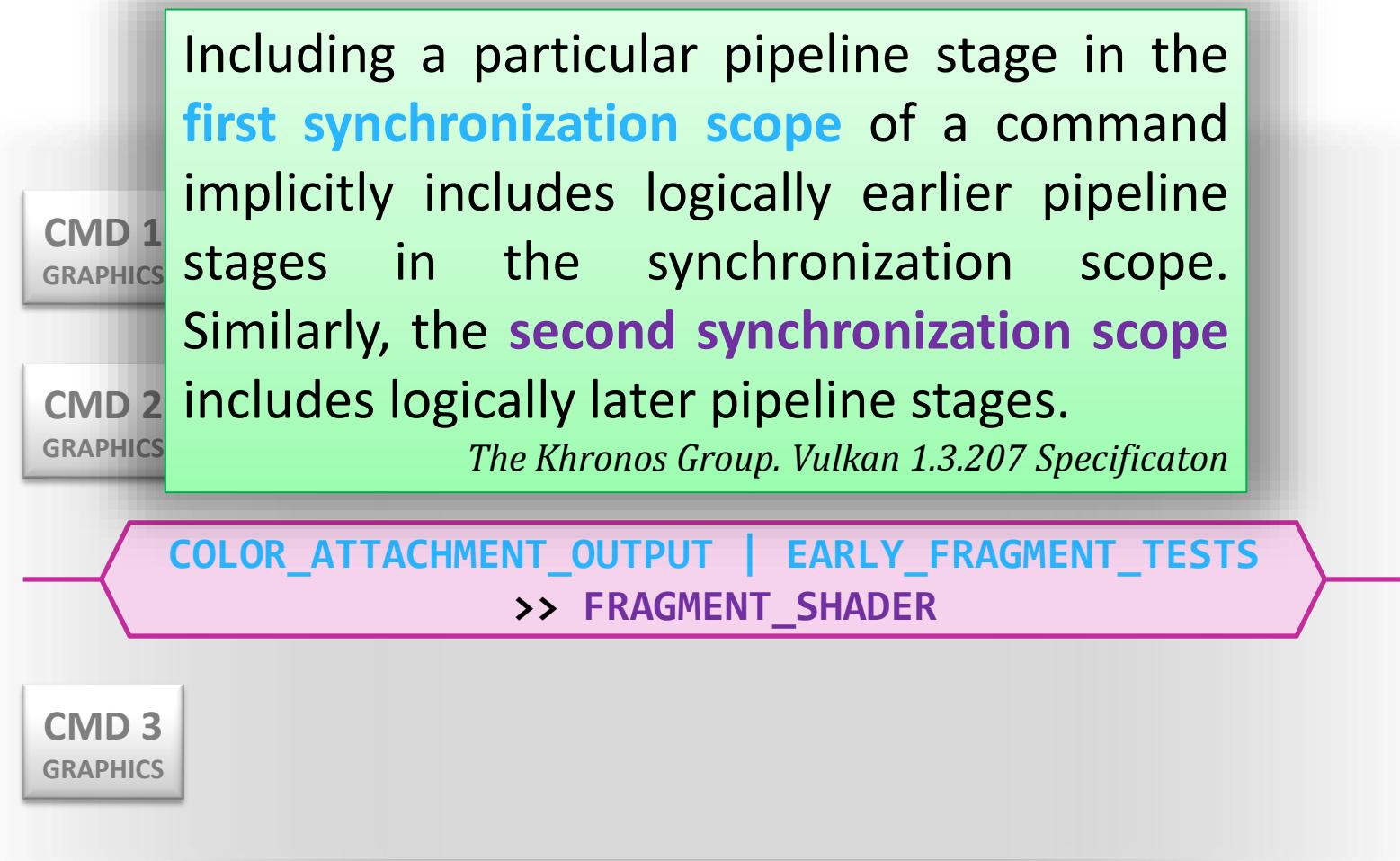
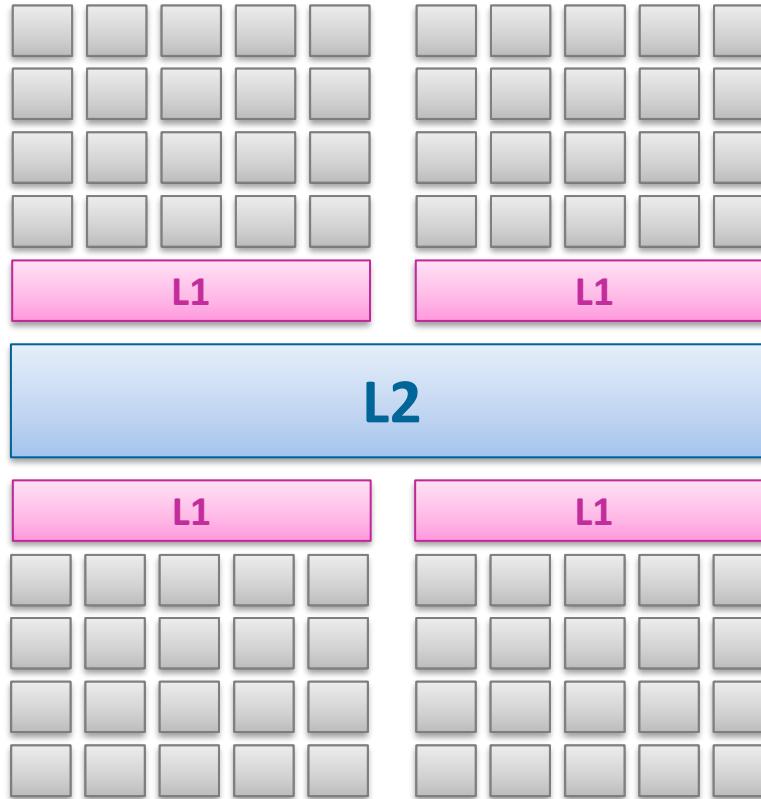
Pipeline Execution Barriers



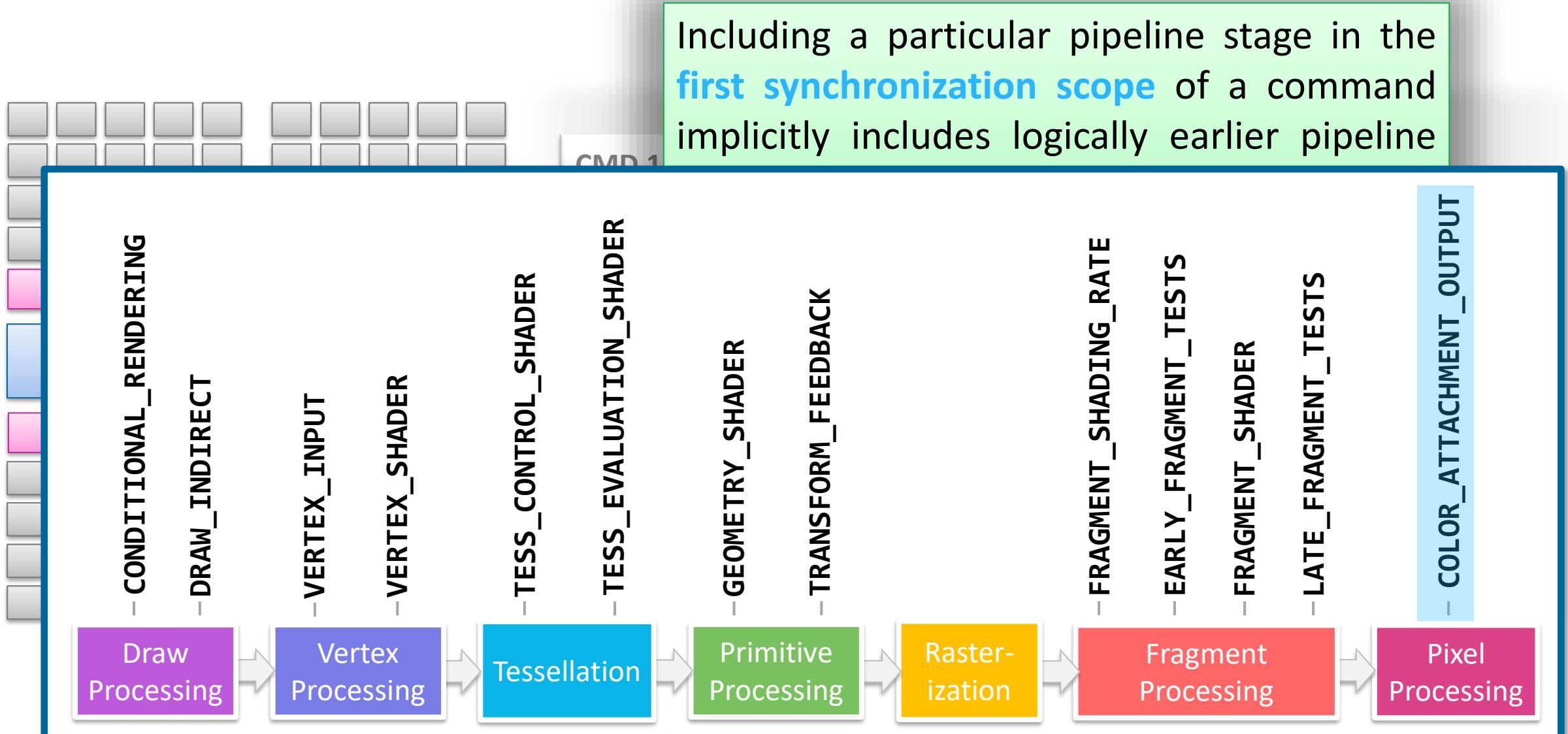
Pipeline Execution Barriers



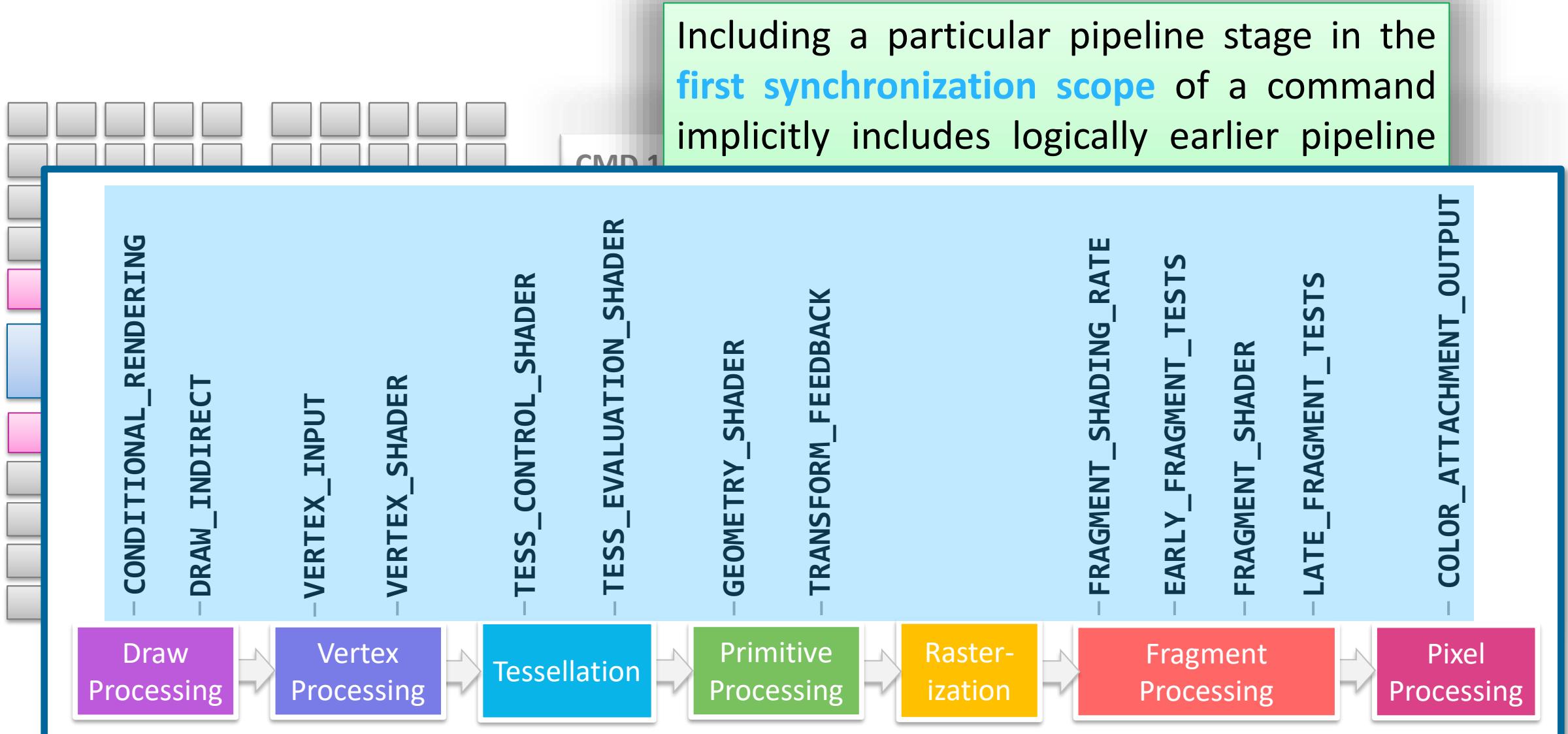
Pipeline Execution Barriers



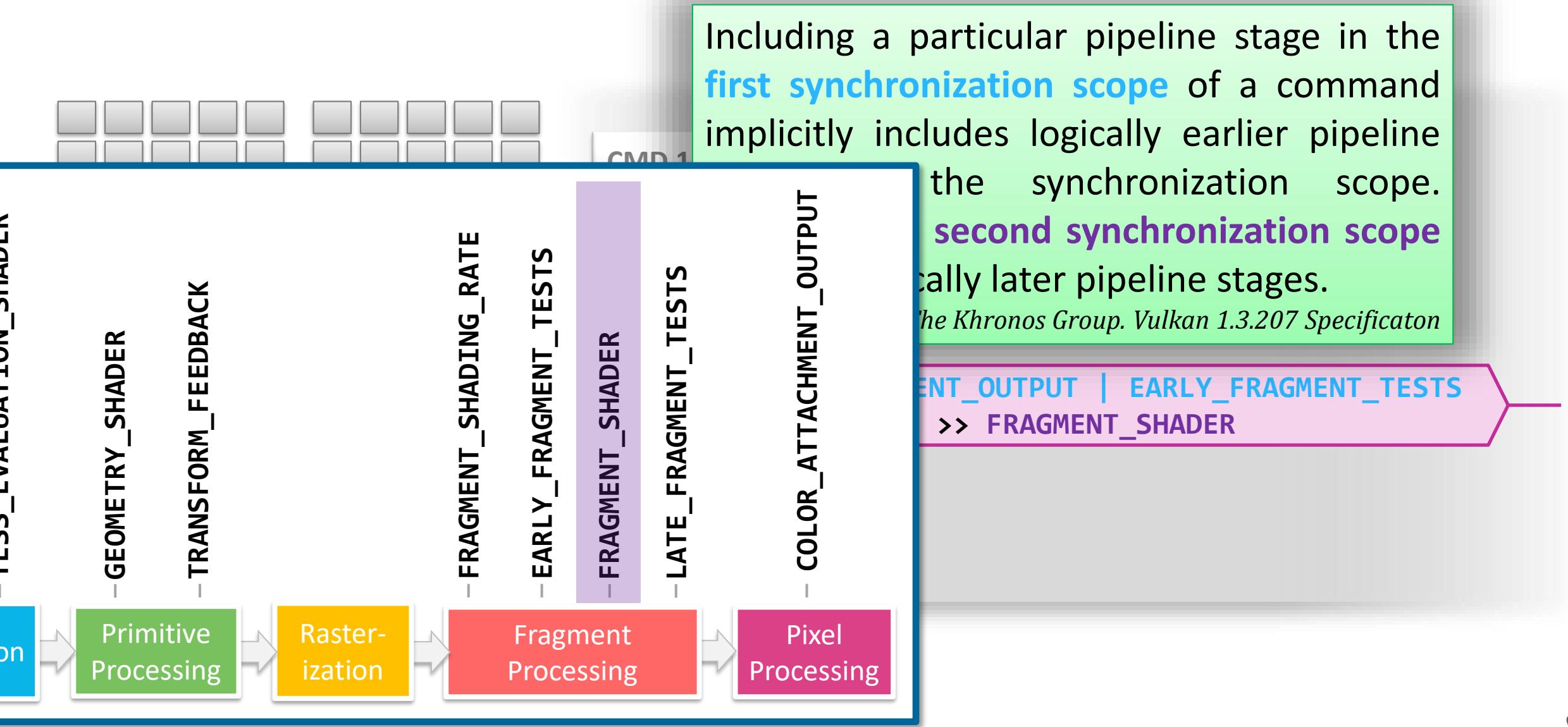
Pipeline Execution Barriers



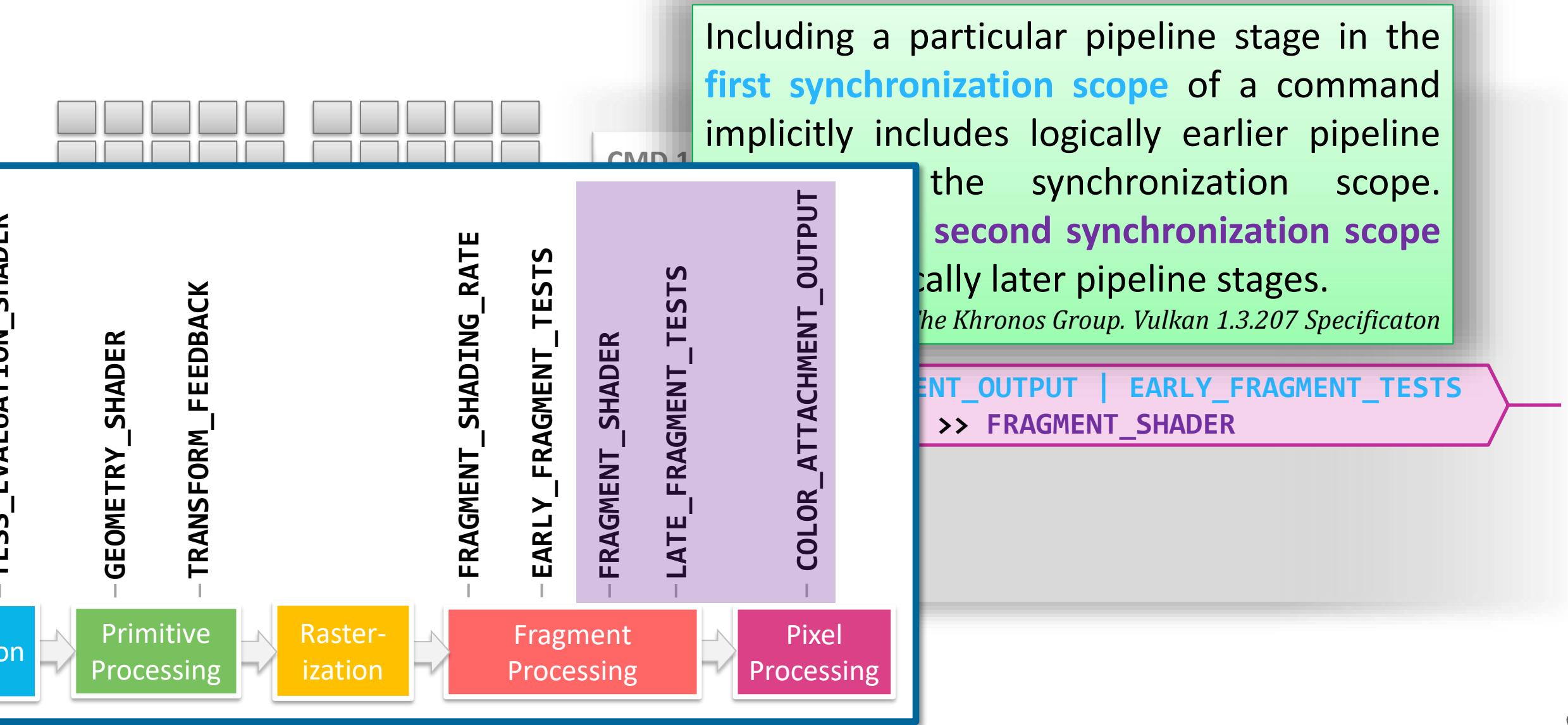
Pipeline Execution Barriers



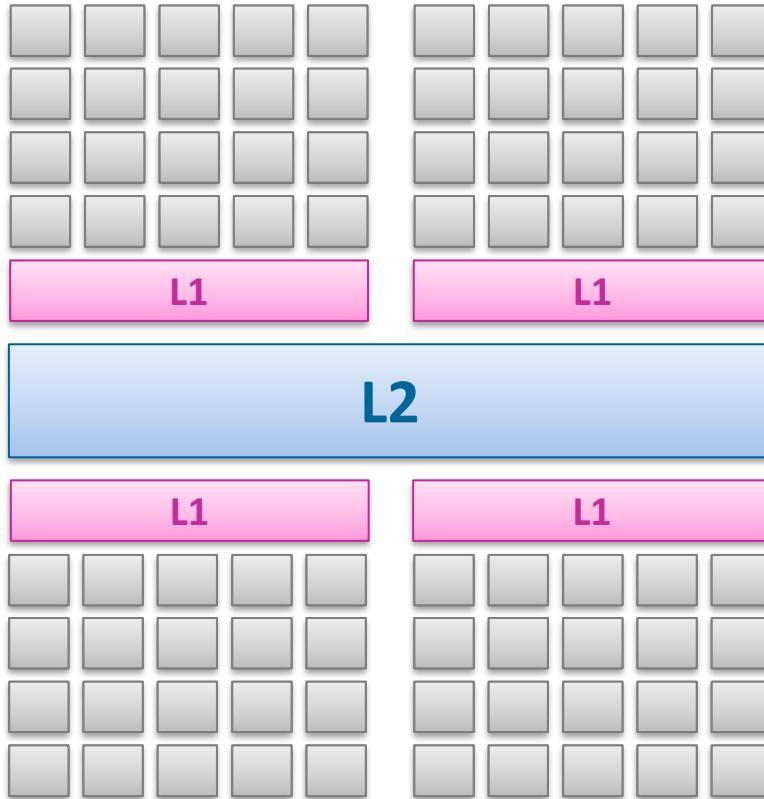
Pipeline Execution Barriers



Pipeline Execution Barriers



Pipeline Execution Barriers



Including a particular pipeline stage in the **first synchronization scope** of a command implicitly includes logically earlier pipeline stages in the synchronization scope. Similarly, the **second synchronization scope** includes logically later pipeline stages.

The Khronos Group. Vulkan 1.3.207 Specification

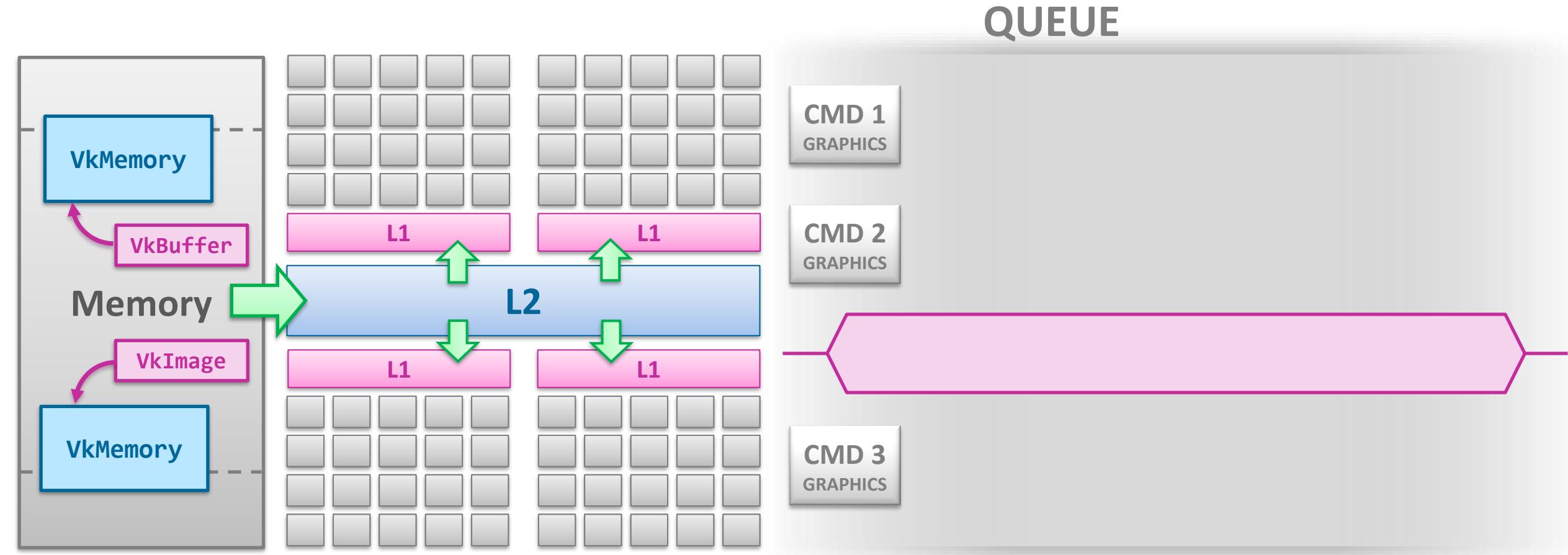
COLOR_ATTACHMENT_OUTPUT | EARLY_FRAGMENT_TESTS
>> FRAGMENT_SHADER

However, note that access scopes are not affected in this way - only the **precise stages specified** are considered **part of each access scope**.

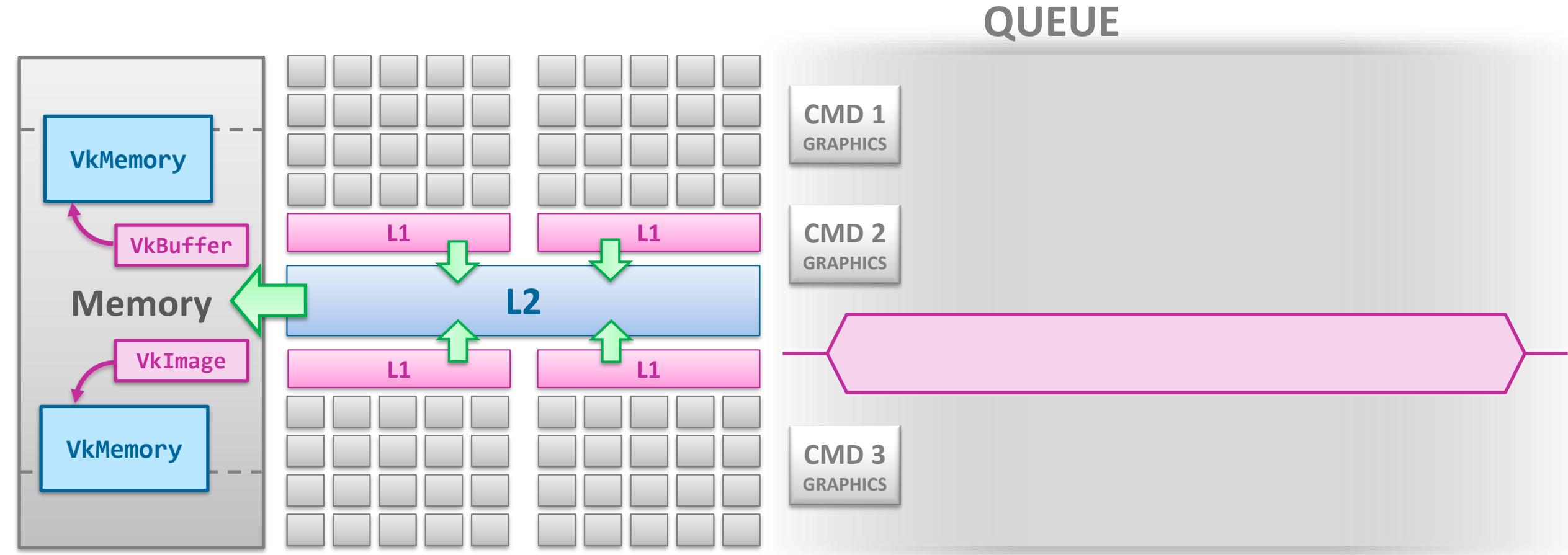
The Khronos Group. Vulkan 1.3.207 Specification



Pipeline Execution Barriers



Pipeline Execution Barriers



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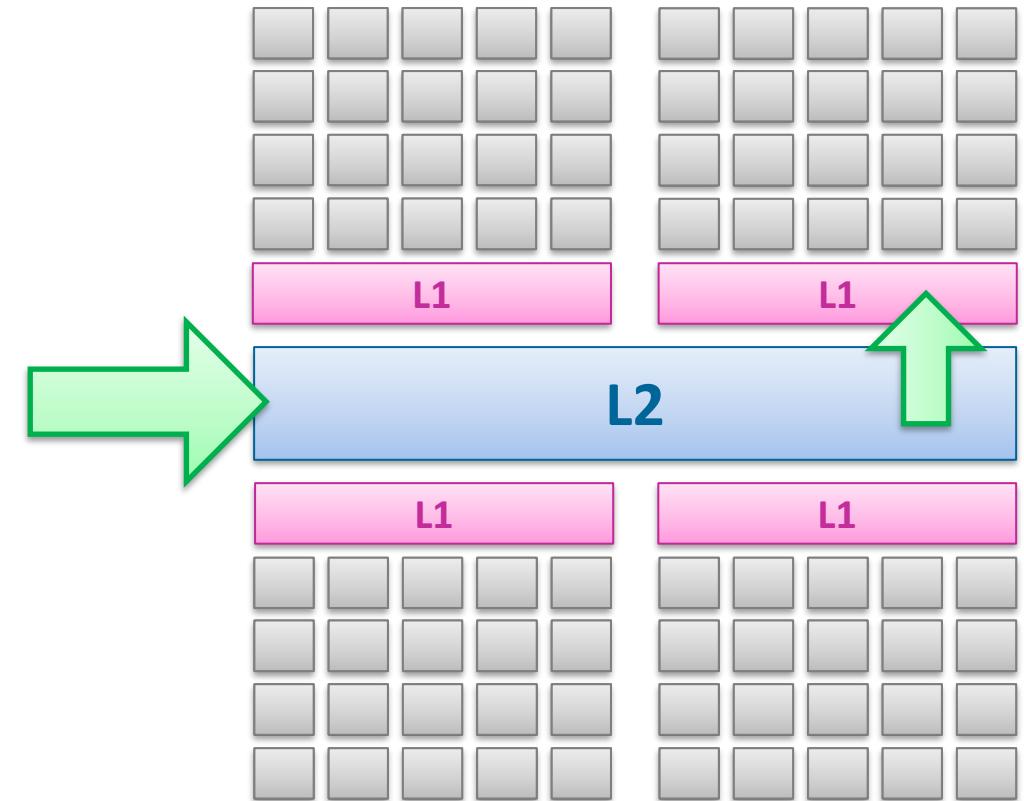
Memory Availability and Visibility

e.g.:

Stage: VK_PIPELINE_STAGE_2_FRAGMENT_SHADER_BIT

Access Mask: VK_ACCESS_2_SHADER_READ_BIT

“**visibility**” always refers to a combination of **pipeline stage + access mask** which memory is *visible to*

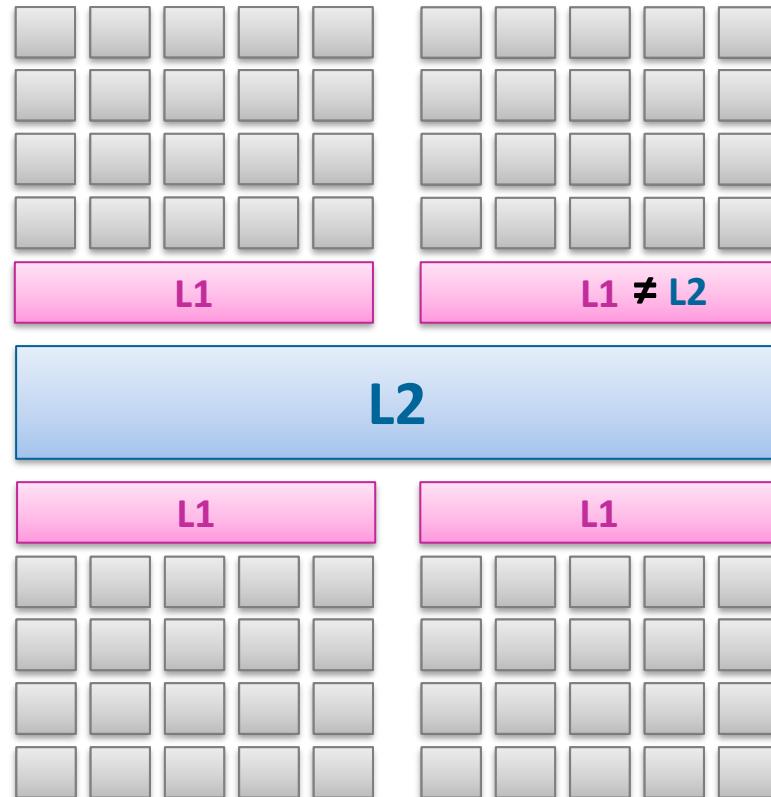


Memory Availability and Visibility

e.g.:

Stage: VK_PIPELINE_STAGE_2_COLOR_ATTACHMENT_OUTPUT_BIT

Access Mask: VK_ACCESS_2_COLOR_ATTACHMENT_WRITE_BIT



“**visibility**” always refers to a combination of **pipeline stage + access mask** which memory is *visible to*

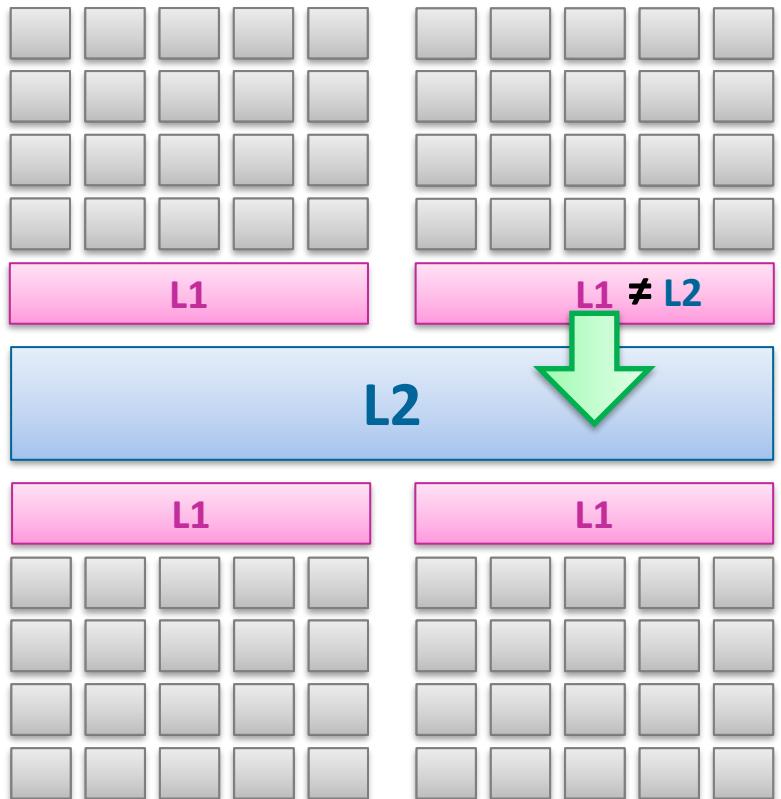
Memory is “**visible**”

Memory is no longer **available**

Can be made available again using a **memory barrier**:



Memory Availability and Visibility



“**visibility**” always refers to a combination of **pipeline stage + access mask** which memory is *visible to*

Memory is “**visible**”

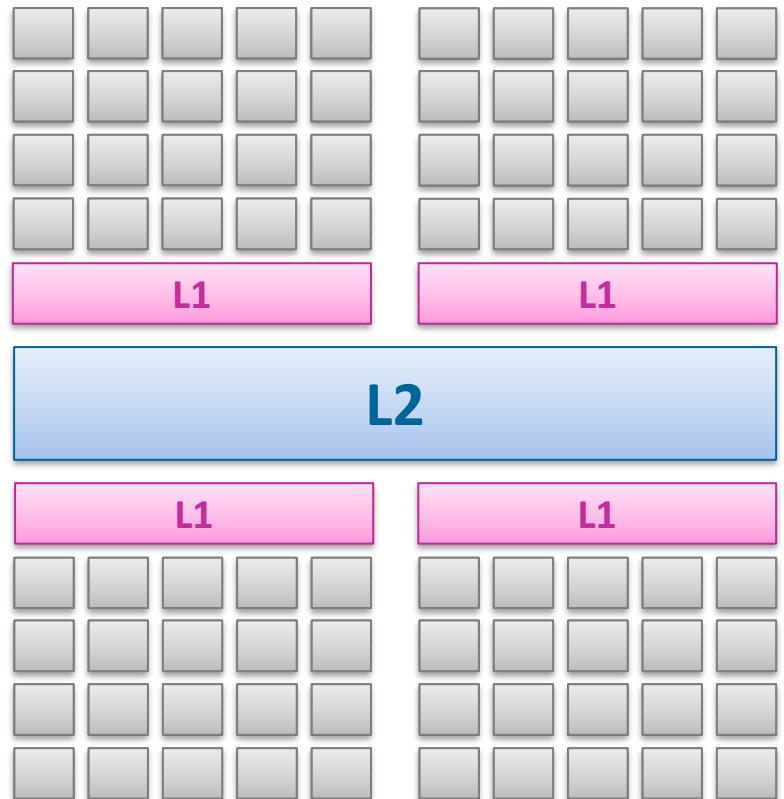
Memory is no longer **available**

Can be made available again using a **memory barrier**:

```
VkMemoryBarrier2 memoryBarrier;  
memoryBarrier.srcStageMask =  
    VK_PIPELINE_STAGE_2_COLOR_ATTACHMENT_OUTPUT_BIT;  
memoryBarrier.srcAccessMask =  
    VK_ACCESS_2_COLOR_ATTACHMENT_WRITE_BIT;
```



Memory Availability and Visibility



Memory is “visible”

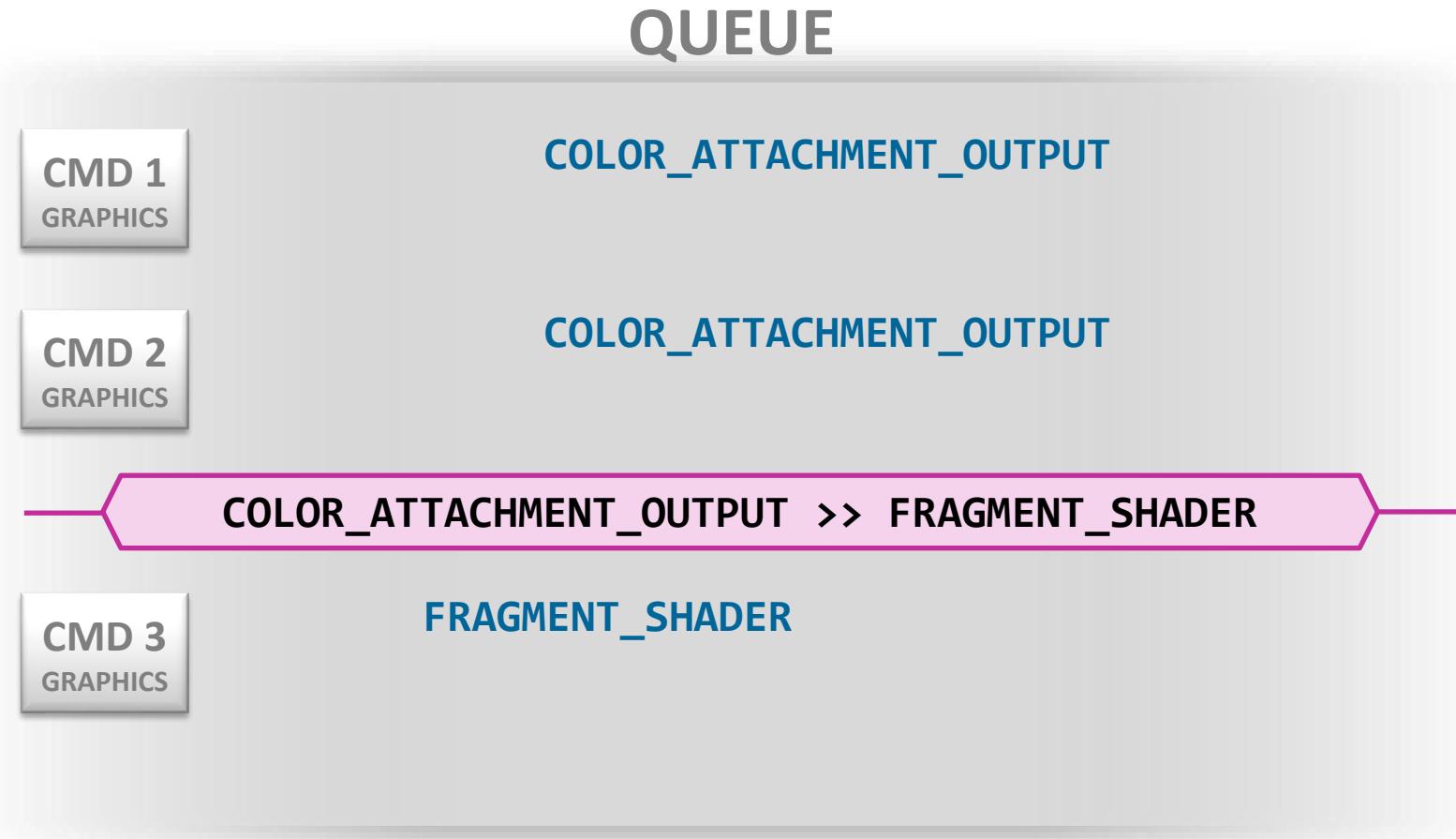
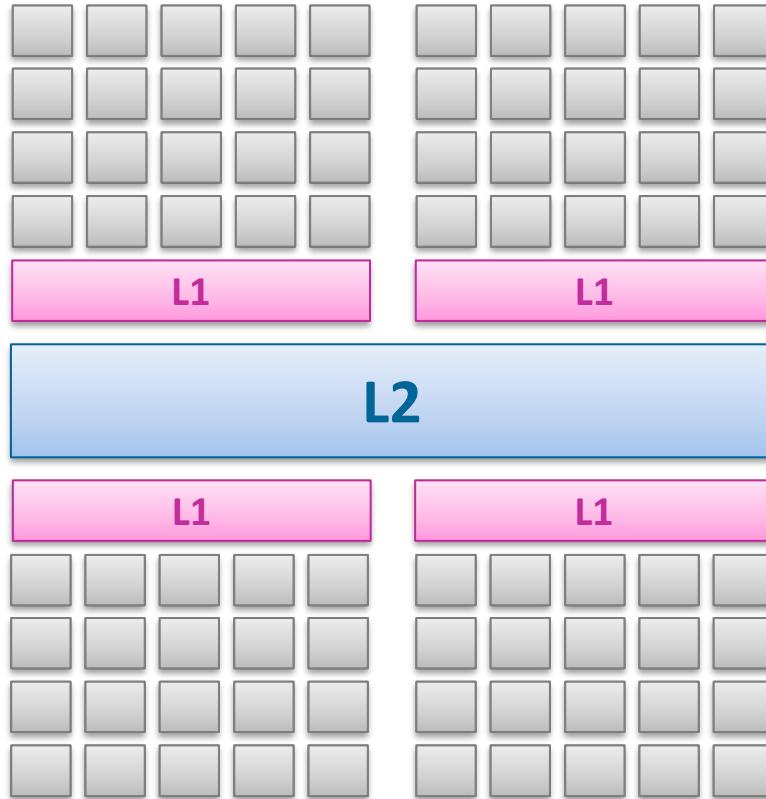
Memory has become **available** again

“**visibility**” always refers to a combination of **pipeline stage + access mask** which memory is *visible to*

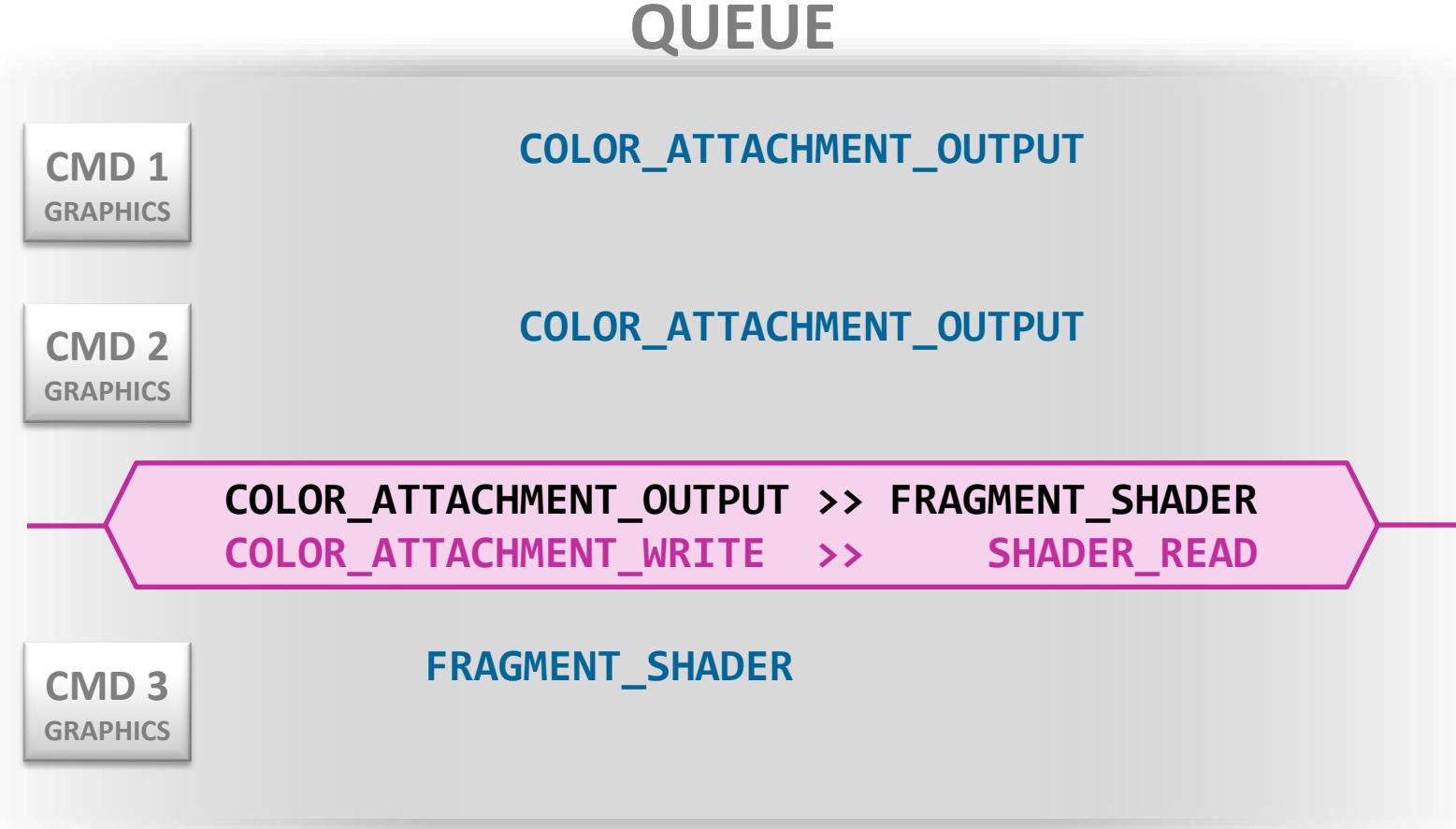
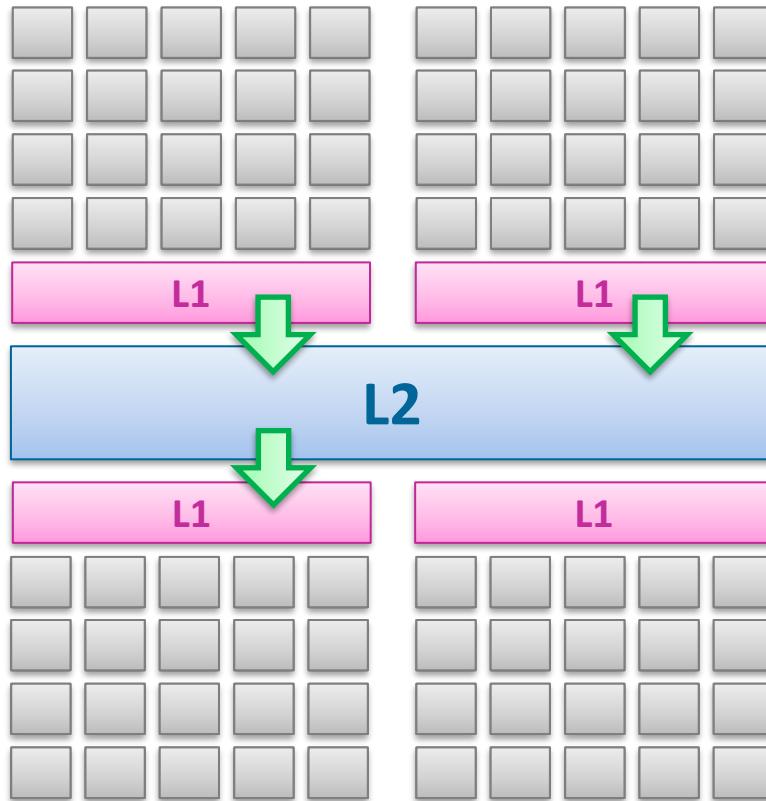
“**availability**” is established from a combination of source **pipeline stage + source access mask**



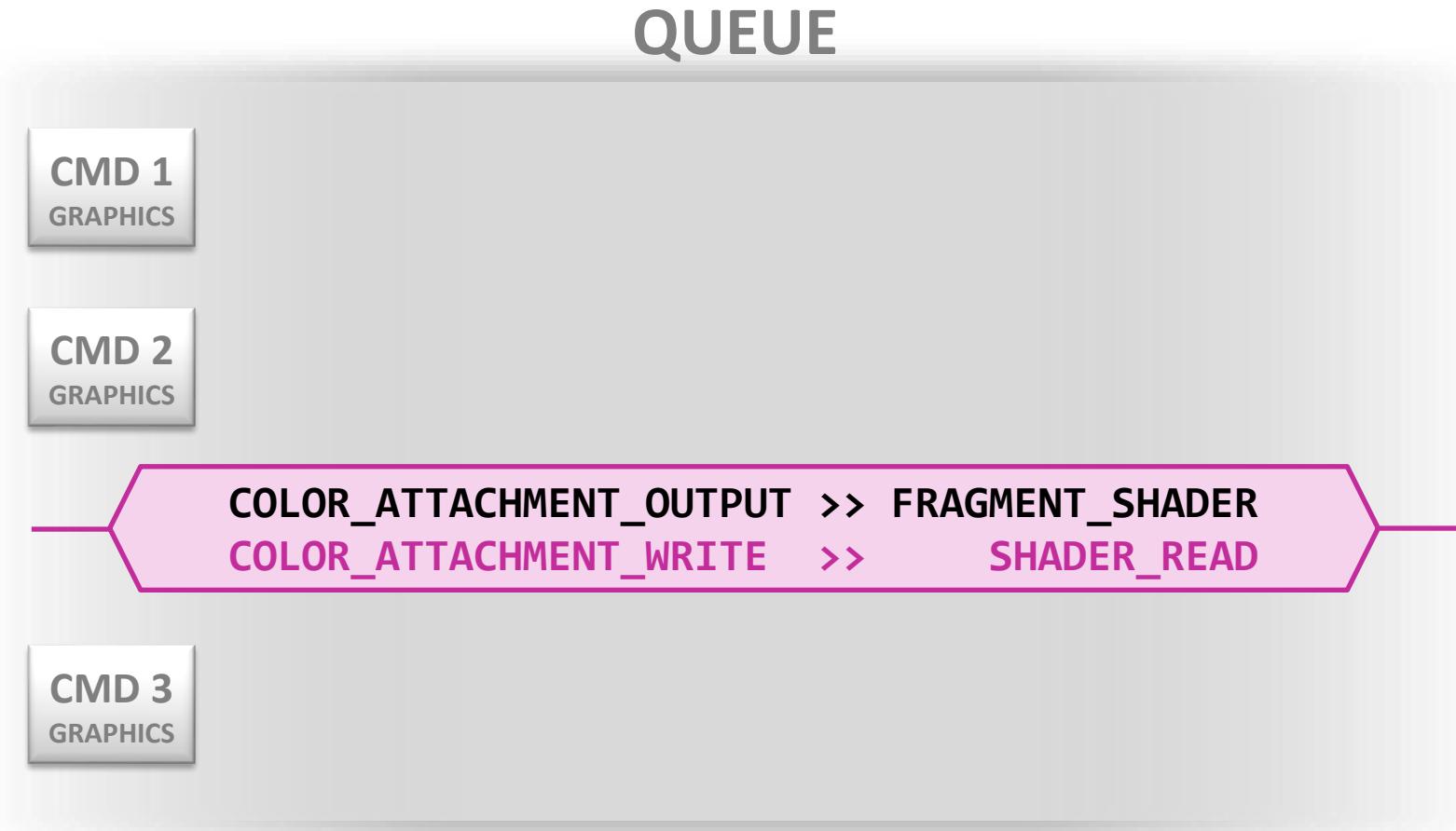
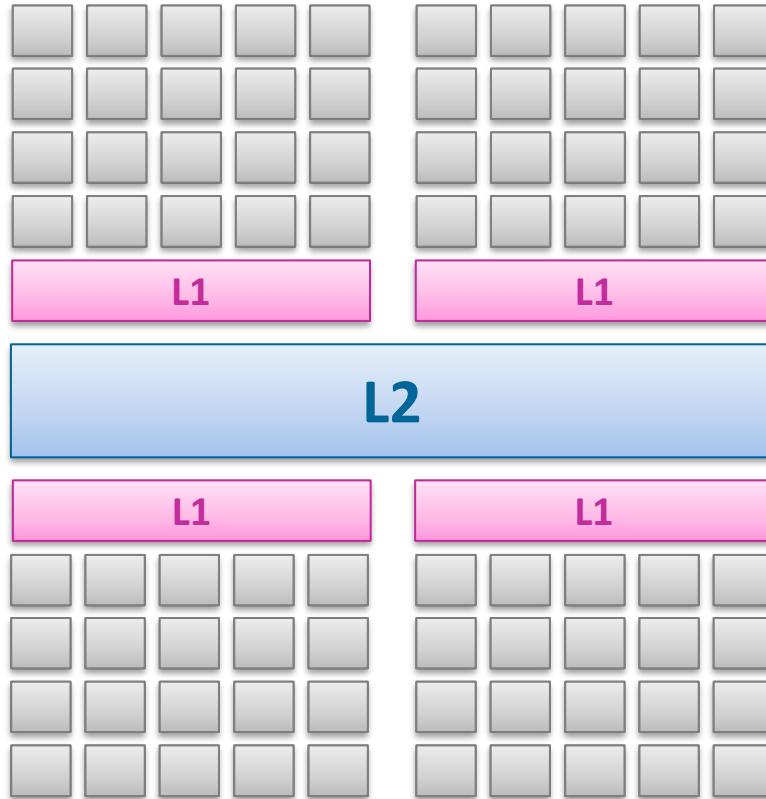
Pipeline Execution Barriers



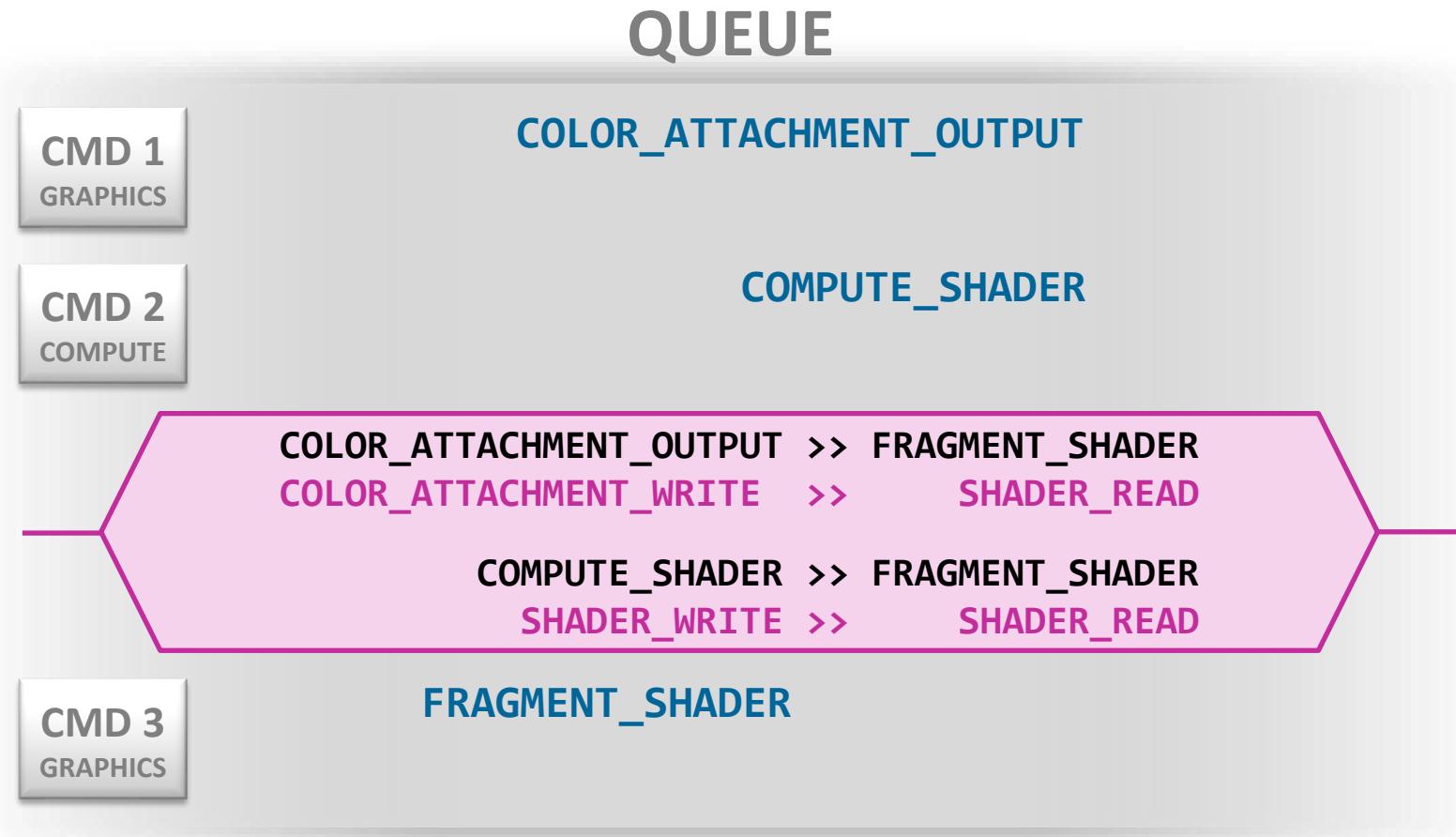
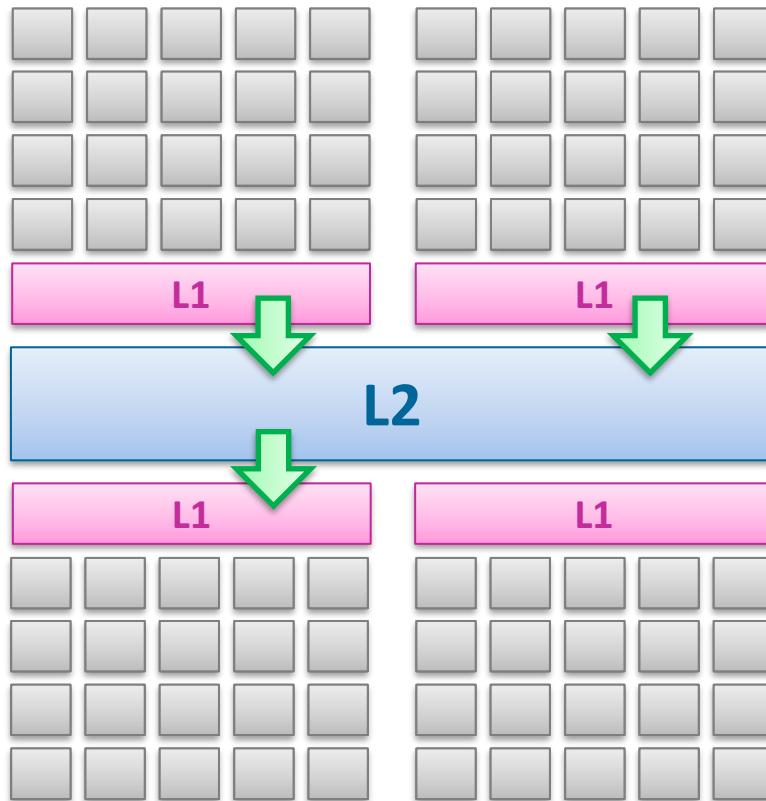
Pipeline Memory Barriers



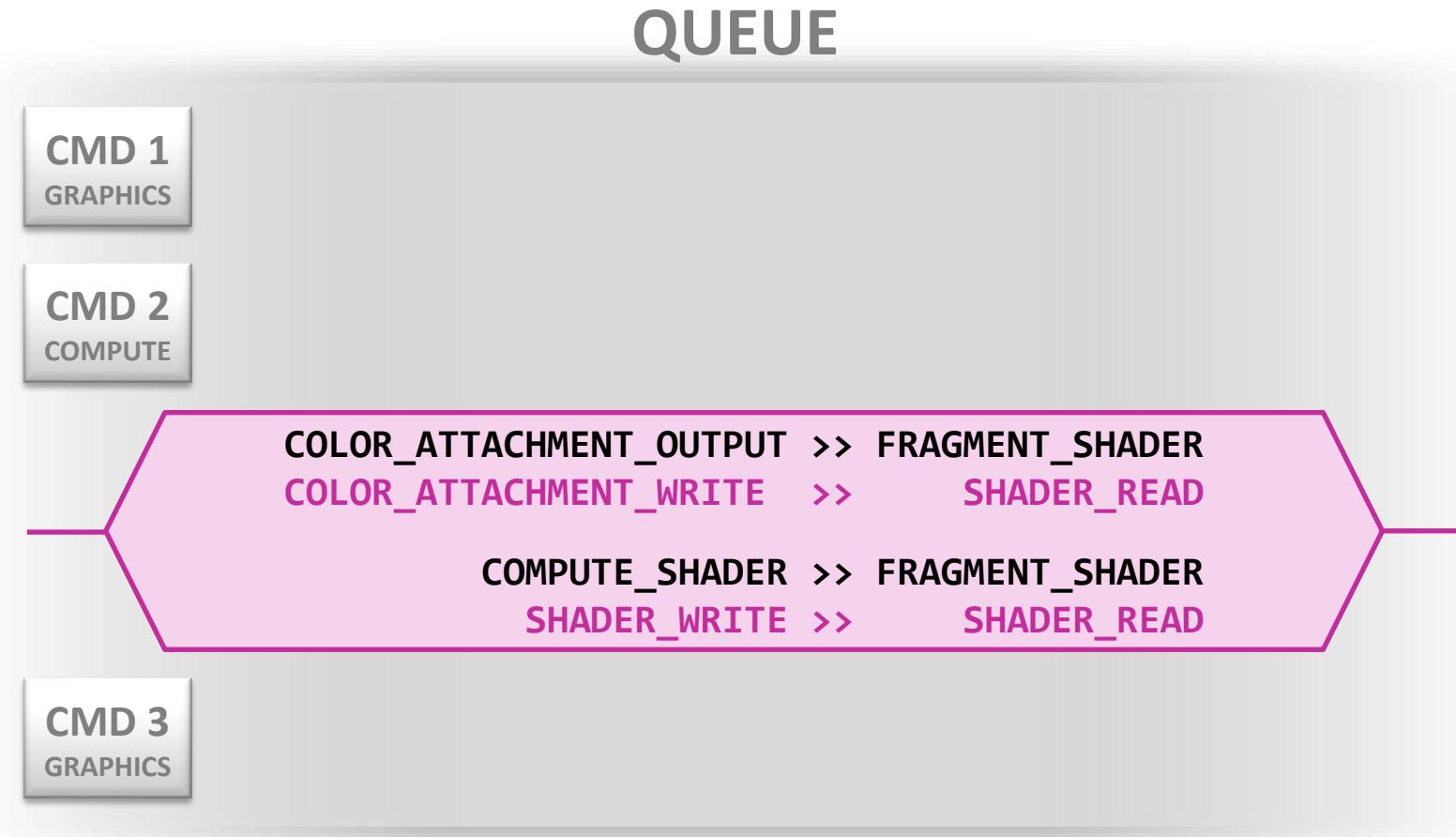
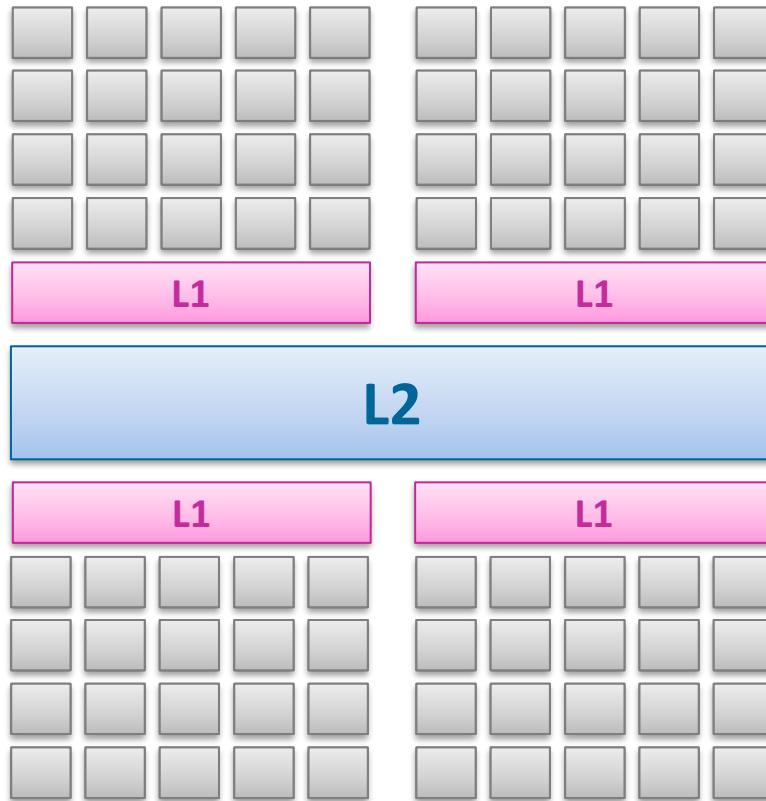
Pipeline Memory Barriers



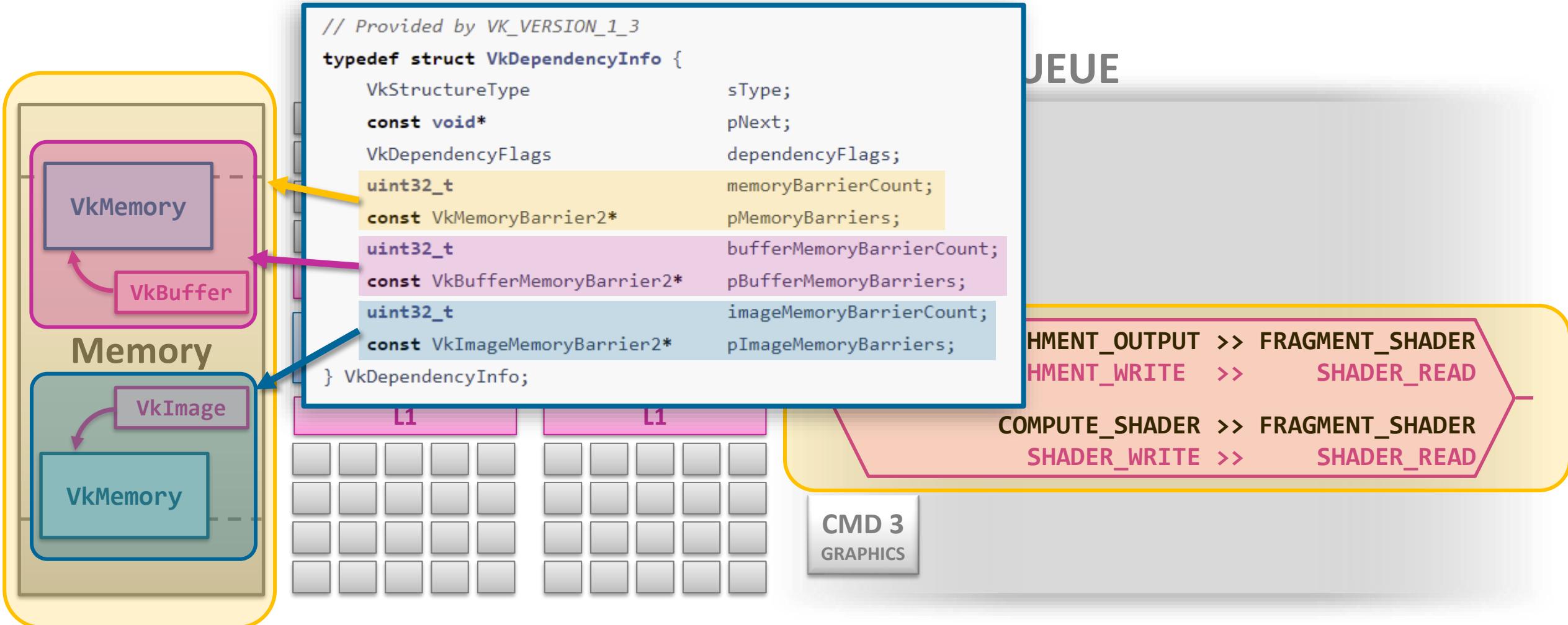
Pipeline Memory Barriers



Pipeline Memory Barriers



Pipeline Memory Barriers



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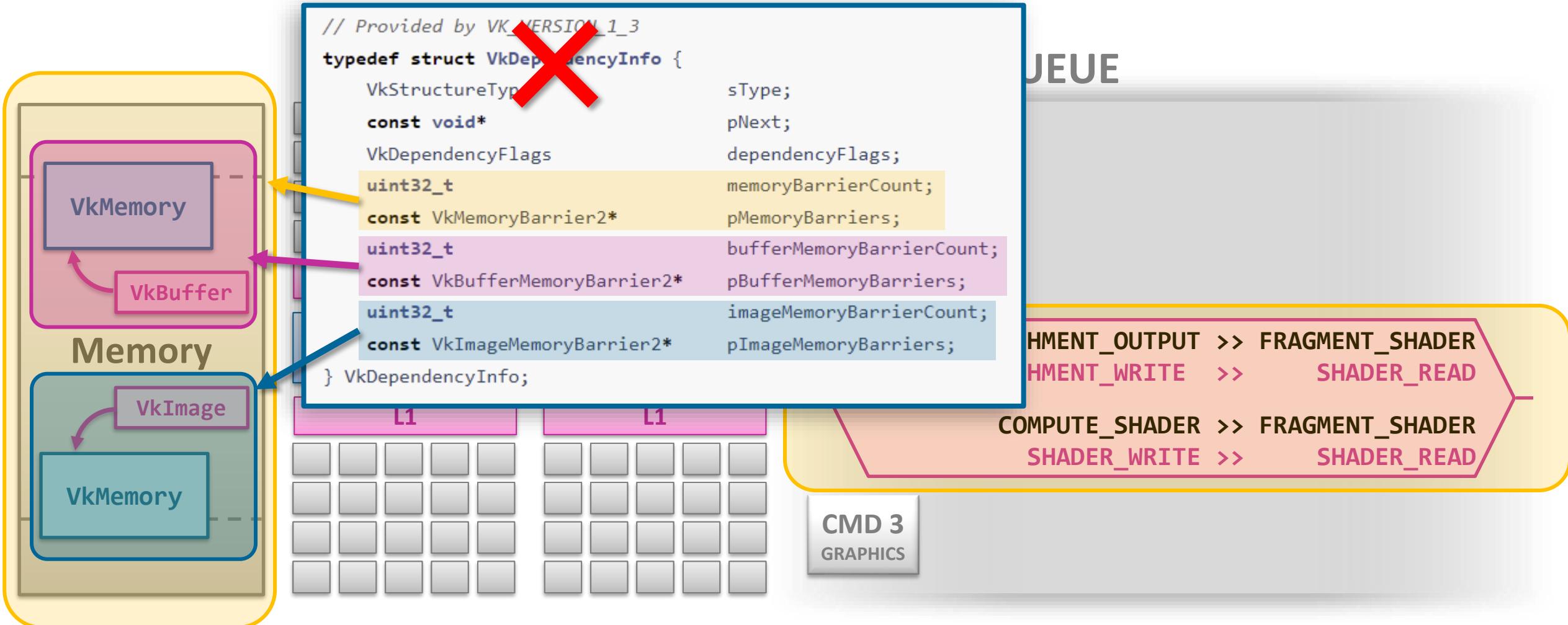


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Limiting the Synchronization Scope...



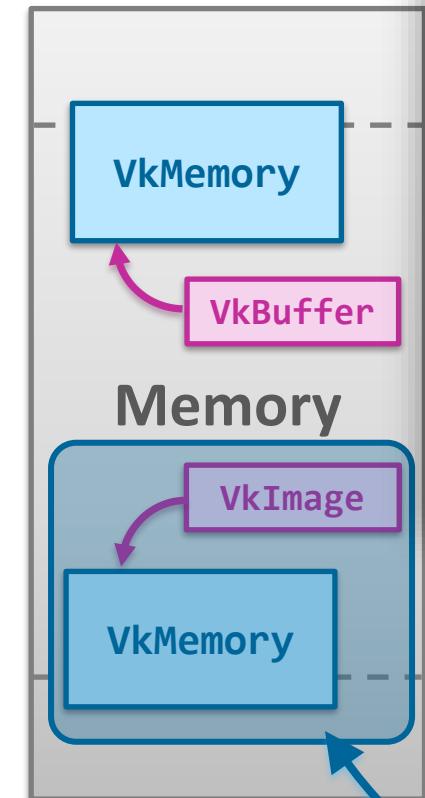
Render Pass Subpass Dependencies

```
// Provided by VK_VERSION_1_2
typedef struct VkSubpassDependency2 {
    VkStructureType          sType;
    const void*               pNext;
    uint32_t                  srcSubpass;
    uint32_t                  dstSubpass;
    VkPipelineStageFlags      srcStageMask;
    VkPipelineStageFlags      dstStageMask;
    VkAccessFlags              srcAccessMask;
    VkAccessFlags              dstAccessMask;
    VkDependencyFlags         dependencyFlags;
    int32_t                   viewOffset;
} VkSubpassDependency2;
```

```
// Provided by VK_VERSION_1_0
typedef struct VkRenderPassBeginInfo {
    VkStructureType          sType;
    const void*               pNext;
    VkRenderPass                renderPass;
    VkFramebuffer              framebuffer;
    VkRect2D                    renderArea;
    uint32_t                  clearValueCount;
    const VkClearValue*        pClearValues;
} VkRenderPassBeginInfo;
```

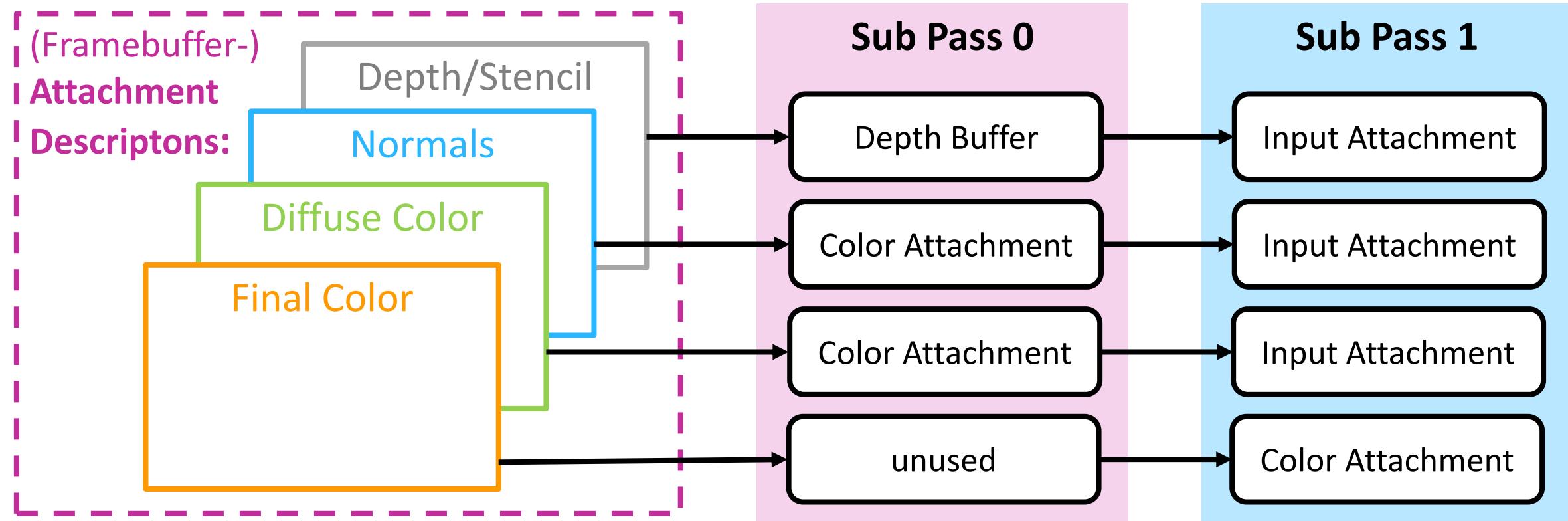
```
// Provided by VK_VERSION_1_2
void vkCmdBeginRenderPass2(
    VkCommandBuffer           commandBuffer,
    const VkRenderPassBeginInfo* pRenderPassBegin,
    const VkSubpassBeginInfo*  pSubpassBeginInfo);
```

```
// Provided by VK_VERSION_1_0
typedef struct VkFramebufferCreateInfo {
    VkStructureType          sType;
    const void*               pNext;
    VkFramebufferCreateFlags   flags;
    VkRenderPass                renderPass;
    uint32_t                  attachmentCount;
    const VkImageView*        pAttachments;
    uint32_t                  width;
    uint32_t                  height;
    uint32_t                  layers;
} VkFramebufferCreateInfo;
```



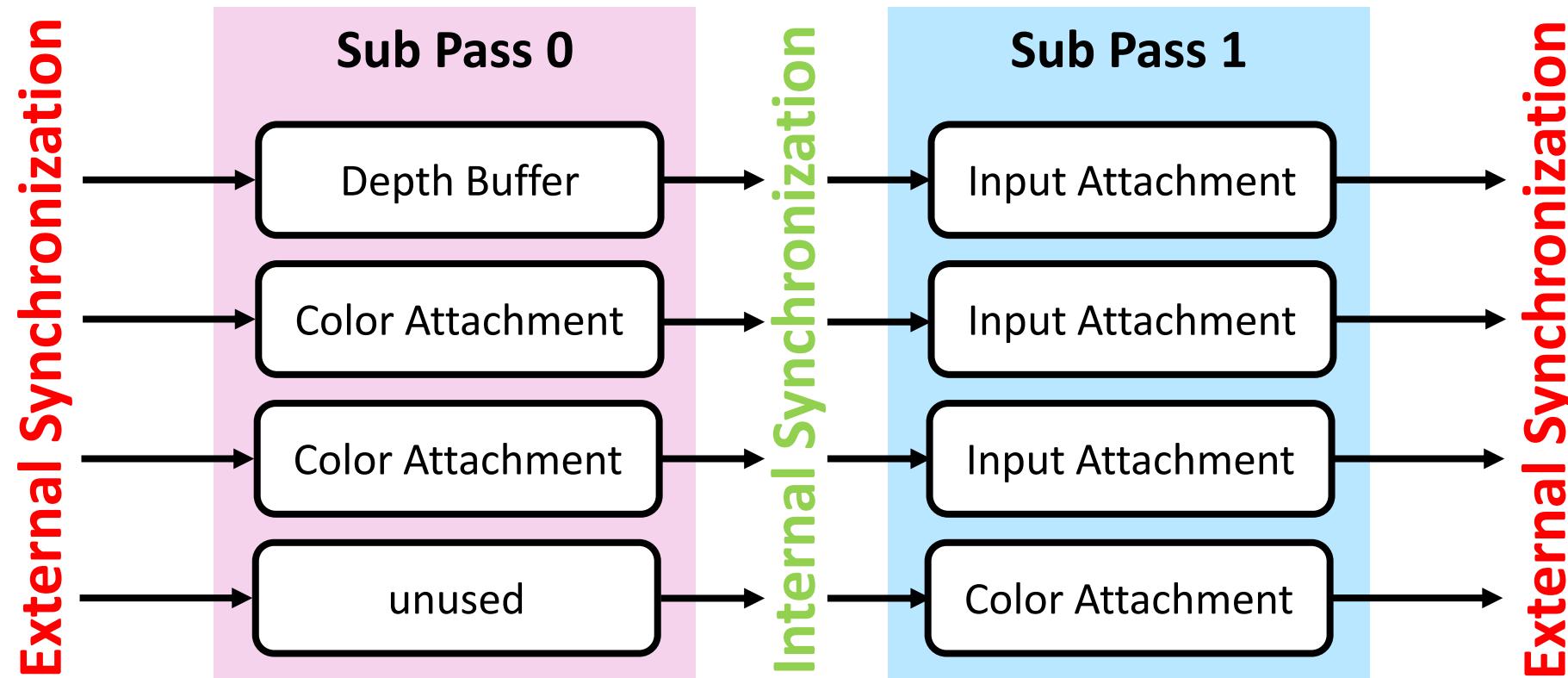
Render Pass

- Only for Graphics Pipelines
- Describes how framebuffer attachments are used
- Describes synchronization between multiple “sub passes”



Sub-pass Dependencies

- External and internal synchronization, i.e. synchronize with other commands, or synchronize with graphics stages and attachments
- Stage flags + access flags (same principle as with pipeline barriers)



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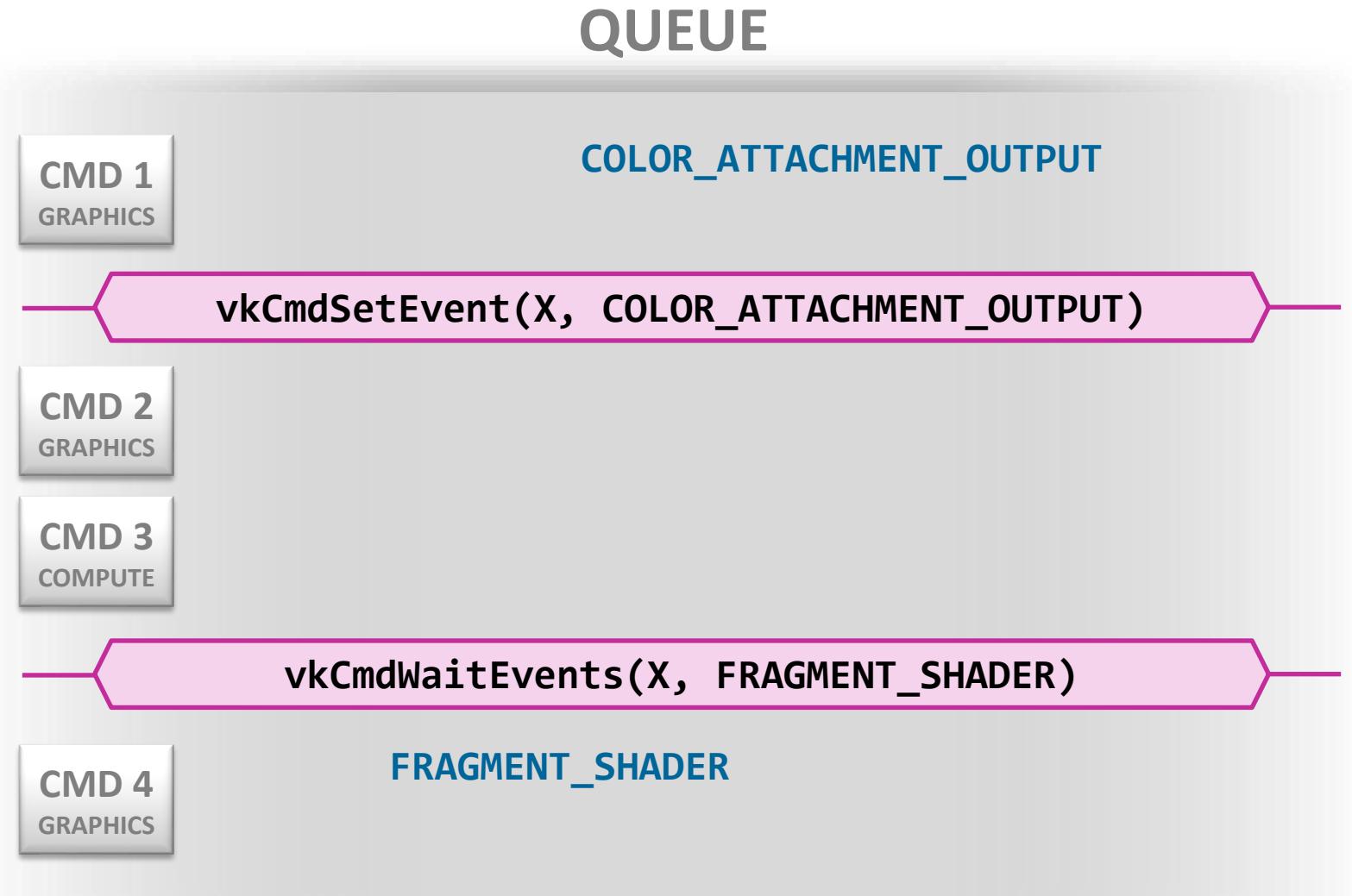
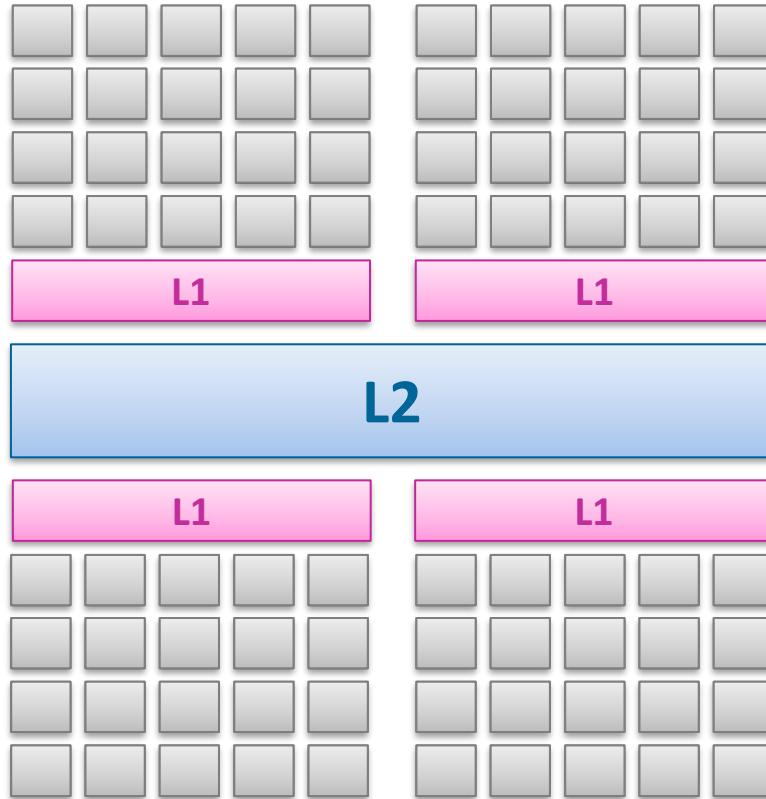


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Events



Events

```
vkCmdSetEvent(X, COLOR_ATTACHMENT_OUTPUT)
```

```
vkCmdWaitEvents(X, FRAGMENT_SHADER)
```

- To signal an event from a device: vkCmdSetEvent2
- To wait for one or more events to enter the signaled state on a device: vkCmdWaitEvents2
- To unsignal the event from a device: vkCmdResetEvent2

- To set the state of an event to signaled from the host: vkSetEvent
- To query the state of an event from the host: vkGetEventStatus
- To set the state of an event to unsignaled from the host: vkResetEvent



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- Render Pass Subpass Dependencies (subpass memory dependencies)
- Events (“split barriers”, host -> device sync)



Useful Stuff

■ Great resources about synchronization

- Hans-Kristian Arntzen. Yet another blog explaining Vulkan synchronization.
<https://themaister.net/blog/2019/08>
- The Khronos Group. Vulkan-Docs. Synchronization Examples
<https://github.com/KhronosGroup/Vulkan-Docs/wiki/Synchronization-Examples>
- Jason Ekstrand. Vulkan Timeline Semaphores.
<https://youtu.be/SpE--Rf516Y>

■ Synchronization validation

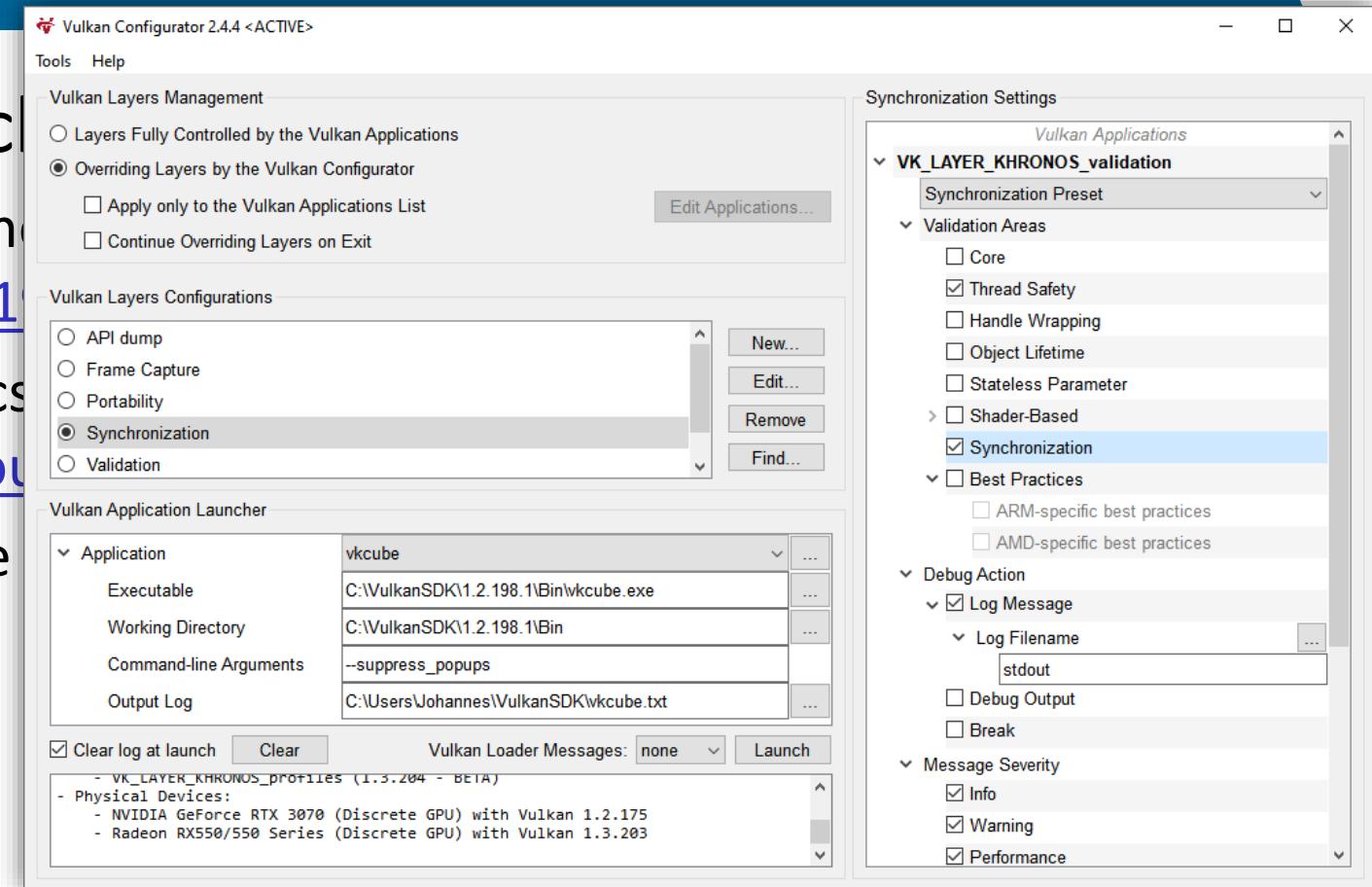
- VK_LAYER_KHRONOS_validation
- The Khronos Group. Vulkan-ValidationLayers. Synchronization Validation
https://github.com/KhronosGroup/Vulkan-ValidationLayers/blob/master/docs/synchronization_usage.md



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Algorithms for Real-Time Rendering

186.192, 2022S, 3.0ECTS

Thank you for your attention!

Johannes Unterguggenberger
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