```
<?xml version="1.0" encoding="UTF-8"?>
<dyntrace>
 <prefix id="A" bitmask="01100111" detail="address size"/>
 <prefix id="B" bitmask="11110000" detail="LOCK"/>
 <prefix id="C" bitmask="01100110" detail="operand size"/>
 <prefix id="D" bitmask="00101110" detail="CS segment override"/>
 <prefix id="E" bitmask="00111110" detail="DS segment override"/>
 <prefix id="F" bitmask="00100110" detail="ES segment override"/>
 <prefix id="G" bitmask="01100100" detail="FS segment override"/>
 <prefix id="H" bitmask="01100101" detail="GS segment override"/>
 <prefix id="I" bitmask="00110110" detail="SS segment override"/>
 <program name="N/A">
  <region type="text:program">
   <opcount bitmask="0000111110000100" mnemonic="Jcc" detail="full displacement" n="2"/>
   <opcount bitmask="0000111110000101" mnemonic="Jcc" detail="full displacement" n="1"/>
   <opcount bitmask="0000111110000110" mnemonic="Jcc" detail="full displacement" n="2"/>
   <opcount bitmask="0010100x11" mnemonic="SUB" detail="register1 to register2" n="1"/>
   <opcount bitmask="0011100" mnemonic="CMP" detail="memory with register" n="3"/>
   <opcount bitmask="0011100x11" mnemonic="CMP" detail="register1 with register2" n="1"/>
   <opcount bitmask="0011101" mnemonic="CMP" detail="register with memory" n="505"/>
   <opcount bitmask="01000" mnemonic="INC" detail="reg (alternate encoding)" n="17"/>
   <opcount bitmask="01010" mnemonic="PUSH" detail="register (alternate encoding)" n="362"/>
   <opcount bitmask="01011" mnemonic="POP" detail="register (alternate encoding)" n="7"/>
   <opcount bitmask="011010x0" mnemonic="PUSH" detail="immediate" n="10"/>
   <opcount bitmask="01110010" mnemonic="Jcc" detail="8-bit displacement" n="169"/>
   <opcount bitmask="01110011" mnemonic="Jcc" detail="8-bit displacement" n="169"/>
   <opcount bitmask="01110100" mnemonic="Jcc" detail="8-bit displacement" n="14"/>
   <opcount bitmask="01110101" mnemonic="Jcc" detail="8-bit displacement" n="205"/>
   <opcount bitmask="01110110" mnemonic="Jcc" detail="8-bit displacement" n="168"/>
   <opcount bitmask="01110111" mnemonic="Jcc" detail="8-bit displacement" n="3"/>
   <opcount bitmask="01111110" mnemonic="Jcc" detail="8-bit displacement" n="1"/>
   <opcount bitmask="01111111" mnemonic="Jcc" detail="8-bit displacement" n="1"/>
   <opcount bitmask="100000xxxx000" mnemonic="ADD" detail="immediate to memory" n="168"/>
   <opcount bitmask="100000xxxx011" mnemonic="SBB" detail="immediate to memory" n="1"/>
   <opcount bitmask="100000xxxx111" mnemonic="CMP" detail="immediate with memory" n="216"/>
   <opcount bitmask="100000xx11000" mnemonic="ADD" detail="immediate to register" n="174"/>
   <opcount bitmask="100000xx11100" mnemonic="AND" detail="immediate to register" n="2"/>
   <opcount bitmask="100000xx11101" mnemonic="SUB" detail="immediate to register" n="182"/>
   <opcount bitmask="100000xx11110" mnemonic="XOR" detail="immediate to register" n="1"/>
   <opcount bitmask="100000xx11111" mnemonic="CMP" detail="immediate with register" n="6"/>
   <opcount bitmask="1000010x11" mnemonic="TEST" detail="register1 and register2" n="5"/>
   <opcount bitmask="1000100" mnemonic="MOV" detail="reg to memory" n="2"/>
   <opcount bitmask="1000100x11" mnemonic="MOV" detail="register1 to register2" n="10"/>
   <opcount bitmask="1000101" mnemonic="MOV" detail="memory to reg" n="350"/>
   <opcount bitmask="10001101" mnemonic="LEA" n="13"/>
   <opcount bitmask="1010000" mnemonic="MOV" detail="memory to AL, AX, or EAX" n="3"/>
   <opcount bitmask="1010001" mnemonic="MOV" detail="AL, AX, or EAX to memory" n="4"/>
   <opcount bitmask="1011" mnemonic="MOV" detail="immediate to register (alternate encoding)
" n="343"/>
   <opcount bitmask="11000011" mnemonic="RET" detail="no argument" n="7"/>
   <opcount bitmask="1100011xxx000" mnemonic="MOV" detail="immediate to memory" n="6"/>
   <opcount bitmask="11001001" mnemonic="LEAVE" n="5"/>
   <opcount bitmask="11101000" mnemonic="CALL" detail="direct" n="184"/>
   <opcount bitmask="11101001" mnemonic="JMP" detail="direct" n="6"/>
   <opcount bitmask="11101011" mnemonic="JMP" detail="short" n="2"/>
   <opcount bitmask="11111111xx100" mnemonic="JMP" detail="memory indirect" n="184"/>
   <opcount bitmask="11111111xx110" mnemonic="PUSH" detail="memory" n="9"/>
  </region>
  <region type="text:library">
   <opcount bitmask="0000000" mnemonic="ADD" detail="register to memory" n="2278"/>
   <opcount bitmask="0000000" mnemonic="ADD" detail="register to memory" prefixes="B" n="83"
/>
   <opcount bitmask="0000000x11" mnemonic="ADD" detail="register1 to register2" n="6825"/>
   <opcount bitmask="0000001" mnemonic="ADD" detail="memory to register" n="9484"/>
   <opcount bitmask="0000010" mnemonic="ADD" detail="immediate to AL, AX, or EAX" n="22"/>
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   <opcount bitmask="0000100" mnemonic="OR" detail="register to memory" n="3"/>
   <opcount bitmask="0000100x11" mnemonic="OR" detail="register1 to register2" n="54"/>
   <opcount bitmask="0000101" mnemonic="OR" detail="memory to register" prefixes="C" n="1"/>
   <opcount bitmask="0000111110000010" mnemonic="Jcc" detail="full displacement" n="1689"/>
   <opcount bitmask="0000111110000011" mnemonic="Jcc" detail="full displacement" n="6"/>
   <opcount bitmask="0000111110000100" mnemonic="Jcc" detail="full displacement" n="3545"/>
   <opcount bitmask="0000111110000101" mnemonic="Jcc" detail="full displacement" n="1633"/>
   <opcount bitmask="0000111110000110" mnemonic="Jcc" detail="full displacement" n="359"/>
   <opcount bitmask="0000111110000111" mnemonic="Jcc" detail="full displacement" n="2080"/>
   <opcount bitmask="0000111110001000" mnemonic="Jcc" detail="full displacement" n="177"/>
   <opcount bitmask="0000111110001110" mnemonic="Jcc" detail="full displacement" n="517"/>
   <opcount bitmask="0000111110001111" mnemonic="Jcc" detail="full displacement" n="8"/>
   <opcount bitmask="0000111110010100xx000" mnemonic="SETcc" detail="memory" n="1"/>
   <opcount bitmask="00001111100l010011000" mnemonic="SETcc" detail="register" n="3"/>
   <opcount bitmask="0000111110010101xx000" mnemonic="SETcc" detail="memory" n="1"/>
   <opcount bitmask="00001111100l010111000" mnemonic="SETcc" detail="register" n="3"/>
   <opcount bitmask="0000111110101111" mnemonic="IMUL" detail="register with memory" n="7"/>
   <opcount bitmask="000011111011000" mnemonic="CMPXCHG" detail="memory, register" prefixes=
"B" n="2"/>
   <opcount bitmask="000011111011011" mnemonic="MOVZX" detail="memory to register" n="9420"/
>
   <opcount bitmask="000011111011011x11" mnemonic="MOVZX" detail="register2 to register1" n=
"337"/>
   <opcount bitmask="000011111011111" mnemonic="MOVSX" detail="memory to reg" n="3380"/>
   <opcount bitmask="0010000" mnemonic="AND" detail="register to memory" n="8"/>
   <opcount bitmask="0010000x11" mnemonic="AND" detail="register1 to register2" n="4376"/>
   <opcount bitmask="0010001" mnemonic="AND" detail="memory to register" n="3"/>
   <opcount bitmask="0010010" mnemonic="AND" detail="immediate to AL, AX, or EAX" n="863"/>
   <opcount bitmask="0010100" mnemonic="SUB" detail="register to memory" n="841"/>
   <opcount bitmask="0010100x11" mnemonic="SUB" detail="register1 to register2" n="4577"/>
   <opcount bitmask="0010101" mnemonic="SUB" detail="memory to register" n="212"/>
   <opcount bitmask="0011000x11" mnemonic="XOR" detail="register1 to register2" n="2240"/>
   <opcount bitmask="0011001" mnemonic="XOR" detail="memory to register" n="1"/>
   <opcount bitmask="0011100" mnemonic="CMP" detail="memory with register" n="9597"/>
   <opcount bitmask="0011100x11" mnemonic="CMP" detail="register1 with register2" n="1868"/>
   <opcount bitmask="0011101" mnemonic="CMP" detail="register with memory" n="5956"/>
   <opcount bitmask="0011110" mnemonic="CMP" detail="immediate with AL, AX, or EAX" n="995"/
>
   <opcount bitmask="01000" mnemonic="INC" detail="reg (alternate encoding)" n="16764"/>
   <opcount bitmask="01001" mnemonic="DEC" detail="register (alternate encoding)" n="572"/>
   <opcount bitmask="01010" mnemonic="PUSH" detail="register (alternate encoding)" n="25370"
/>
   <opcount bitmask="01011" mnemonic="POP" detail="register (alternate encoding)" n="16128"/
>
   <opcount bitmask="011010x0" mnemonic="PUSH" detail="immediate" n="952"/>
   <opcount bitmask="01110010" mnemonic="Jcc" detail="8-bit displacement" n="1705"/>
   <opcount bitmask="01110011" mnemonic="Jcc" detail="8-bit displacement" n="1007"/>
   <opcount bitmask="01110100" mnemonic="Jcc" detail="8-bit displacement" n="26375"/>
   <opcount bitmask="01110101" mnemonic="Jcc" detail="8-bit displacement" n="22939"/>
   <opcount bitmask="01110110" mnemonic="Jcc" detail="8-bit displacement" n="1667"/>
   <opcount bitmask="01110111" mnemonic="Jcc" detail="8-bit displacement" n="2745"/>
   <opcount bitmask="01111000" mnemonic="Jcc" detail="8-bit displacement" n="204"/>
   <opcount bitmask="01111001" mnemonic="Jcc" detail="8-bit displacement" n="509"/>
   <opcount bitmask="01111100" mnemonic="Jcc" detail="8-bit displacement" n="5"/>
   <opcount bitmask="01111101" mnemonic="Jcc" detail="8-bit displacement" n="350"/>
   <opcount bitmask="01111110" mnemonic="Jcc" detail="8-bit displacement" n="921"/>
   <opcount bitmask="01111111" mnemonic="Jcc" detail="8-bit displacement" n="1031"/>
   <opcount bitmask="100000xxxx000" mnemonic="ADD" detail="immediate to memory" n="1224"/>
   <opcount bitmask="100000xxxx001" mnemonic="OR" detail="immediate to memory" n="171"/>
   <opcount bitmask="100000xxxx101" mnemonic="SUB" detail="immediate to memory" n="6"/>
   <opcount bitmask="100000xxxx111" mnemonic="CMP" detail="immediate with memory" n="19962"/
>
   <opcount bitmask="100000xxxx111" mnemonic="CMP" detail="immediate with memory" prefixes="
C" n="411"/>
   <opcount bitmask="100000xx11000" mnemonic="ADD" detail="immediate to register" n="15766"/
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>
   <opcount bitmask="100000xx11001" mnemonic="OR" detail="immediate to register" n="13"/>
   <opcount bitmask="100000xx11100" mnemonic="AND" detail="immediate to register" n="8742"/>
   <opcount bitmask="100000xx11101" mnemonic="SUB" detail="immediate to register" n="9513"/>
   <opcount bitmask="100000xx11110" mnemonic="XOR" detail="immediate to register" n="1181"/>
   <opcount bitmask="100000xx11111" mnemonic="CMP" detail="immediate with register" n="2289"
/>
   <opcount bitmask="100000xx11111" mnemonic="CMP" detail="immediate with register" prefixes
="C" n="2"/>
   <opcount bitmask="1000010" mnemonic="TEST" detail="memory and register" n="2"/>
   <opcount bitmask="1000010x11" mnemonic="TEST" detail="register1 and register2" n="23774"/
>
   <opcount bitmask="1000100" mnemonic="MOV" detail="reg to memory" n="27345"/>
   <opcount bitmask="1000100" mnemonic="MOV" detail="reg to memory" prefixes="C" n="174"/>
   <opcount bitmask="1000100x11" mnemonic="MOV" detail="register1 to register2" n="32209"/>
   <opcount bitmask="1000101" mnemonic="MOV" detail="memory to reg" n="93819"/>
   <opcount bitmask="1000101" mnemonic="MOV" detail="memory to reg" prefixes="C" n="171"/>
   <opcount bitmask="10001101" mnemonic="LEA" n="13827"/>
   <opcount bitmask="1000111011" mnemonic="MOV" detail="register to segment register, regist
er to SS" n="1"/>
   <opcount bitmask="10010000" mnemonic="NOP" n="1326"/>
   <opcount bitmask="10011001" mnemonic="CDQ/CWD" n="7"/>
   <opcount bitmask="10011100" mnemonic="PUSHF/PUSHFD" n="41"/>
   <opcount bitmask="10011101" mnemonic="POPF/POPFD" n="41"/>
   <opcount bitmask="1010100" mnemonic="TEST" detail="immediate and AL, AX, or EAX" n="85"/>
   <opcount bitmask="1011" mnemonic="MOV" detail="immediate to register (alternate encoding)
" n="6360"/>
   <opcount bitmask="1011" mnemonic="MOV" detail="immediate to register (alternate encoding)
" prefixes="C" n="1"/>
   <opcount bitmask="1100000xxx100" mnemonic="SHL" detail="memory by immediate count" n="1"/
>
   <opcount bitmask="1100000xxx101" mnemonic="SHR" detail="memory by immediate count" n="2"/
>
   <opcount bitmask="1100000x11100" mnemonic="SHL" detail="register by immediate count" n="6
992"/>
   <opcount bitmask="1100000x11101" mnemonic="SHR" detail="register by immediate count" n="5
470"/>
   <opcount bitmask="1100000x11111" mnemonic="SAR" detail="register by immediate count" n="1
892"/>
   <opcount bitmask="11000011" mnemonic="RET" detail="no argument" n="7817"/>
   <opcount bitmask="1100011xxx000" mnemonic="MOV" detail="immediate to memory" n="7759"/>
   <opcount bitmask="1100011xxx000" mnemonic="MOV" detail="immediate to memory" prefixes="C"
 n="5"/>
   <opcount bitmask="11001001" mnemonic="LEAVE" n="6077"/>
   <opcount bitmask="11001101" mnemonic="INT n" n="220"/>
   <opcount bitmask="1101000xxx111" mnemonic="SAR" detail="memory by 1" n="5"/>
   <opcount bitmask="1101000x11100" mnemonic="SHL" detail="register by 1" n="559"/>
   <opcount bitmask="1101000x11101" mnemonic="SHR" detail="register by 1" n="337"/>
   <opcount bitmask="1101001xxx100" mnemonic="SHL" detail="memory by CL" n="1"/>
   <opcount bitmask="1101001x11000" mnemonic="ROL" detail="register by CL" n="8"/>
   <opcount bitmask="1101001x11100" mnemonic="SHL" detail="register by CL" n="12"/>
   <opcount bitmask="1101001x11101" mnemonic="SHR" detail="register by CL" n="2"/>
   <opcount bitmask="11011001xx111" mnemonic="FSTCW" n="1"/>
   <opcount bitmask="11101000" mnemonic="CALL" detail="direct" n="11952"/>
   <opcount bitmask="11101001" mnemonic="JMP" detail="direct" n="2984"/>
   <opcount bitmask="11101011" mnemonic="JMP" detail="short" n="3064"/>
   <opcount bitmask="1111001010010111" mnemonic="REPNE SCAS" n="702"/>
   <opcount bitmask="1111001110010010" mnemonic="REP MOVS" n="1069"/>
   <opcount bitmask="1111001110010101" mnemonic="REP STOS" n="2322"/>
   <opcount bitmask="1111011xxx000" mnemonic="TEST" detail="immediate and memory" n="14"/>
   <opcount bitmask="1111011xxx110" mnemonic="DIV" detail="AL, AX, or EAX by memory" n="1003
"/>
   <opcount bitmask="1111011x11000" mnemonic="TEST" detail="immediate and register" n="18"/>
   <opcount bitmask="1111011x11010" mnemonic="NOT" detail="register" n="4207"/>
   <opcount bitmask="1111011x11011" mnemonic="NEG" detail="register" n="11"/>
```

```
   <opcount bitmask="1111011x11101" mnemonic="IMUL" detail="AL, AX, or EAX with register" n=
"942"/>
   <opcount bitmask="1111011x11111" mnemonic="IDIV" detail="AL, AX, or EAX by register" n="7
"/>
   <opcount bitmask="11111100" mnemonic="CLD" n="695"/>
   <opcount bitmask="1111111xxx000" mnemonic="INC" detail="memory" n="2944"/>
   <opcount bitmask="1111111xxx001" mnemonic="DEC" detail="memory" n="512"/>
   <opcount bitmask="1111111xxx001" mnemonic="DEC" detail="memory" prefixes="C" n="3"/>
   <opcount bitmask="11111111xx010" mnemonic="CALL" detail="memory indirect" n="179"/>
   <opcount bitmask="11111111xx100" mnemonic="JMP" detail="memory indirect" n="2250"/>
   <opcount bitmask="11111111xx110" mnemonic="PUSH" detail="memory" n="14073"/>
   <opcount bitmask="1111111111010" mnemonic="CALL" detail="register indirect" n="174"/>
   <opcount bitmask="1111111111100" mnemonic="JMP" detail="register indirect" n="2084"/>
  </region>
 </program>
</dyntrace>
```

```
<?xml version="1.0" encoding="UTF-8"?>
<dyntrace>
  <program name="N/A">
    <region type="text:program">
      <opcount mnemonic="Jcc" n="735"/>
      <opcount mnemonic="SUB" n="183"/>
      <opcount mnemonic="CMP" n="731"/>
      <opcount mnemonic="INC" n="17"/>
      <opcount mnemonic="PUSH" n="381"/>
      <opcount mnemonic="POP" n="7"/>
      <opcount mnemonic="ADD" n="342"/>
      <opcount mnemonic="SBB" n="1"/>
      <opcount mnemonic="AND" n="2"/>
      <opcount mnemonic="XOR" n="1"/>
      <opcount mnemonic="TEST" n="5"/>
      <opcount mnemonic="MOV" n="718"/>
      <opcount mnemonic="LEA" n="13"/>
      <opcount mnemonic="RET" n="7"/>
      <opcount mnemonic="LEAVE" n="5"/>
      <opcount mnemonic="CALL" n="184"/>
      <opcount mnemonic="JMP" n="192"/>
    </region>
    <region type="text:library">
      <opcount mnemonic="ADD" n="35682"/>
      <opcount mnemonic="OR" n="242"/>
      <opcount mnemonic="Jcc" n="69472"/>
      <opcount mnemonic="SETcc" n="8"/>
      <opcount mnemonic="IMUL" n="949"/>
      <opcount mnemonic="CMPXCHG" n="2"/>
      <opcount mnemonic="MOVZX" n="9757"/>
      <opcount mnemonic="MOVSX" n="3380"/>
      <opcount mnemonic="AND" n="13992"/>
      <opcount mnemonic="SUB" n="15149"/>
      <opcount mnemonic="XOR" n="3422"/>
      <opcount mnemonic="CMP" n="41080"/>
      <opcount mnemonic="INC" n="19708"/>
      <opcount mnemonic="DEC" n="1087"/>
      <opcount mnemonic="PUSH" n="40395"/>
      <opcount mnemonic="POP" n="16128"/>
      <opcount mnemonic="TEST" n="23893"/>
      <opcount mnemonic="MOV" n="167844"/>
      <opcount mnemonic="LEA" n="13827"/>
      <opcount mnemonic="NOP" n="1326"/>
      <opcount mnemonic="CDQ/CWD" n="7"/>
      <opcount mnemonic="PUSHF/PUSHFD" n="41"/>
      <opcount mnemonic="POPF/POPFD" n="41"/>
      <opcount mnemonic="SHL" n="7565"/>
      <opcount mnemonic="SHR" n="5811"/>
      <opcount mnemonic="SAR" n="1897"/>
      <opcount mnemonic="RET" n="7817"/>
      <opcount mnemonic="LEAVE" n="6077"/>
      <opcount mnemonic="INT n" n="220"/>
      <opcount mnemonic="ROL" n="8"/>
      <opcount mnemonic="FSTCW" n="1"/>
      <opcount mnemonic="CALL" n="12305"/>
      <opcount mnemonic="JMP" n="10382"/>
      <opcount mnemonic="REPNE SCAS" n="702"/>
      <opcount mnemonic="REP MOVS" n="1069"/>
      <opcount mnemonic="REP STOS" n="2322"/>
      <opcount mnemonic="DIV" n="1003"/>
      <opcount mnemonic="NOT" n="4207"/>
      <opcount mnemonic="NEG" n="11"/>
      <opcount mnemonic="IDIV" n="7"/>
      <opcount mnemonic="CLD" n="695"/>
    </region>
```

```
  </program>
</dyntrace>
```

```
<?xml version="1.0" encoding="UTF-8"?>
<dyntrace>
  <program name="N/A">
    <region type="text:program">
      <opcount mnemonic="Jcc" n="735" relfreq="0.20856981"/>
      <opcount mnemonic="SUB" n="183" relfreq="0.05192963"/>
      <opcount mnemonic="CMP" n="731" relfreq="0.20743473"/>
      <opcount mnemonic="INC" n="17" relfreq="0.00482406"/>
      <opcount mnemonic="PUSH" n="381" relfreq="0.10811578"/>
      <opcount mnemonic="POP" n="7" relfreq="0.00198638"/>
      <opcount mnemonic="ADD" n="342" relfreq="0.09704881"/>
      <opcount mnemonic="SBB" n="1" relfreq="0.00028377"/>
      <opcount mnemonic="AND" n="2" relfreq="0.00056754"/>
      <opcount mnemonic="XOR" n="1" relfreq="0.00028377"/>
      <opcount mnemonic="TEST" n="5" relfreq="0.00141884"/>
      <opcount mnemonic="MOV" n="718" relfreq="0.20374574"/>
      <opcount mnemonic="LEA" n="13" relfreq="0.00368899"/>
      <opcount mnemonic="RET" n="7" relfreq="0.00198638"/>
      <opcount mnemonic="LEAVE" n="5" relfreq="0.00141884"/>
      <opcount mnemonic="CALL" n="184" relfreq="0.05221339"/>
      <opcount mnemonic="JMP" n="192" relfreq="0.05448354"/>
    </region>
    <region type="text:library">
      <opcount mnemonic="ADD" n="35682" relfreq="0.06613522"/>
      <opcount mnemonic="OR" n="242" relfreq="0.00044854"/>
      <opcount mnemonic="Jcc" n="69472" relfreq="0.12876369"/>
      <opcount mnemonic="SETcc" n="8" relfreq="0.00001483"/>
      <opcount mnemonic="IMUL" n="949" relfreq="0.00175894"/>
      <opcount mnemonic="CMPXCHG" n="2" relfreq="0.00000371"/>
      <opcount mnemonic="MOVZX" n="9757" relfreq="0.01808423"/>
      <opcount mnemonic="MOVSX" n="3380" relfreq="0.00626470"/>
      <opcount mnemonic="AND" n="13992" relfreq="0.02593363"/>
      <opcount mnemonic="SUB" n="15149" relfreq="0.02807809"/>
      <opcount mnemonic="XOR" n="3422" relfreq="0.00634255"/>
      <opcount mnemonic="CMP" n="41080" relfreq="0.07614020"/>
      <opcount mnemonic="INC" n="19708" relfreq="0.03652802"/>
      <opcount mnemonic="DEC" n="1087" relfreq="0.00201471"/>
      <opcount mnemonic="PUSH" n="40395" relfreq="0.07487058"/>
      <opcount mnemonic="POP" n="16128" relfreq="0.02989263"/>
      <opcount mnemonic="TEST" n="23893" relfreq="0.04428476"/>
      <opcount mnemonic="MOV" n="167844" relfreq="0.31109241"/>
      <opcount mnemonic="LEA" n="13827" relfreq="0.02562781"/>
      <opcount mnemonic="NOP" n="1326" relfreq="0.00245769"/>
      <opcount mnemonic="CDQ/CWD" n="7" relfreq="0.00001297"/>
      <opcount mnemonic="PUSHF/PUSHFD" n="41" relfreq="0.00007599"/>
      <opcount mnemonic="POPF/POPFD" n="41" relfreq="0.00007599"/>
      <opcount mnemonic="SHL" n="7565" relfreq="0.01402144"/>
      <opcount mnemonic="SHR" n="5811" relfreq="0.01077047"/>
      <opcount mnemonic="SAR" n="1897" relfreq="0.00351602"/>
      <opcount mnemonic="RET" n="7817" relfreq="0.01448851"/>
      <opcount mnemonic="LEAVE" n="6077" relfreq="0.01126349"/>
      <opcount mnemonic="INT n" n="220" relfreq="0.00040776"/>
      <opcount mnemonic="ROL" n="8" relfreq="0.00001483"/>
      <opcount mnemonic="FSTCW" n="1" relfreq="0.00000185"/>
      <opcount mnemonic="CALL" n="12305" relfreq="0.02280685"/>
      <opcount mnemonic="JMP" n="10382" relfreq="0.01924264"/>
      <opcount mnemonic="REPNE SCAS" n="702" relfreq="0.00130113"/>
      <opcount mnemonic="REP MOVS" n="1069" relfreq="0.00198135"/>
      <opcount mnemonic="REP STOS" n="2322" relfreq="0.00430374"/>
      <opcount mnemonic="DIV" n="1003" relfreq="0.00185902"/>
      <opcount mnemonic="NOT" n="4207" relfreq="0.00779751"/>
      <opcount mnemonic="NEG" n="11" relfreq="0.00002039"/>
      <opcount mnemonic="IDIV" n="7" relfreq="0.00001297"/>
      <opcount mnemonic="CLD" n="695" relfreq="0.00128816"/>
    </region>
```

```
  </program>
</dyntrace>
```