Kevin Chen

10 Lisa Lane, Cherry Hill, NJ 08003 | kchen7@seas.upenn.edu
(856) 857 – 4373 | linkedin.com/in/kevinchen929

EDUCATION

University of Pennsylvania, School of Engineering & Applied Science, Philadelphia, PA — May 2021
Master of Science in Engineering in Electrical Engineering — **GPA:** 3.86/4.00
Bachelor of Science in Engineering in Computer Engineering — Dean's List '17 – '18, '18 – '19
Minors: Mathematics; Engineering Entrepreneurship
Relevant Coursework: Digital Circuit-Level Modeling; Data Structures and Algorithms; Electrical Circuits and Systems; Embedded Systems Lab; *Current*: Computer Organization and Design; Intro to MEMS and NEMS

Cherry Hill High School East, Cherry Hill, NJ — June 2017
Graduate, **Class Rank:** 1/541 — **GPA:** 4.00/4.00
Honors Included: American Invitational Math Exam Qualifier, Cum Laude Honor Society, National AP Scholar

TECHNICAL EXPERIENCE

Electrical Lead, Penn Electric Racing, *University of Pennsylvania* — 2017 – present

- Led a 25 member electrical team in designing the custom electronics and wiring harness used in electric racecars that consistently place top three internationally at FSAE Lincoln, America's largest student-built racecar competition
- Designed a semi-distributed, 600V battery management system, consisting of daughter boards that monitor the temperature and voltage of lithium ion batteries and a motherboard that analyzes and responds to the data
- Drove the PCB design timeline by reviewing 15 four-layer boards, including a LCD dashboard and a power distribution unit, that use STM32 microcontrollers, implement CAN, and total 2000 components
- Fostered team growth by managing a Confluence documentation effort, resulting in 50 pages of knowledge transfer and design justification, expediting the onboarding process for a 75% increase in hardware membership

Electrical Engineering Intern, Latch, *New York City* — Summer 2019

- Designed an interface board for all Latch devices, halving the number of setup steps required to debug problems
- Implemented a solution for monitoring the battery capacity and usage of Latch devices in lifecycle testing
- Established a series of bring-up tests to verify the power management and data transmission of new devices

Software Intern, Office of Naval Research, *Naval Surface Warfare Center Philadelphia Division* — Summer 2018

- Developed Java software that simulates programmable logic controller's input/output modules, expediting the process of analyzing different module configurations while removing costs associated with testing hardware solutions
- Prototyped a modeling environment, and its user interface, to lay out the framework for efficient automated testing

COMMUNITY ACTIVITIES

Head Teaching Assistant, Penn Engineering Online Learning — 2018 – present

- Course: Discrete Mathematics for Computer Science
- Coordinated the schedules of 5 Teaching Assistants to assist over 250 online master students from around the world
- Helped spearhead Penn's new Online Master of Computer and Information Technology (MCIT) by holding weekly webinars and office hours to go over topics in discrete mathematics like combinatorics, graph theory, and probability

Committee Director, Science Olympiad at the University of Pennsylvania — 2017 – present

- Organized a competition attended by over 900 people forming top Science Olympiad teams from across the nation
- Acted as the liaison between a committee of nine event writers and the rest of the executive board, ensuring the preparation of five different national-level events. Personally wrote and ran labs for dc circuit analysis and optics

SKILLS AND INTERESTS

- **Skills:** PCB Design (Altium), SPICE, Electronics Laboratory Equipment, Surface Mount Soldering, Excel
- **Programming Languages:** Java, C, C++, MATLAB, OCaml, Embedded Prototyping Platforms (Arduino, mbed)
- **Interests:** Origami, Touch Typing (90 WPM), Combinatorics