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| **Education** | | | | | | |
| Bachelor of Science in Software Engineering  Brigham Young University – Idaho (GPA 3.6) | | | | | December 2020  Rexburg, ID | |
| * Emphasis: Computer Systems * Minor: Computer Engineering * Certificates: Embedded Systems and Data Science * Honors: Thomas E Ricks Academic Scholarship, and Tau Beta Pi Engineering Honor Society * Coursework: Digital System Design, Advanced Embedded Systems, Computer Architecture, Electric Circuit Theory, System Security, Project Management, Discrete Math, Data Wrangling and Visualization, Database Development | | | | | | |
| **Skills** | | | | | | |
| Computer Programming and Systems | | | | | | |
| * + Embedded C   + ROS | * + C/C++   + Python | * Mathematica * MATLAB | * SQL * Java | * HTML * CSS | * Bash/GIT * Verilog | * Assembly * VxWorks |
| Data Science Programming | | | | | | |
| * R and Rstudio | * Tidyverse | * Reticulate | * Lubridate | * Python | * Pandas | * MatplotLib |
| **Experience** | | | | | | |
| Teaching Assistant and Tutor  BYUI CSEE & CS Help Lab | | | | April 2018 – Present  Rexburg, ID | | |
| * Lead students to increase their knowledge of Embedded, and Digital Systems and Discrete Math * Guide students to improvement with feedback | | | | | | |
| * Help students with programming Erlang (Elisp), Verilog and Embedded C | | | | | | |
| **BYU- Idaho CSEE Projects** | | | | | | |
| Electric Skateboard | | | | | | 40 Hours |
| * Assembled the electronics and programmed the VESC in Embedded C * Presented at the Research and Creative Works Conference | | | | | | |
| Calendaring App | | | | | | 50 Hours |
| * Worked with team to develop the Java code while using Github (reword) * Presented to the Eastern Idaho Innovator's Challenge | | | | | | |
| Optimizing Calendar C++ Program | | | | | | 15 Hours |
| Current-limited (BJT and OP-AMP) DC Power Supply | | | | | | 20 Hours |
| FPGA (Verilog) Multifunction Calculator | | | | | | 30 Hours |

Data science