



PimpriChinchwad Education Trust's  
**PimpriChinchwad College of Engineering**  
Sector No. 26, Pradhikaran,  
Nigdi, Pune – 411 044



**Minutes of Meeting: 1<sup>st</sup> BoS (E&TC) meeting on 5<sup>th</sup> Jan 2021**

The first meeting of the BoS (E&TC) of PimpriChinchwad College of Engineering (PCCoE) was held on Tuesday, 5<sup>th</sup> January, 2021 at 02:30 pm in LRDC Hall.

**Following members were present:**

Sr. No.	Name of Member	Category
1	Dr.M.T.Kolte , Professor Head of Department	Chairman
2	Dr.N.B.Chopade, Professor Subject Head- Embedded Systems	Subject Head
3	Dr.S.U.Bhandari, Professor Subject Head – Microelectronics	Subject Head
4	Dr.K.S.Kinage Professor Subject Head – Signal Processing	Subject Head
5	Prof.A.B.Patil, Associate Professor Subject Head – Energy Systems	Subject Head
6	Prof.A.R.Suryawanshi,, Associate Professor Subject Head - Control and Automation	Subject Head
7	Prof S L Tade, Associate Professor Subject Head – Image processing	Subject Head
8	Dr V K Harpale, Associate Professor Subject Head – PG – Embedded Systems	Subject Head
9	Dr P K Rajani Associate Professor Subject Head- Soft Computing	Subject Head
10	Dr VarshaBendre, Associate Professor Subject Head – UG VLSI	Subject Head
11	DrDeeptiKhurge, Associate Professor Subject Head – PG VLSI	Subject Head
12	Prof V A Kulkarni,, Assistant Professor Subject Head- Devices and Circuits	Subject Head
13	Prof G R Rahate Assistant Professor Subject Head - Communication	Subject Head
14	Prof Anjali Shrivastav, Assistant Professor Subject Head – Humanities and Sciences	Subject Head

Sr. No.	Name of Member	Category
15	Dr D S Bormane, Principal, AISSMS, Pune	University Nominee
16	Mr AmitBabalalNahar, Knorr-Bremse Technology Center India Private Limited, Pune	Industry Representative
17	Mr Suni Kharate C-Gate, Pune	Industry Representative
18	Dr S E Talole. Professor. Defense Institute of Advanced Technology, Pune	Subject Expert other than Parent University
19	Dr Deepak Bhoir , Professor and Head, Dept. of Electronics at Fr. Conceicao Rodrigues College of Engineering, Mumbai, Maharashtra	Subject Expert other than Parent University
20	Dr. Deepak S. Bankar, Department of Electrical Engineering, BV DUCOE, Pune	Subject Expert other than Parent University
21	Mrs.ArchanaLalge, Content Management Executive at Jugaadfunda Innovations LLP, KapilMalhar, Baner, Pune, Maharashtra 411045	PG Meritorious Alumnus
22	Dr M S Sutaone, Govt. College of Engineering, Pune	Subject Expert
23	Dr. Mrs. Subbaraman, Professor in Electronics Engineering (PG Side) WCE, Sangli	Subject Expert other than Parent University
24	Ravindra Peerannawar, Program Manager, Capgemini, Pune, India	Industry Representative
25	Mr KiranBacchav, John Deere, Pune	Industry Representative
26	Mr.TalwareYogendraVasantstromenergie Pune	Industry Representative
27	Mr. ArunJangam, Nayan Electronics, Pune	Industry Representative
28	Mrs.Pranjal Jog	Invited Member
29	Mr.P.R.Sonawane	Invited Member

**Following Permanent Invitees were present:**

1.	Academics Coordinators	Academics(Permanent Invitees)
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**Agenda for the meeting was as under:**

1. Tribute to Late Dr G.V. Parishwad (Ex-Director, PCCoE)
2. Welcome of BoS members by Chairman.
3. Discussion on UG structure
4. First year (BTech) Syllabus Discussion -Branch specific course.
5. Discussion on PG structure and First year M.Tech syllabus.
6. Any other point with the permission of chair



**01: 01 Tribute to Late Dr G.V. Parishwad (Ex-Director, PCCoE)**

*The gathering offered homage to Late Dr. G. V. Parishwad by observing a 2-minute silence.*

**01: 02 Welcome of BoS members by Chairman**

*Dr. M.T.Kolte Head E&TC and Chairman BoS welcomed the members and introduced all members of the BoS.*

**01: 03 Discussion on UG structure.**

*Discussion was initiated to finalize UG structure. Mr.G.R.Rahate presented the features of UG structure and proposed the UG structure to the members and invited discussion on the same.*

- *Mr.Kiran Bacchav said that internship in structure will be too early at SY B.Tech. level and should be for a long duration for it to be effective. If possible it should be for 6 months. Mr.Sunil Kharate supported this and added students will get awareness about industry culture.*
- *Dr.DeepakBankar mentioned whether it is advisable to keep internship at First year level. Dr S E Talole also raised the same issue. Dr. SheetalBhandari answered that student can work with research labs, PhD candidates or even with NGOs as a part of their internship. Dr. N B Chopade supported her saying that this internship will give industry exposure to them. It will also inculcate entrepreneurship skills in them and will support the AatmaNirbhar Bharat. Mr. YogendraVasantTalware, Mr.ArunJangam and Mrs.ArchanaLalge also supported the same. Dr. Deepak Bhoiralso supported this and added that it will develop social awareness among students.Dr.Shaila Subbaraman suggested semester wise evaluation of internship and MOOC courses .*
- *Dr.DeepakBankar asked if it is possible to accommodate 2 months of internship alongwith academics. Upon which Dr. Mrs.SheetalBhandari answered that in they will be having only MOOC courses in that semester. **It is resolved that internship will be offered for 6 months and to be in Sem VII and VIII with proper evaluation.***
- *Dr. S. E. Talolesuggested that a specific course related to audit course should be conducted and it should be given some weightage in the mark sheet.He also raised a query about HSMC courses. Dr.SheetalBhandari answered, saying that foreign language courses are arranged in 1st and 2nd semesters. Proficiency Development courses are proposed from 3rd semester onwards.**It was resolved that the audit course will be non-credit and two letter grade will be offered for the same in student's mark sheet on successful completion of the course.***
- *Dr Deepak Bhoir said that students must develop liking through curriculum by giving them continuous choice based electives for developing specialization. He appreciated the structure too, as it includes this. He further suggested to add industry driven courses. The curriculum of the same should be framed by industry people and should be either fully or partially delivered by them. Mr.Sunil Kharate supported the same.**It is resolved that the syllabus structure is well defined as per the need of the stake holders as it includes all the required categories of the courses with balanced weightage. The industry driven courses will also be offered progressively as system gets matured.***



#### 01: 04 First year (BTech) Syllabus Discussion -Branch specific course.

Mr.P.R.Sonawane presented the syllabus of **Basic electronics** course and invited discussion on the same.

•Mr.Ravindra Peerannawar, said that syllabus should be in line with industry expectations. It will be better to include simulation of analog circuits and reliability engineering. Dr Deepak Bhoir,Dr.Shaila Subbaraman also supported this point.

•Mr.Sunil Kharate appreciated the syllabus framed. He suggested that more focus should be given on testing of components and verification should also be included. Mr.Ravindra Peerannawar also supported this.

•Dr. S. E. Talole mentioned that some dedicated time should be allotted for prerequisites. If possible it should be in the form of courses.

•Dr.Shaila Subbaraman suggested that microelectronics should be included in first year. Students should be introduced to ICs like 555 and 741. Her suggestion was- not to include JFET but include MOSFET.

• Mr.Kiran Bacchav,said that practical exposure needs to be given to first year students.

• Dr N B Chopade suggested that sensors have been introduced to motivate the interdisciplinary projects at small levels. **It was decided to refine contents to add one simulation practical to practical list and sensorsrelated topic in unit 4 will be focusing only on working principle of sensors being very basic course for E&TC students. The prerequisite courses/topics to be added in the syllabus of the course.**

Mrs.Pranjal Jog presented the syllabus of **Basic electrical and electronics** and invited discussion on the same.

•Mr. Y.V.Talware suggested that along with power system, difference between electronics and electrical instruments should be taught. Concepts of generation of harmonics should be included. Handling of different analysers must also be introduced targeting automation and digitization.

•Outcome 2 should be framed as to determine various computer electrical parameters Mr.Sunil Kharate supported this.

•Dr Deepak Bhoir appreciated the syllabus framed. He suggested that more hands-on should be given to the students.**It is resolved that outcome 2 will be reframed as – Analyse single phase and 3 phase circuits to determine various computed electrical parameters. Other inputs will be progressively adapted.**

#### 01: 05Discussion on PG structure and First year M.Tech syllabus.

Dr. Varsha Harpale presented the structure and detailed syllabi of PG FYMTech Sem-I and invited the discussion on the same. All BOS members have gone through the proposed syllabus in details and discussion was initiated for some improvement points mentioned below.

1.Dr. S.E.Tatole suggested following points:

To add compulsory subject as a mathematical modelling and statistics.

Add Introduction to Latex in Research Methodology.

Prerequisites need to be mentioned in terms of courses.

Mooc/ Nptel Credit normalization is difficult.

The Audit Courses can be conducted using MOOCs or NPTEL

2.Mr. Sunil Kharate suggested following points:

To add topics like Data converters, Introduction to PLL, ADC, DAC and Band-gap in the

syllabus.

In the System on Chips theory, Introduction to Verilog can be added.

3. Dr. Deepak Bhoir suggested following points:

Refinement of syllabus is needed as per weightage of the hrs.

Book titled "Introduction to Semiconductor and Devices" by M. S. Tyagi is to be added in reference books.

4. Dr. Shaila Subraman also suggested following points:

Python is needed for students and add more no of VLSI electives.

CO's of all courses in UG and PG program must be mapped to NBA PO's.

All the suggestions are taken positively and can be incorporated at possible contents. Dr Deepak Bhoir Mr. Sunil Kharate all other members appreciated the syllabus framed.

**It has been resolved that,**

**1. Introduction to Latex will be added in seminar activity.**

**2. The CO's of all courses will also be mapped according to NBA PO.**

**3. Book titled "Introduction to Semiconductor and Devices" by M. S. Tyagi can be added for device modelling.**

**4. Introduction to VHDL/Verilog C concepts can be added in Skill development Lab 1 or recommend the students to select MOOC related to VHDL/Verilog C.**

**5. Policy can be framed to normalize the Mooc/NPTEL credits as per weightage of hrs.**

**6. Refinement of syllabus can be done for contents and its distribution.**

**7. The statistics is included in research methodology and the courses will be analysed for required Mathematical modelling and necessary mathematics will be added at concern courses.**

The meeting ended with vote of thanks, proposed by Dr.M.T.Kolte.

**Prepared by:**

Dr. Varsha Harpale (PG section)

Mr. G. R. Rahate (UG section)



**Dr. M. T. Kolte**  
**Chairman –BoS(E&TC)**