

CprE 381, Computer Organization and Assembly Level Programming

Team Contract – Project Part 2

Project Teams Group #: Proj2 6 4

Team Members: Kevin Dickey

Brock Dykhuis

Owen Parker

Discuss the following aspects of teamwork with your team – make sure to get input from each member. Write down your team’s consensus for each of the bolded headings. Italicized text contains instructions and examples and should be deleted once you’ve read it. Please see the example contract for rough length expectations.

Course Goals: *List and acknowledge the goals of your individual team members.*

Examples may include:

- *learn everything about computer architecture*
- *know enough to understand security risks posed by hardware primitives*
- *get an A/B/C/Pass in the course*
- *minimize the number of lost points*
- *prepare myself for a career in hardware design*
- *prepare myself to be able to do research involving FPGAs*
- *be able to explain the workings of a stored-program computer from gates to C*

Team Expectations:

- **Conduct:**
- Work on and complete assigned parts in a timely manner
- Keep violence to a minimum (this is a joke we will not engage in violence)
- **Communication:**
- Snapchat group is setup for communication, we also share a few classes together.
- Messages should be seen within a day or so and responses similar time frame. Just a response to acknowledge the message is fine.
- We see each other 5 days a week so responses shouldn’t be an issue.
- **Group conventions:**
- I for input, s for signal, o for output
- Keep vhdL file names easy to understand, if a component is designed for a specific bit-width include that in the name of the file separated with a “_”
- Document really confusing things when necessary.

- If updating a file make note of it in the header comments of the file.

- **Meetings:**

- We will work together in lab on the project. Outside of lab, we may meet in person to work on it, though we may also work on it online collaboratively or not.
- Our meeting times are roughly 3:05pm Monday/Wednesday to 4:05pm.

- **Peer Evaluation Criteria:**

- Work contributed
- Completed assigned work on time
- Good communication

Being a chill viber

Role Responsibilities: Complete the following planning table. Each lab part should be the responsibility of one team member. Also make sure that no one team member is the lead on both the design and test aspects of a single lab part. These guidelines aid in all students having a complete view of the lab. Note that the non-lead is encouraged to participate and support the lead wherever possible, increasing both the quality of the lab part and each team member's knowledge.

Lab Part		Estimated Time	Design		Test	
			Lead	Deadline	Lead	Deadline
Software-Scheduled Pipeline	Control Signals	0.5 hr	Owen	Nov 10	Owen	End
	Datapath	3 hr	Owen	Nov 10	Owen	End
	Testing	3 hr	All	Nov 10	All	End
	Synthesis (human effort)	0.5 hr	Kevin	Nov 10	Kevin	End
Hardware-Scheduled Pipeline	Pipeline Register Update	1 hr	Owen	Nov 15	Owen	End
	Data Hazard Avoidance	4 hr	Brock	Nov 10	Brock	End
	Control Hazard Avoidance	2-6 hr based on group size	All	Nov 10	All	End
	Integration (Hardware-Schedule Pipeline)	3 hr	Kevin	Nov 10	Kevin	End
	Testing	3 hr	All	Nov 17	All	End
	Synthesis	0.5 hr	Kevin	Nov 17	Kevin	End

Estimated Time is given as a **very rough** guide for even distribution of tasks assuming you've already read through the lab document and have the prerequisite knowledge. Please note that to be done properly, the test programs will require significant time investment, but will result in a much stronger final design.

Integrity of Work: Do not delete the following. We agree that the work we provide to other team members and ultimately submit for a grade is a direct result of our own work as described in the course syllabus. Specifically, we will generate all VHDL code

ourselves and not copy VHDL code from online sources, other groups, book companion material, or past student projects to which anyone outside of my team has contributed.

Student Signature Kenin Dickey

Date 10/26/2023

Student Signature Brock Dykhuis

Date 10/26/2023

Student Signature Owen Parker

Date 10/26/2023