REMOVE THE JUMPER AT JP3 THAT IS CONNECTING PIN 1 TO 2 AND MOVE IT TO CONNECT FROM PIN 2 TO PIN 3. THIS WILL ALLOW THE SIGNAL 'RA3' TO WORK PROPERLY. DEVELOPMENT BOARD +5V R1 R2 R3 R4 SW1 U1 SW DIP-4 **D1** OSC2/CLKOUT RA0/AN0 RAU/ANU RA1/AN1 RA2/AN2/VREF-/CVREF RA3/AN3VREF+ RA4/T0CKIC1OUT RA5/AN4/SS/HLVDIN/C2OUT RB0/INT0/FLT0/AN12 RB1/INT1/AN10 RB2/INT2/AN8 RB3/AN9/CCP2 RB4/KBI0/AN11 38 LED_RGB COMMON CATHODE RB6/KBI2/PGC RB7/KBI3/PGD 40 RB5/KBI1/PGM LOGIC ANALYZER PROBE USB_POWER RC0/T1OSO/T13CKI RC1/T1OSI/CCP2 RC2/CCP1/P1A LED2 // LED RC3/SCK/SCL RC4/SDI/SDA RC5/SDO RC6/TX/CK RC7/RX/DT 130 MCLR/Vpp OSC1/CLKIN/RA7 LED4 // LED RC7/RX/DT HEADER 5X2 RD0/PSP0 RD1/PSP1 RD2/PSP2 RD3/PSP3 Notes: Make sure to connect pins 9 and 10 of logic analyzer probe to the board's ground HEADER 6 RINTZ RINT8 RINT9 RD3/PSP3 RD4/PSP4 RD5/PSP5/P1B RD6/PSP6/P1C RD7/PSP7/P1D RE0/RD/AN5 RE1/WR/AN6 RE2/CS/AN7 PIC18F4620 D2 LED_RGB COMMON CATHODE

ECE3301L SESSION 3 LAB 3