REMOVE THE JUMPER AT JP3 THAT IS CONNECTING PIN 1 TO 2 AND MOVE IT TO CONNECT FROM PIN 2 TO PIN 3. THIS WILL ALLOW THE SIGNAL 'RA3' TO WORK PROPERLY. DEVELOPMENT BOARD +5V SW1 U1 SW DIP-4 90 **D1** OSC2/CLKOUT RA0/AN0 RA1/AN1 RA2/AN2/VREF-/CVREF RA3/AN3VREF+ RA4/T0CKIC1OUT RA5/AN4/SS/HLVDIN/C2OUT RB0/INT0/FLT0/AN12 RB1/INT1/AN10 RB2/INT2/AN8 LED_RGB COMMON CATHODE RB3/AN9/CCP2 RB4/KBI0/AN11 38 RB5/KBI1/PGM 39 to LA1 RB6/KBI2/PGC RB7/KBI3/PGD 40 LOGIC ANALYZER PROBE RINT3 RINT4 RINT5 USB_POWER RC0/T1OSO/T13CKI RC1/T1OSI/CCP2 RC2/CCP1/P1A RC3/SCK/SCL 130 MCLR/Vpp OSC1/CLKIN/RA7 RC4/SDI/SDA RC5/SDO RC6/TX/CK RC7/RX/DT RD0/PSP0 RD1/PSP1 RD2/PSP2 20 21 RD2/PSP2 22 HEADER 5X2 Notes: Make sure to connect pins 9 and 10 of logic analyzer probe to the board's ground HEADER 6 RD3/PSP3 22 RD4/PSP4 28 RD5/PSP5/P1B RD6/PSP6/P1C RD7/PSP7/P1D 30 RE0/RD/AN5 RE1/WR/AN6 RE2/CS/AN7 Vss PIC18F4620 LED_RGB COMMON CATHODE ECE3301L SESSION 3 LAB 2