**Inputs/Outputs:**

* Digital pin 11 to datapin of shift register A
* Digital pin 12 to clock pin of shift register A
* Digital pin 8 to latch pin of shift register A
  + 0: DP
  + 1: G
  + 2: F
  + 3: E
  + 4: D
  + 5: C
  + 6: B
  + 7: A
* Digital pin 6 to datapin of shit register C (Blue)
* Digital pin 12 to clock pin of shift register C (Yellow)
* Digital pin 7 to latch pin of shift register C (Green)

**Shift Register C**

* Q0: Score left decrement
* Q1: Score right decrement
* Q2: Score left indicator
* Q3: Score right indicator
* Q4: Penalty left 1
* Q5: Penalty left 2
* Q6: Penalty right 1
* Q7: Penalty right 2
* Digital pin 1 and 2 to binary configuration
* Digital pin 3 to Penalty and Score button LED
* Analog 0 to sensor left
* Analog 1 to sensor right
* Score pin 2
* Penalty pin 3