**Inputs/Outputs:**

* Digital pin 11 to datapin of shift register A
* Digital pin 12 to clock pin of shift register A
* Digital pin 9 to latch pin of shift register A
  + SReg: LED: Pin
  + 0: DP: 5
  + 1: G: 10
  + 2: F: 9
  + 3: E: 1
  + 4: D: 2
  + 5: C: 4
  + 6: B: 6
  + 7: A: 7
* Digital pin 6 to datapin of shit register C (Blue)
* Digital pin 12 to clock pin of shift register C (Yellow)
* Digital pin 7 to latch pin of shift register C (Green)

**Shift Register C**

* Q0: --
* Q1: --
* Q2: Score left indicator
* Q3: Score right indicator
* Q4: Penalty left 1
* Q5: Penalty left 2
* Q6: Penalty right 1
* Q7: Penalty right 2
* Digital pin 4 and 5 to binary configuration
* Digital pin 3 to Penalty and Score button LED
* Analog 0 to sensor left
* Analog 1 to sensor right
* Score pin 2
* Penalty pin 3

Pinot: