This diagram shows an overview of the build process
implemented with the Makefile.

For example, to generate "etherload.prg" file, the
"etherload.a65" file is passed into the "Ophis" compiler.

The ".incBIN" denotes that the lower file 'includes' the
upper file, and the upper file is inserted into the lower
file in a binary format.
Similarly:
the ".include" denotes that the lower file 'includes' the
upper file, and the upper file is inserted into the lower
file in a text-based format.

The "creates" suggests that the upper file 'generates'
the lower file.

The "Choose..." suggests that one of the upper files is
chosen to be "included" in the lower file.
Both the upper files are compiled by default.
The lower file is modified to select which of the two
upper files to include.

The "pngprepare" executable is built from source. Details
of this are not provided (refer to the Makefile).
The "pngprepare" file takes the upper file and generates
the lower file.

A "Python Script" is used to generate one lower file from
two upper files.

etherload.a65

Ophis

etherload.prg   diskmenu.a65

.incBIN   .incBIN   .include   .include

diskchooser.a65   diskmenuc000.a65   diskmenuprg.a65

Ophis   Ophis   Ophis

diskchooser   diskmenu_c000.bin   diskmenu.prg

Choose ONE or   kickstart_task.a65
the OTHER
                       kickstart_mem.a65

version.sh   kickstart_dos.a65

creates   creates   kickstart_machine.a65

                                                kickstart_process_descriptor.a65
version.vhdl   version.a65

.include   .incBIN   .include

kickstart.a65

Ophis

mega65_64x64.png   8x8font.png   kickstart65gs.bin   rom_template.vhdl

./pngprepare   ./pngprepare   Python Script
                                              (./makerom)

BOOTLOGO.G65   charrom.vhdl   kickstart.vhdl

                                              } Used by Xilinx ISE
                                                to be included within
                                                the FPGA's bitstream.

                       To be copied onto the SD-Card.
                       This image is displayed
                       during the boot process.