



DUT Power off:
MCU GPIO:
/WE_33 = 1
/CS_33 = 1
/OE_33 = 1
/VPPFON_33 = 1
VCCON_33 = 0
SKT28_33 = 0
SKTEE_33 = 0
BZY = output = 0
Write all latches to "0"
Wait 100ms
LAT_OE = 1
This will HI-Z all of the HC573 signals

DUT Power on:
MCU GPIO:
BZY = input
VCCON_33 = 1
LAT_OE = 0

Variable pin configurations for EPROMS:
1: VPP/VPPF/A18
3: VPP/A15/BZY/RST
22: /CE/PGM
24: /OE/VPP
30: VCC/A17
31: A18/PGM

S/N: 001
VPPF 4.749 0.796
VPPF MON 11.925 1.987

Title			FF PGMR-II		
Size	Number		Revision		-
Tabloid					
Date:	6/14/2024		Sheet	of	
File:	C:\Users\...\pgmr2.SchDoc		Drawn By:		