

6.2.6 MRAPI Requirements Implications

- Fast locks supporting multiple writers and a single reader are required; maximum lock rate << 6ms on 800mhz core would be typical
- Locks must work transparently whether they are uni-core or multi-core
- Ability to select shared memory region based on attribute: SRAM
- Ability to select shared memory region based on attribute: logical == physical
- Ability to select shared memory region based on attribute: no MMU overhead (other than initial page entry set up if required)

6.2.7 Mental Models

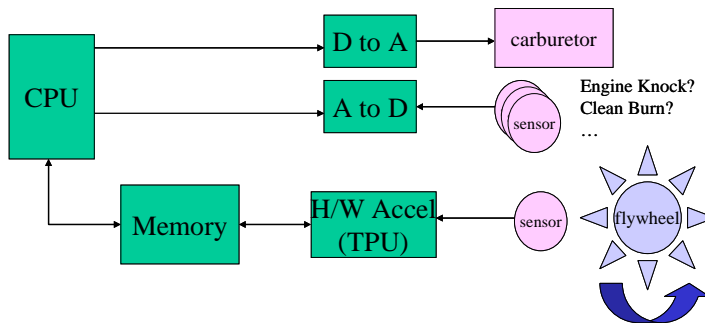


Figure 1 – example hardware

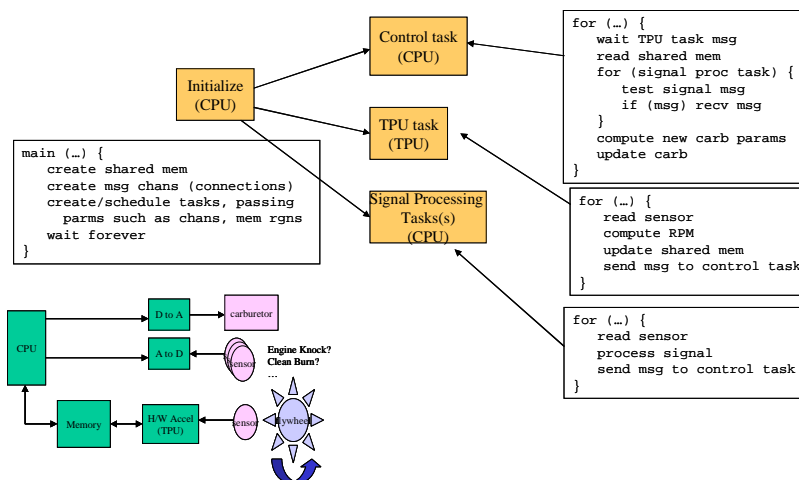


Figure 2 – A possible mapping