

BCD TO COMMON ANODE 7-SEGMENT DECODER												
DEC	SEGMENT	SW_W	SW_X	SW_Y	SW_Z	A	B	C	D	E	F	G
	PIN	RB1	RB6	RB3	RA2	RA1	RB4	RB5	RB2	RB7	RA0	RBO
0		0	0	0	0	0	0	0	0	0	0	1
1		0	0	0	1	1	0	0	1	1	1	1
2		0	0	1	0	0	0	1	0	0	1	0
3		0	0	1	1	0	0	0	0	1	1	0
4		0	1	0	0	1	0	0	1	1	0	0
5		0	1	0	1	0	1	0	0	1	0	0
6		0	1	1	0	0	1	0	0	0	0	0
7		0	1	1	1	0	0	0	1	1	1	1
8		1	0	0	0	0	0	0	0	0	0	0
9		1	0	0	1	0	0	0	0	1	0	0
A		1	0	1	0	0	0	0	1	0	0	0
b		1	0	1	1	1	1	0	0	0	0	0
C		1	1	0	0	0	1	1	0	0	0	1
d		1	1	0	1	1	0	0	0	0	1	0
E		1	1	1	0	0	1	1	0	0	0	0
F		1	1	1	1	0	1	1	1	0	0	0

INITIALIZATION									HEX
PORTA	-	-	-	<i>RA4</i>	<i>RA3</i>	<i>RA2</i>	<i>RA1</i>	<i>RA0</i>	0x04
	0	0	0	0	0	1	0	0	
PORTB	<i>RB7</i>	<i>RB6</i>	<i>RB5</i>	<i>RB4</i>	<i>RB3</i>	<i>RB2</i>	<i>RB1</i>	<i>RB0</i>	0x4A
	0	1	0	0	1	0	1	0	

I/O CONFIGURATIONS									BIN	HEX
ZERO	-	-	-	RA4	RA3	RA2	RA1	RA0	11100	1C
	0	0	0	1	1	1	0	0		
	RB7	RB6	RB5	RB4	RB3	RB2	RB1	RB0		
	0	1	0	0	1	0	1	1		
ONE	-	-	-	RA4	RA3	RA2	RA1	RA0	11111	1F
	0	0	0	1	1	1	1	1		
	RB7	RB6	RB5	RB4	RB3	RB2	RB1	RB0		
	1	1	0	0	1	1	1	1		
TWO	-	-	-	RA4	RA3	RA2	RA1	RA0	11001111	CF
	0	0	0	1	1	1	0	1		
	RB7	RB6	RB5	RB4	RB3	RB2	RB1	RB0		
	0	1	1	0	1	0	1	0		
THREE	-	-	-	RA4	RA3	RA2	RA1	RA0	11101	1D
	0	0	0	1	1	1	0	1		
	RB7	RB6	RB5	RB4	RB3	RB2	RB1	RB0		
	1	1	0	0	1	0	1	0		
FOUR	-	-	-	RA4	RA3	RA2	RA1	RA0	11110	1E
	0	0	0	1	1	1	1	0		
	RB7	RB6	RB5	RB4	RB3	RB2	RB1	RB0		
	1	1	0	0	1	1	1	0		
FIVE	-	-	-	RA4	RA3	RA2	RA1	RA0	11100	1C
	0	0	0	1	1	1	0	0		
	RB7	RB6	RB5	RB4	RB3	RB2	RB1	RB0		
	1	1	0	1	1	0	1	0		
SIX	-	-	-	RA4	RA3	RA2	RA1	RA0	11100	1C
	0	0	0	1	1	1	0	0		
	RB7	RB6	RB5	RB4	RB3	RB2	RB1	RB0		
	0	1	0	1	1	0	1	0		
SEVEN	-	-	-	RA4	RA3	RA2	RA1	RA0	11101	1D
	0	0	0	1	1	1	0	1		
	RB7	RB6	RB5	RB4	RB3	RB2	RB1	RB0		
	1	1	0	0	1	1	1	1		
EIGHT	-	-	-	RA4	RA3	RA2	RA1	RA0	11100	1C
	0	0	0	1	1	1	0	0		
	RB7	RB6	RB5	RB4	RB3	RB2	RB1	RB0		
	0	1	0	0	1	0	1	0		
NINE	-	-	-	RA4	RA3	RA2	RA1	RA0	11100	1C
	0	0	0	1	1	1	0	0		
	RB7	RB6	RB5	RB4	RB3	RB2	RB1	RB0		
	1	1	0	0	1	0	1	0		
A	-	-	-	RA4	RA3	RA2	RA1	RA0	11100	1C
	0	0	0	1	1	1	0	0		
	RB7	RB6	RB5	RB4	RB3	RB2	RB1	RB0		
	0	1	0	0	1	1	1	0		
b	-	-	-	RA4	RA3	RA2	RA1	RA0	11110	1E
	0	0	0	1	1	1	1	0		
	RB7	RB6	RB5	RB4	RB3	RB2	RB1	RB0		
	0	1	0	1	1	0	1	0		
C	-	-	-	RA4	RA3	RA2	RA1	RA0	11100	1C
	0	0	0	1	1	1	0	0		
	RB7	RB6	RB5	RB4	RB3	RB2	RB1	RB0		
	0	1	1	1	1	0	1	1		
d	-	-	-	RA4	RA3	RA2	RA1	RA0	11111	1F
	0	0	0	1	1	1	1	1		
	RB7	RB6	RB5	RB4	RB3	RB2	RB1	RB0		
	0	1	0	0	1	0	1	0		
E	-	-	-	RA4	RA3	RA2	RA1	RA0	11100	1C
	0	0	0	1	1	1	0	0		
	RB7	RB6	RB5	RB4	RB3	RB2	RB1	RB0		
	0	1	1	1	1	0	1	0		
F	-	-	-	RA4	RA3	RA2	RA1	RA0	11100	1C
	0	0	0	1	1	1	0	0		
	RB7	RB6	RB5	RB4	RB3	RB2	RB1	RB0		
	0	1	1	1	1	1	1	0		