

CSC 350 Project 2 Proposal: Simulation of the Intel 8008

Keanu Enns - keanelekenns@uvic.ca

Neal Manning - nmanning@uvic.ca

Nat Comeau - ncomeau@uvic.ca

Louis Kedziora - kedziora@uvic.ca

- a) Proposed Topic:
 - Simulation of the Intel 8008
- b) What the group intends to do:
 - Simulate a significant subset of the Intel 8008 (approximately half of the Instruction Set) in C running on the linux.csc.uvic.ca server. We plan to implement a simple memory and I/O system to facilitate testing.
- c) How the group intends to go about doing it:
 - First, read the Intel 8008 manual. We'll meet the week of March 10th to plan the software architecture of our simulator.
 - The group will then implement the following virtual CPU components:
 - ALU
 - Timing
 - Instruction Decode and control
 - I/O
 - Accumulator, data registers, program counter, stack
- d) Proposed deliverable:
 - A Simulator for the Intel 8008 that is able to execute 3 assembly language test functions we will write.
- e) Initial responsibilities:
 - Everyone read the manual- Week of March 11th
 - Virtual component drafts (C file)- Week of March 18th
 - Each group member implements one test function in assembly- Week of March 18th
 - Each group member implements decode of ~5 instructions from the instruction set- Week of March 25th
 - Testing and Documentation- Week of March 25th