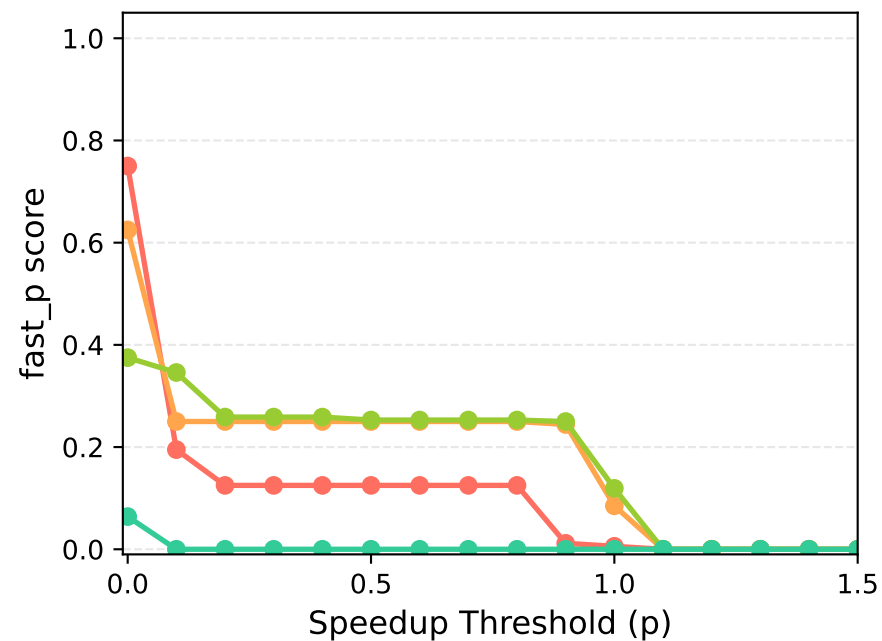
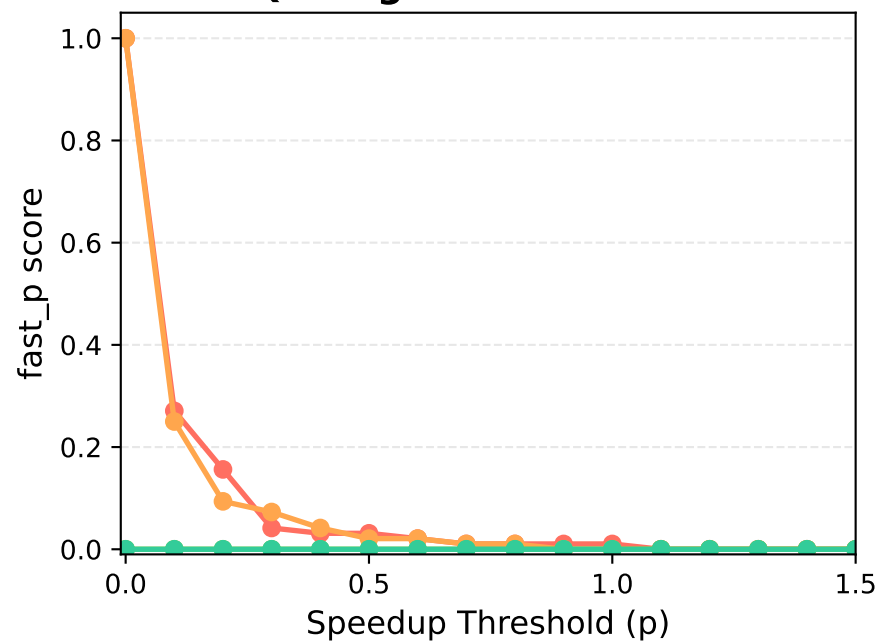


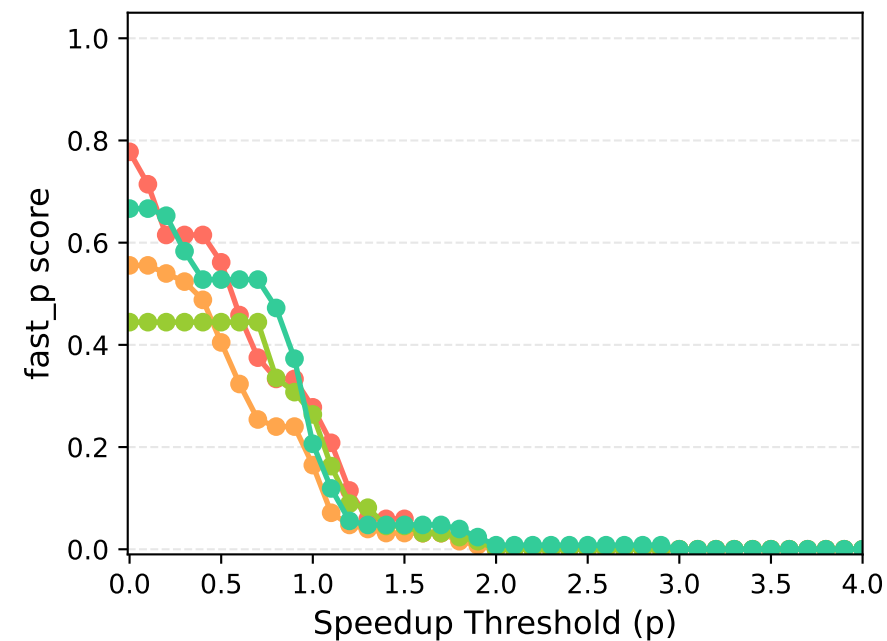
**GEMM CUDA**



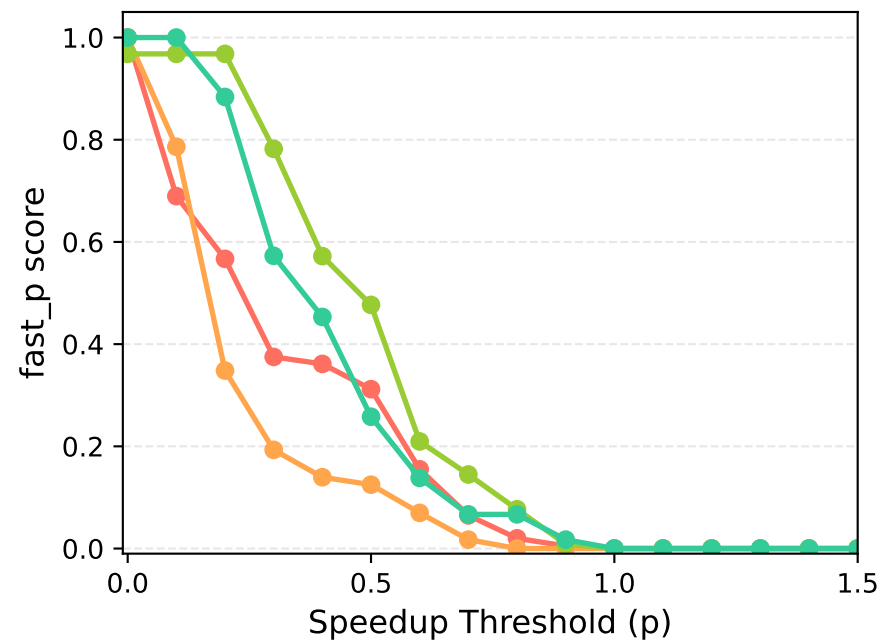
**GQA Paged Decode CUDA**



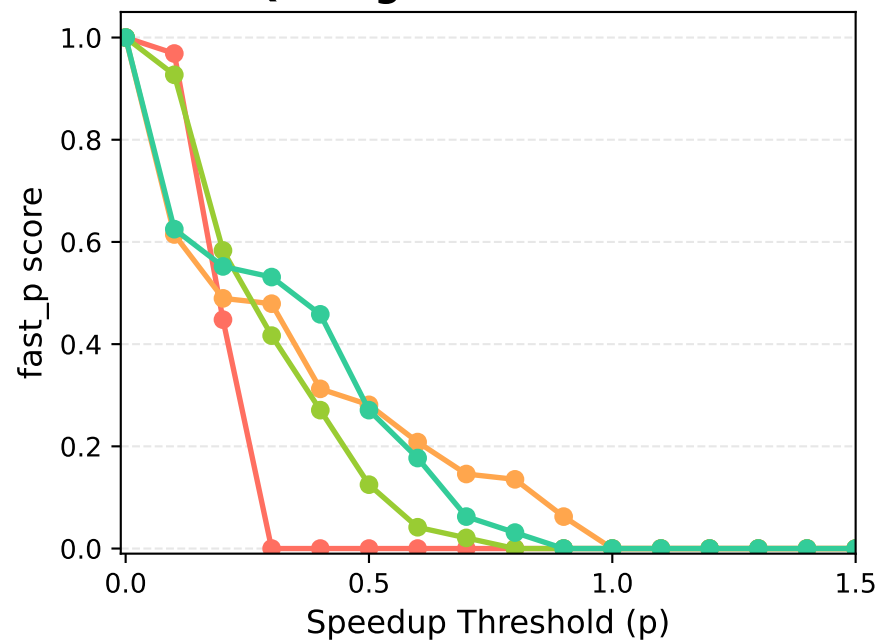
**RMSNorm CUDA**



**GEMM Triton**



**GQA Paged Decode Triton**



**RMSNorm Triton**

