



1. Description

1.1. Project

Project Name	stm32_config
Board Name	custom
Generated with:	STM32CubeMX 6.10.0
Date	01/30/2024

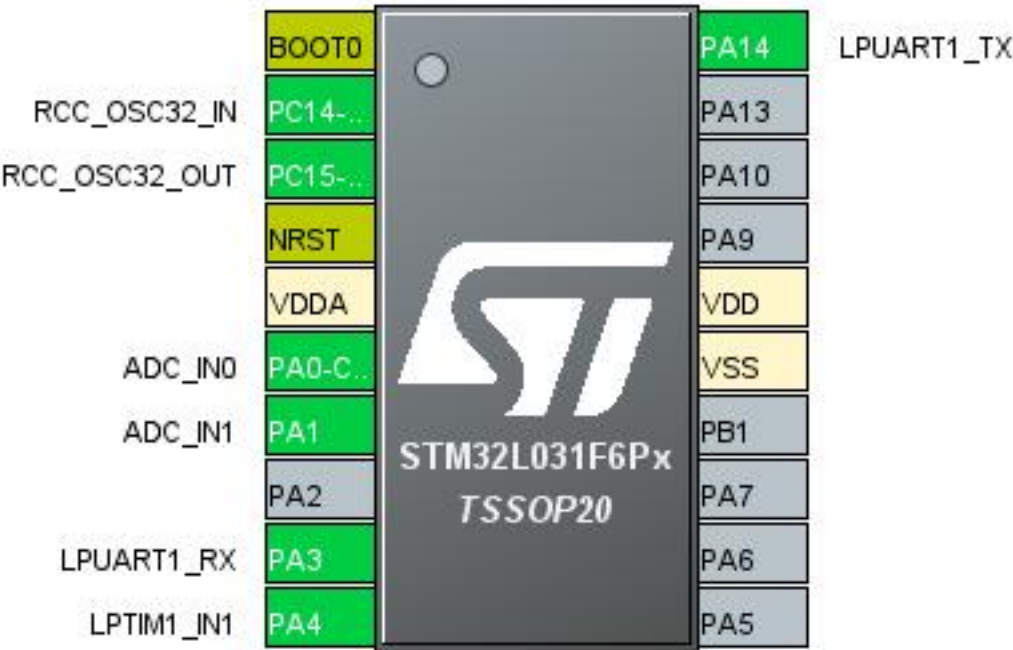
1.2. MCU

MCU Series	STM32L0
MCU Line	STM32L0x1
MCU name	STM32L031F6Px
MCU Package	TSSOP20
MCU Pin number	20

1.3. Core(s) information

Core(s)	Arm Cortex-M0+
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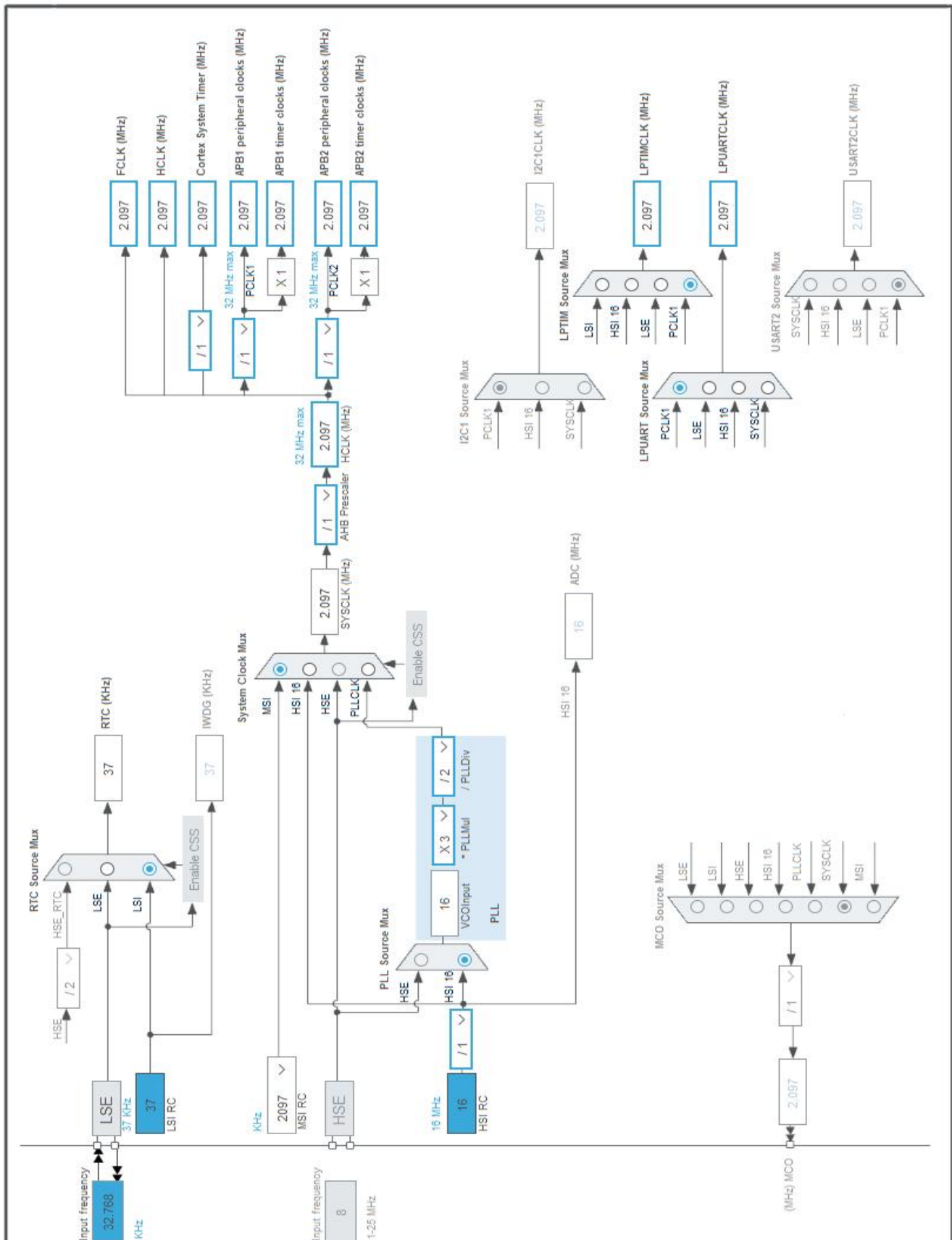
2. Pinout Configuration



3. Pins Configuration

Pin Number TSSOP20	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	BOOT0	Boot		
2	PC14-OSC32_IN	I/O	RCC_OSC32_IN	
3	PC15-OSC32_OUT	I/O	RCC_OSC32_OUT	
4	NRST	Reset		
5	VDDA	Power		
6	PA0-CK_IN	I/O	ADC_IN0	
7	PA1	I/O	ADC_IN1	
9	PA3	I/O	LPUART1_RX	
10	PA4	I/O	LPTIM1_IN1	
15	VSS	Power		
16	VDD	Power		
20	PA14	I/O	LPUART1_TX	

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

Name	Value
Project Name	stm32_config
Project Folder	C:\Users\kecaj\Desktop\svn\STM32_ESP32_pulse_counter\software\stm32_conf
Toolchain / IDE	EWARM V8.32
Firmware Package Name and Version	STM32Cube FW_L0 V1.12.2
Application Structure	Advanced
Generate Under Root	No
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_ADC_Init	ADC
4	MX_LPTIM1_Init	LPTIM1
5	MX_LPUART1_UART_Init	LPUART1
6	MX_RTC_Init	RTC
7	MX_WWDG_Init	WWDG

1. Power Consumption Calculator report

1.1. Microcontroller Selection

Series	STM32L0
Line	STM32L0x1
MCU	STM32L031F6Px
Datasheet	DS10668_Rev4

1.2. Parameter Selection

Temperature	25
Vdd	3.0

1.3. Battery Selection

Battery	Li-SOCL2(AAA700)
Capacity	700.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	10.0 mA
Max Pulse Current	30.0 mA
Cells in series	1
Cells in parallel	1

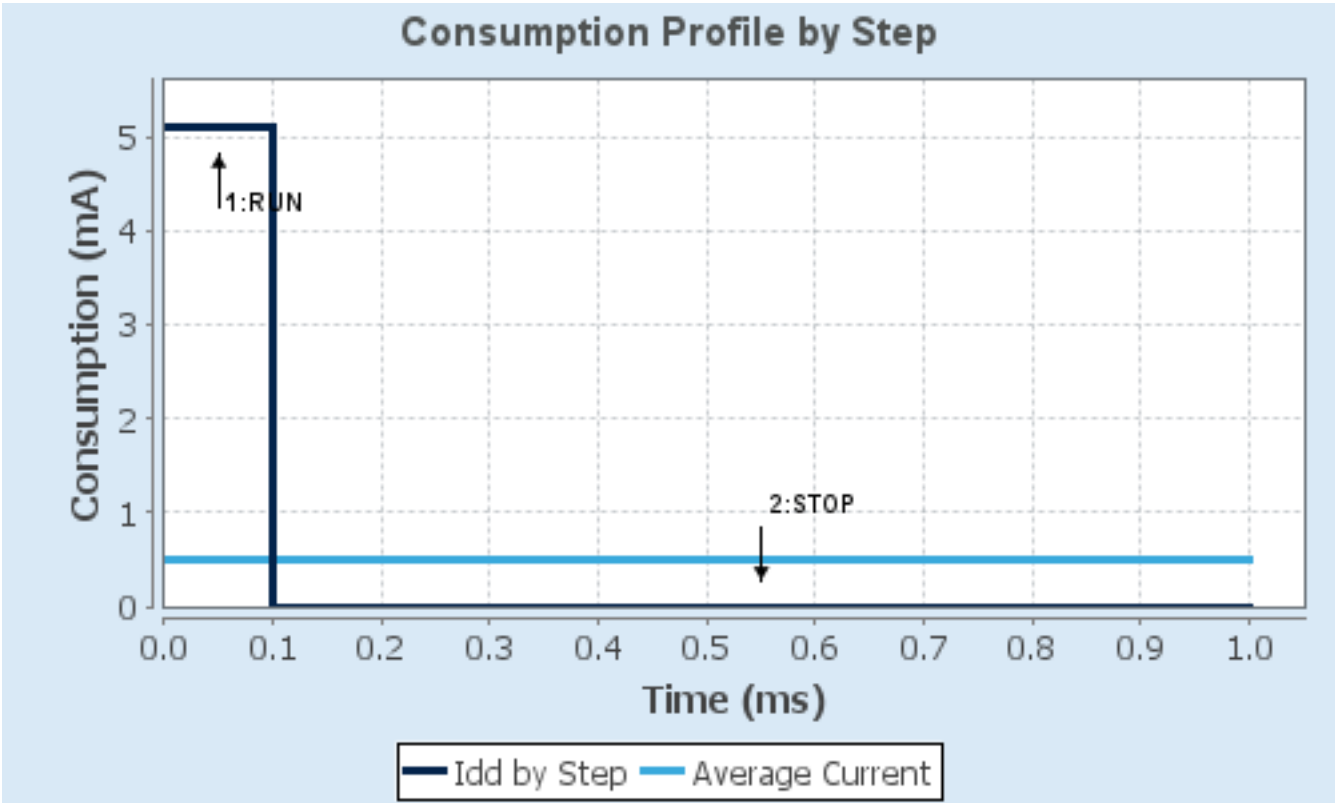
1.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP
Vdd	3.0	3.0
Voltage Source	Battery	Battery
Range	Range1-High	NoRange
Fetch Type	FLASH	n/a
CPU Frequency	32 MHz	0 Hz
Clock Configuration	HSI PLL	ALL CLOCKS OFF
Clock Source Frequency	16 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	5.1 mA	380 nA
Duration	0.1 ms	0.9 ms
DMIPS	30.0	0.0
Ta Max	104.08	105
Category	In DS Table	In DS Table

1.5. Results

Sequence Time	1 ms	Average Current	510.34 μ A
Battery Life	1 month, 26 days, 15 hours	Average DMIPS	30.4 DMIPS

1.6. Chart



2. Peripherals and Middlewares Configuration

2.1. ADC

mode: IN0

mode: IN1

mode: Temperature Sensor Channel

2.1.1. Parameter Settings:

ADC_Settings:

Clock Prescaler	Synchronous clock mode divided by 1
Resolution	ADC 12-bit resolution
Data Alignment	Right alignment
Scan Direction	Forward
Continuous Conversion Mode	Disabled
Discontinuous Conversion Mode	Disabled
DMA Continuous Requests	Disabled
End Of Conversion Selection	End of single conversion
Overrun behaviour	Overrun data preserved
Low Power Auto Wait	Disabled
Low Frequency Mode	Enabled
Auto Off	Disabled
Oversampling Mode	Disabled

ADC_Regular_ConversionMode:

Sampling Time	1.5 Cycles
External Trigger Conversion Source	Regular Conversion launched by software
External Trigger Conversion Edge	None

WatchDog:

Enable Analog WatchDog Mode	false
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2.2. LPTIM1

Mode: Counts external Clock events with internal synchronization

2.2.1. Parameter Settings:

Clock:

ULP Clock Polarity	Rising Edge
ULP Clock Sample Time	Direct Transition

Preload:

Update Mode	Update Immediate
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Trigger:

Trigger Source

Software Trigger

2.3. LPUART1

Mode: Asynchronous

2.3.1. Parameter Settings:

Basic Parameters:

Baud Rate	209700
Word Length	7 Bits (including Parity)
Parity	None
Stop Bits	1

Advanced Parameters:

Data Direction	Receive and Transmit
Single Sample	Disable

Advanced Features:

TX Pin Active Level Inversion	Disable
RX Pin Active Level Inversion	Disable
Data Inversion	Disable
TX and RX pins Swapping	Disable
Overrun	Enable
DMA on RX Error	Enable
MSB First	Disable

2.4. RCC

Low Speed Clock (LSE) : Crystal/Ceramic Resonator

2.4.1. Parameter Settings:

System Parameters:

VDD voltage (V)	3.3
Buffer Cache	Enabled
Prefetch	Disabled
Preread	Enabled
Flash Latency(WS)	0 WS (1 CPU cycle)

RCC Parameters:

HSI Calibration Value	16
MSI Calibration Value	0
HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

Power Parameters:

Power Regulator Voltage Scale

Power Regulator Voltage Scale 1

2.5. RTC

mode: Activate Clock Source

WakeUp: Internal WakeUp

2.5.1. Parameter Settings:

General:

Hour Format

Hourformat 24

Asynchronous Predivider value

127

Synchronous Predivider value

255

Wake UP:

Wake Up Clock

RTCCLK / 16

Wake Up Counter

0

2.6. SYS

Timebase Source: SysTick

2.7. WWDG

mode: Activated

2.7.1. Parameter Settings:

Watchdog Clocking:

WWDG counter clock prescaler

1

WWDG window value

64

WWDG free-running downcounter value

64

Watchdog Interrupt:

Early wakeup interrupt

Disable

*** User modified value**

3. System Configuration

3.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
ADC	PA0-CK_IN	ADC_IN0	Analog mode	No pull-up and no pull-down	n/a	
	PA1	ADC_IN1	Analog mode	No pull-up and no pull-down	n/a	
LPTIM1	PA4	LPTIM1_IN1	Alternate Function Push Pull	No pull-up and no pull-down	Low	
LPUART1	PA3	LPUART1_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PA14	LPUART1_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
RCC	PC14-OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	
	PC15-OSC32_OUT	RCC_OSC32_OUT	n/a	n/a	n/a	

3.2. DMA configuration

nothing configured in DMA service

3.3. NVIC configuration

3.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable Interrupt	true	0	0
Hard fault interrupt	true	0	0
System service call via SWI instruction	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	3	0
Window watchdog interrupt	unused		
PVD interrupt through EXTI line 16	unused		
RTC global interrupt through EXTI lines 17, 19 and 20 and LSE CSS interrupt through EXTI line 19	unused		
Flash and EEPROM global interrupt	unused		
RCC global interrupt	unused		
ADC, COMP1 and COMP2 interrupts (COMP interrupts through EXTI lines 21 and 22)	unused		
LPTIM1 global interrupt / LPTIM1 wake-up interrupt through EXTI line 29	unused		
LPUART1 global interrupt / LPUART1 wake-up interrupt through EXTI line 28	unused		

3.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable Interrupt	false	true	false
Hard fault interrupt	false	true	false
System service call via SWI instruction	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true

* User modified value

4. System Views

4.1. Category view

4.1.1. Current

Middleware				
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System Core	Analog	Timers	Connectivity	Computing
DMA	ADC	LPTIM1	LPUART1	
GPIO		RTC		
IVIC				
RCC				
SYS				
WWDG				

5. Docs & Resources

Type	Link
IBIS models	https://www.st.com/resource/en/ibis_model/stm32l0_ibis.zip
System View Description	https://www.st.com/resource/en/svd/stm32l0_svd.zip
Presentations	https://www.st.com/resource/en/product_presentation/gt_stm32f0-l0.pdf
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Application Notes for related Tools & Software	https://www.st.com/resource/en/application_note/an4865-low-power-timer-lptim-applicative-use-cases-on-stm32-mcus-and-mpus-stmicroelectronics.pdf
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Application Notes for related Tools & Software	https://www.st.com/resource/en/application_note/an5126-how-to-calibrate-internal-oscillators-on-stm32g0-mcus-stmicroelectronics.pdf
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