Kedar **Mahale**

GRADUATE ENGINEER

Bengaluru, India

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Summary

Hard-working and sincere individual, picks up required skills fast, a team-player.

Skills

Languages : C, C++, Verilog, Java, Python
Software : MATLAB, Cadence Virtuoso, Xilinx Vitis, Xilinx Vivado

Experience

Saankhya Labs Pvt. Ltd. — *Bengaluru, Karnataka*

Member of technical staff (Embedded Software Developer) — *Oct. 2020 - Present*

- Working in the 5G RU team as an embedded C developer with emphasis on ORAN front-haul interface.
- Embedded software development for Arm processors such as A53, R5 (baremetal, freeRTOS).
- Development of PA control and monitoring at the RF frontend.
- Developed test modules for fault management at fronthaul interface - worked in C, Python, Cython.

SIEMENS Energy Automation Ltd. — *Verna, Goa*

Intern — *May. 2018 - Jun. 2018*

- Worked with the incoming quality control team, also got an overall overview of the manufacturing process in PCB industry.

Education

National Institute of Technology Goa — *Ponda, Goa*

Bachelor of Technology in Electronics and Communication Engineering, CGPA: 8.52/10 — *Aug. 2016 - Jun. 2020*

Deepvihar Higher Secondary School — *Headland-Sada, Goa*

Higher Secondary School Certificate, Percentage: 89.19% — *Jun. 2014 - Apr. 2016*

St. Andrew's Institute — *Vasco, Goa*

Secondary School Certificate, Percentage: 91.67% — *Jun. 2008 - Apr. 2014*

Projects

Marine VHF Radio Receiver

B.Tech. Project — *Aug. 2019 - Jun. 2020*

- 1.8 V, 180 nm radio receiver operating in the 156MHz - 174MHz (Marine VHF) band.
- Design of a low noise amplifier.
  -Gain of 14 dB at 165 MHz, integrated noise-figure of 4.0 dB(156-175MHz), 21 mW of power.
- Design of an active mixer with current injection.
  -Conversion gain of 14 dB, noise figure 27 dB at 15 MHz.
- Design of a ring oscillator with quadrature outputs tuned to 150 MHz.

Simulation of a 2-user CDMA system

MATLAB simulation — *Mar. 2019 - Apr. 2019*

- Design and simulation of a 2-user 4-bit CDMA communication system.

Dynamic Range Radio frequency jammer

Mini project — *Mar. 2018 - Apr. 2018*

- Design and implementation of radio frequency jammer in 89 - 105 MHz band.

Workshops

Internet of Things — *NIT Goa*

2 Day workshop on an introduction to internet of things — *Oct. 2016*

Accelorobitics — *IIT Bombay*

2 Day workshop on an introduction to acclerometer and its working with arduino — *Dec. 2018*

Hobbies and other interests

Photography, photo editing, travelling, trekking, also interested in astronomy, maths, statistics and physics.

References

Dr. Trilochan Panigrahi — *NIT-Goa*

Associate Professor, Department of Electronics and Communication Engineering — *tpanigrahi@nitgoa.ac.in*

Dr. Badri Narayan Subudhi — *IIT-Jammu*

Assistant Professor, Department of Electrical Engineering — *subudhi.badri@iitjammu.ac.in*