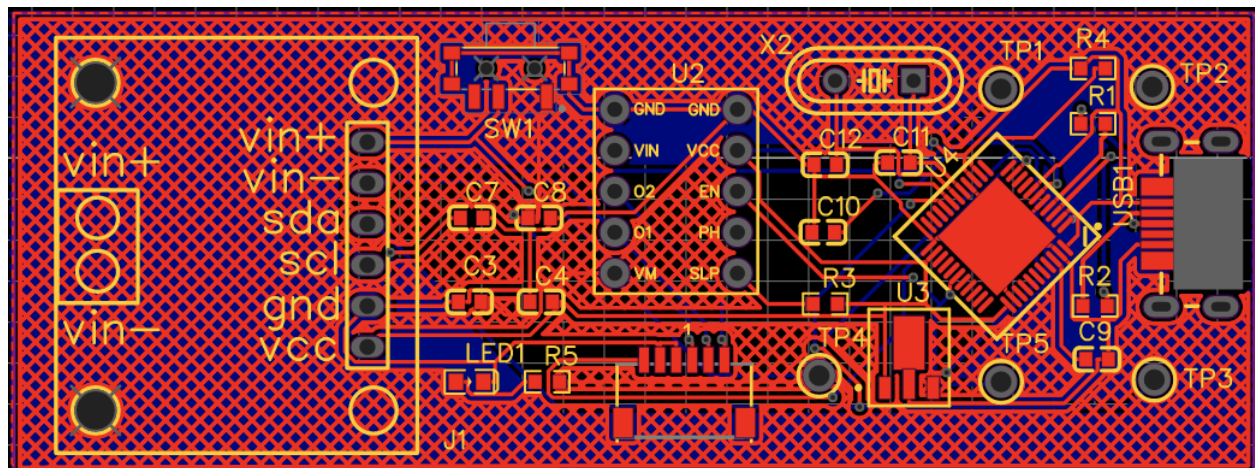


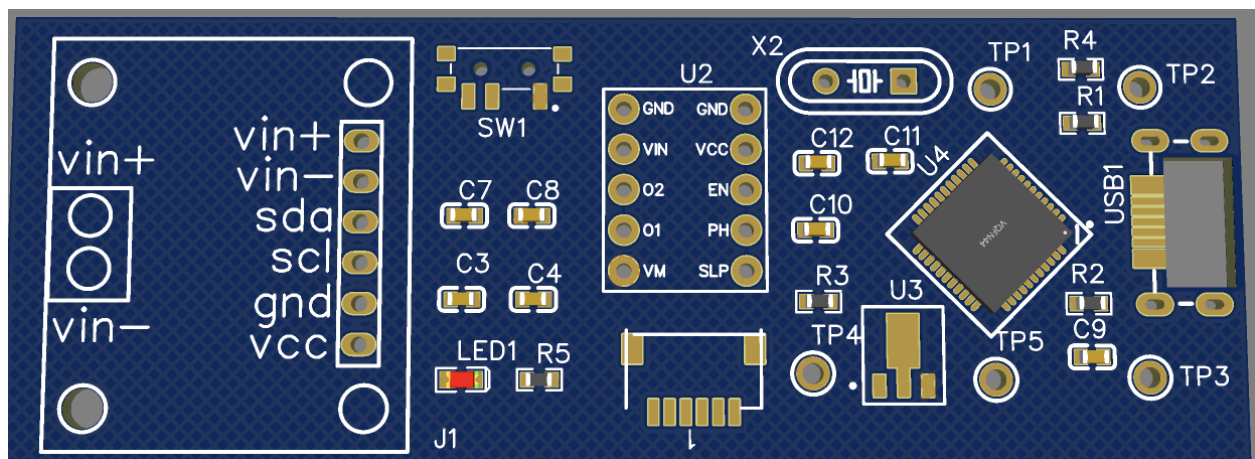
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Weekly Recap and Future Plans

I currently have purchased the separate parts for the PCB. The hardest part for this has been the chip shortage so I have had to constantly check updates for finding the microchip which was finally restocked at JLCPCB today which is where I plan to buy the PCB so I can finally finalize that purchase. I was worried about buying the PCB before I had the microchip because if I couldn't find the right package I used on the PCB then it wouldn't work.



The current PCB is 2 layers because its not as complex so it shouldn't need 4 layers, also saves money. I have 2 copper pours on the top layer I'm using VDD and on the bottom layer there is Ground. This will just help dissipate heat and reduce noise. There are 5 testing points just in case there is something wrong with the PCB we can test it manually and see what connection is going wrong or if something is shorted.



I think the final dimensions were 28.5 mm x 77.5 mm

Currently I am working on my preliminary build which is a fairly long 3500 word paper that's due next friday. This is considered almost a rough draft for the final research report, so there's some sections like abstract, current research, and plans that I need to write up. From this I will probably write the background information from your published research articles and then write about the work that I am doing.