Workflow Summary: From Verilog Testbench to Python Output

1. Updated Verilog Testbench (testbench.sv):

- Set input date to year = 2025, month = 7, day = 20.

- Used $display to print whether it's a leap year and the day of the year (N).

2. Compiled and Simulated the Verilog Code:

- Ran the simulation using run.sh inside WSL Ubuntu environment.

- Command used: ./run.sh > output.txt

- This generated output.txt with simulation results.

3. Output File (output.txt) Sample:

Date	Leap	N (Day of Year)
01-01	0	1
01-03	0	60
01-03	1	61
19-07	1	201

4. Python Script (read_n_value.py):

- Read the output.txt file.

- Parsed the last meaningful line containing N value.

- Extracted N = 201 for use in further calculations.

5. Executed Python Script in Ubuntu:

- Ran: python3 read_n_value.py

- Output: Last N value: 201

Now the N value is ready for use in further Python-based calculations.