

Modification to run CPU02CMI on 2.5MHz bus.

Replace xtal into 10MHz.

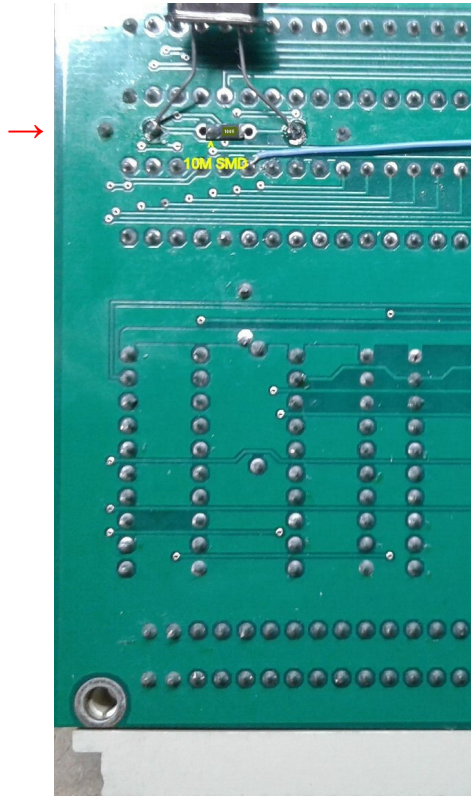
Replace C1 and C2 by 22 pF.

Place 10M 1206 smd between crystal pins.

You are done.....

See Modification to “RUN CPU02CMI at 12MHz”.

↓ Only 10M 1206 smd between crystal pins.



Remarks:

Works even fine with HD6350., MC6850.

Board also tested with the AMI S6802, this 1Mhz chips runs perfect on 3MHz.

CdeJ