Modification to run CPU02CMI on 2.75MHz bus.

Replace xtal into 11.0592MHz.
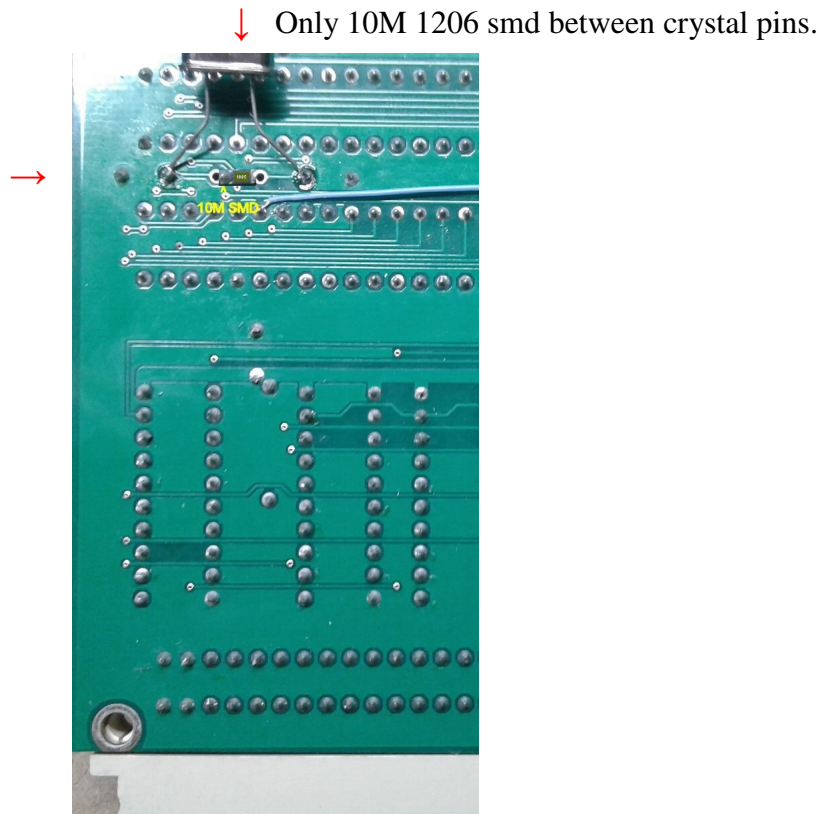Replace C1 and C2 by 18 pF.
Place 10M 1206 smd between crystal pins.

Replace GAL CMI_1 by CMI-3_1

You are done…..
 See Modification to "RUN CPU02CMI at 12MHz".

↓   Only 10M 1206 smd between crystal pins.

Remarks:
   Added the 11MHz option because 1 piece MC68B02 fail to run TSC BASIC
   and TSC XBASIC "FOR NEXT" loop correct on 12MHz.
   Works even fine with HD6350., MC6850.
   Board also tested with the AMI S6802, this 1Mhz chips runs perfect up to 3MHz.

   Tested 10 pieces MC68B02P and 21 pieces S6802P, they all work perfect on this speed.

   Board can now with FLEXnC.SYS Read/Write 8" DSDD drives.

CdeJ