Modification to run CPU02CMI on 3MHz bus.
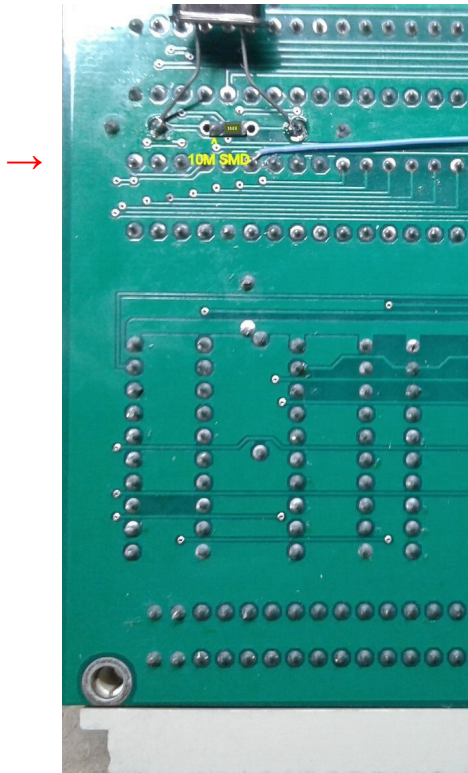
Replace xtal into 12MHz.
Replace C1 and C2 by 18pF.
Place 10M smd between crystal pins.

Replace GAL CMI_1 by CMI-3_1

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↓ Only 10M 1206 smd between crystal pins.

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Remarks:
Works even fine with HD6350,MC6850.
Board also tested with the AMI S6802, this 1Mhz chips runs perfect on 3MHz.

Tested 10 pieces MC68B02P and 21 pieces S6802P,
one MC68B02P has a temperature drift problem see "RUN CPU02CMI at 11Mhz.pdf".

Board can now with FLEXnC.SYS Read/Write 8" DSDD drives.

CdeJ