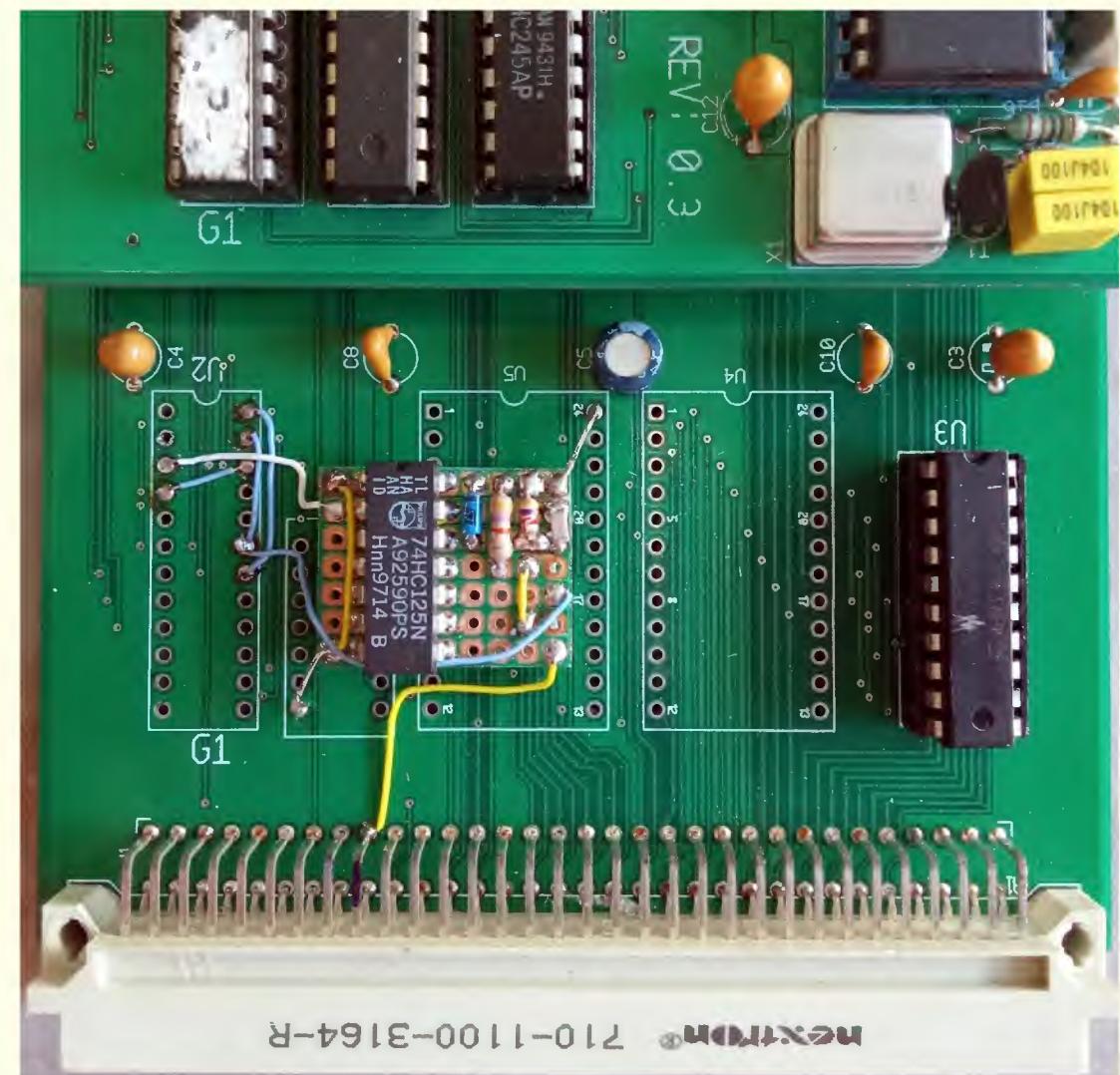
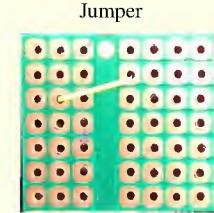
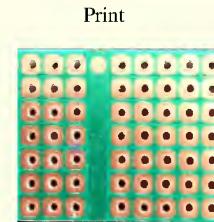


#	Device	Value	Package	Type	Refdes
RES	250W	47K	10%	RES12	R1
RES	125W	4K7	5%	RES12	R2
CER	CAP	39p	5%	1206 SMD	Cx
CER	CAP	56p	5%	1206 SMD	Cx
DIODE	SCHOTT	BAT43		DIO_D035	D1
74HC125		74HC125		DIP14	U1

Solder all parts like SMD.



Title		
Size	Number	Revision
B		
Date:	15-Dec-2025	Sheet of
File:	CMSOFLP_MRDYFLP_MRDY.ddb	Drawn By:

Above show MRDY for 09FLP.

It will stretch E with 1 cycle.

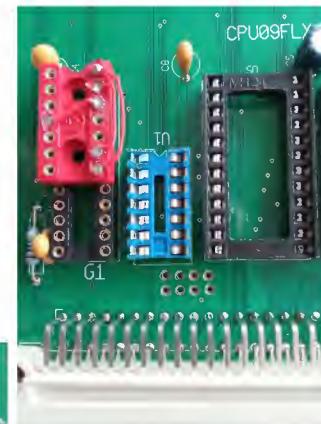
Below show MRDY option for all IO except CPU09RAM and CPU09ID6,

these two work fine on a 5MHz bus.

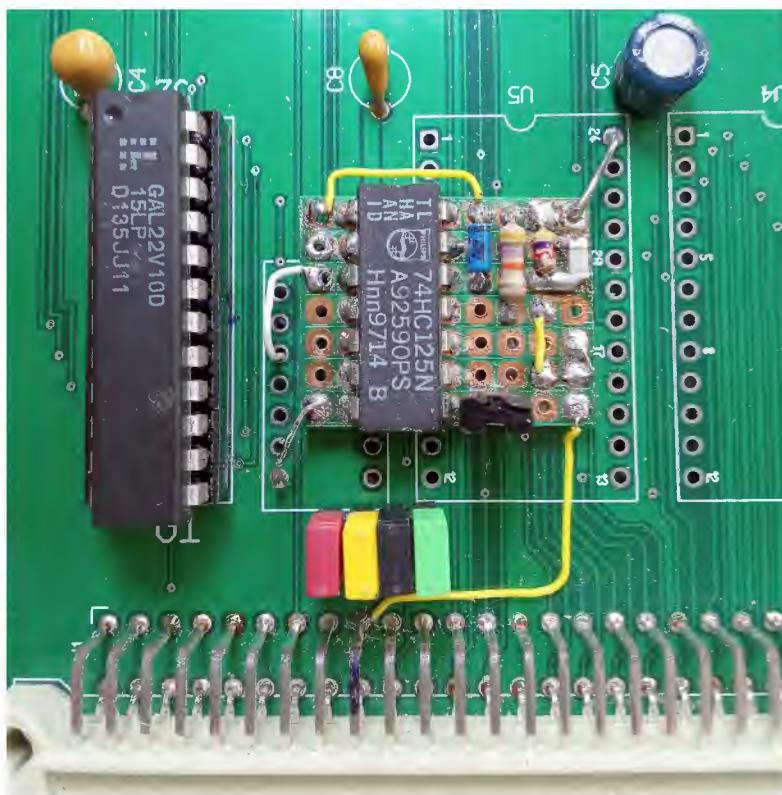
Also the option to use MRDY for the ACIA on the CPU09CMI, see “RUN CPU09CMI at 20Mhz.”.

It will stretch E with $\frac{1}{2}$ or 1 cycle depending on Cx.

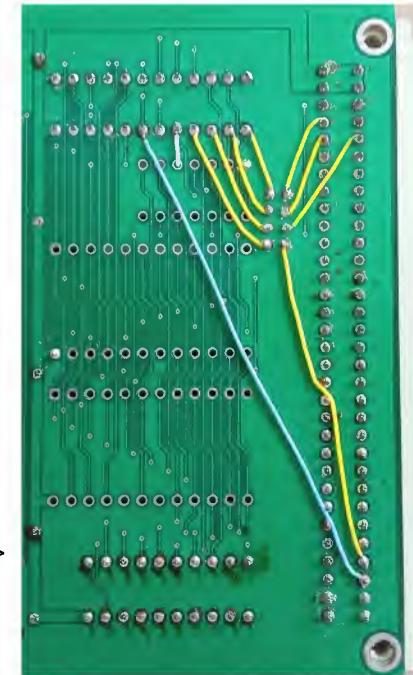
The ACIA, DIV3, DIV4, DIV7 are jumper selectable.



Drill 8 holes for the jumper block >



and wire the FLXMIN_M >



Use the print from before, but the wiring is different.

Select Cx for $\frac{1}{2}$ or 1 cycle.

Use a GAL with internal pull-ups,
parts U1, U4, U5 may not be placed !

Jumper MDY On.

Select for MRDY:

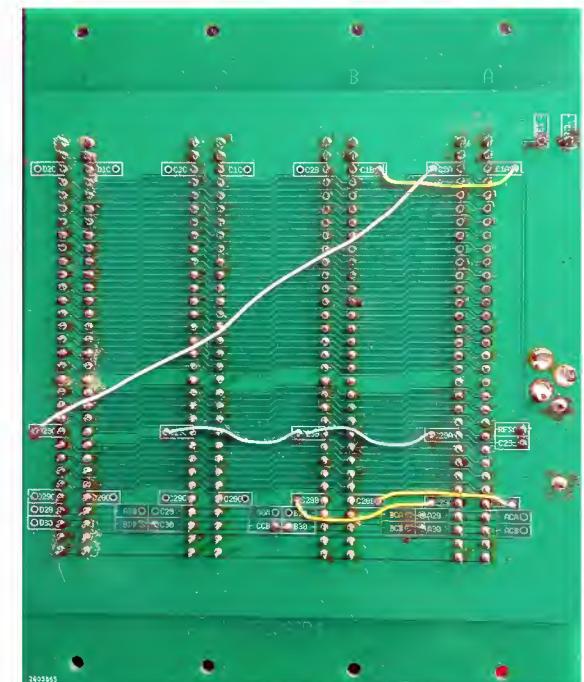
Red = M_ACIA

Yellow = DIV7

Black = DIV3

Green = (DIV4 with DIV4 jumper on CPU09CMI)

Backplane wiring >



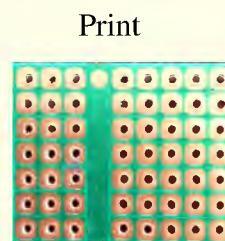
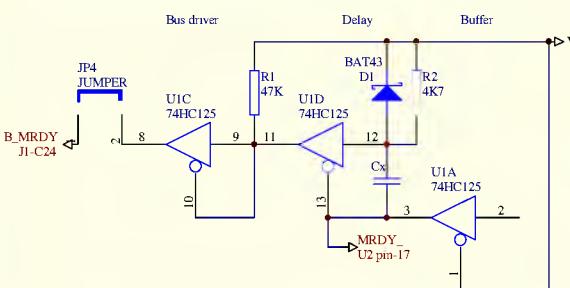
1 2 3 4 5 6

**MRDY for MINFLX and ACIA on CPU09CMI
need G1 - FLX_2**

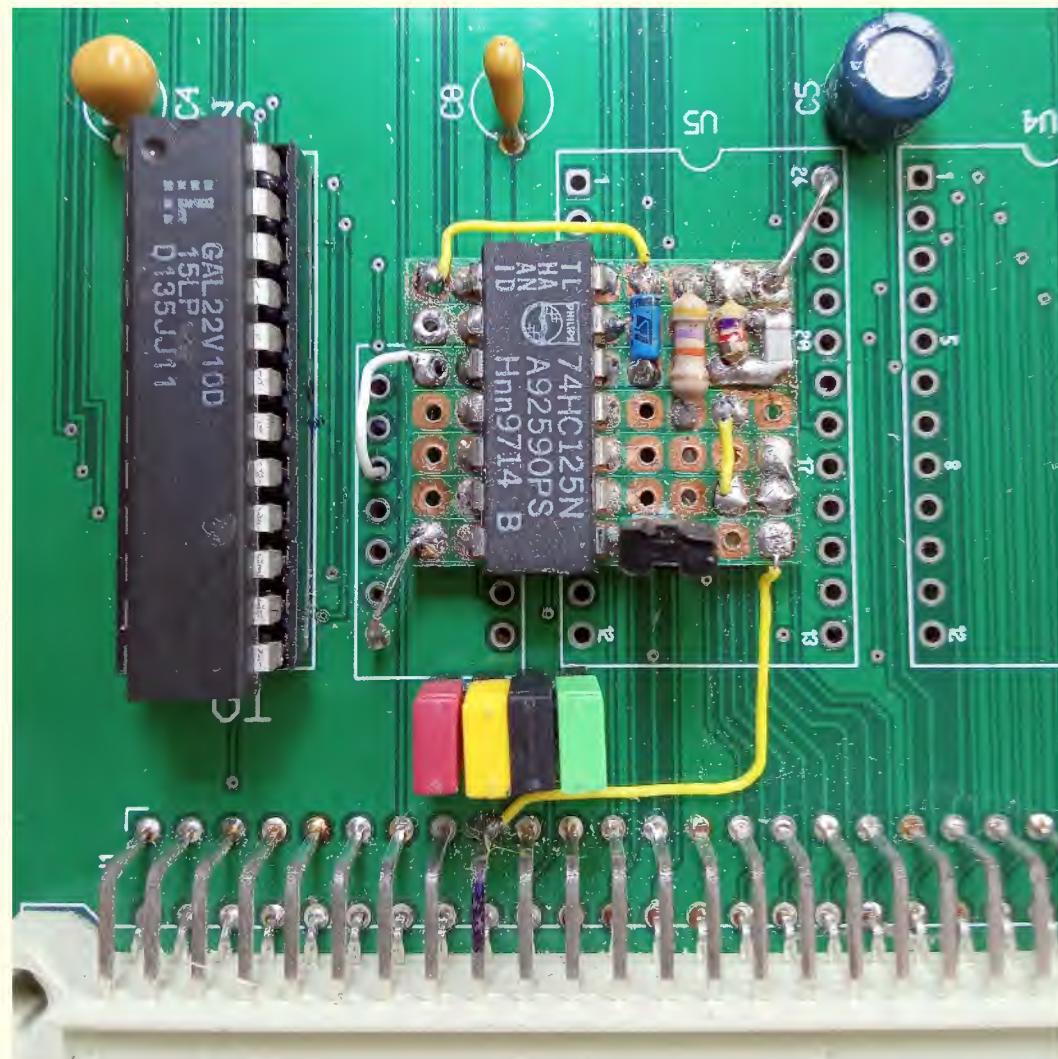
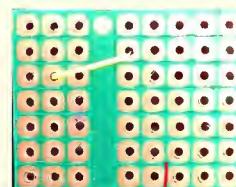
Timing:

5MHz CPU bus Cx 27pF E=200nsec /E=100nsec FDC 3.3MHz

5MHz CPU bus Cx 47pF E=300nsec /E=100nsec FDC 2.5MHz



Jumper



Title		
Size	Number	Revision
B		
Date: 19-Jan-2026	Sheet of	
File: CMSOFLP_MRDYFLXMIN.M.ddb		Drawn By:

1 2 3 4 5 6