

Chip stress test in the CPUxxCMI

| chip | Bus 2 MHz | | Bus 3 MHz | | Bus 4 MHz | |
|--|-----------|---|-----------|---|-----------|----|
| ACIA's tested on CPUxxCMI and CPU09SR4 | | | | | | |
| HD6350 | 20 | 0 | 20 | 0 | 20 | 0 |
| HD63B50 | 40 | 0 | 40 | 0 | 40 | 0 |
| MC6850 | 15 | 0 | 15 | 0 | 15 | 0 |
| MC68B50 | 13 | 0 | 13 | 0 | 13 | 0 |
| CPU's tested on CPUxxCMI running FLEX | | | | | | |
| HD6809 | 6 | X | 6 | X | 6 | X |
| HD68B09 | 1 | X | 1 | X | 1 | X |
| MC6809 | 2 | X | 2 | X | 2 | X |
| MC68B09 | 3 | X | 3 | X | 3 | X |
| HD63C09 | 40 | 0 | 40 | 0 | 40 | 0 |
| CPU's tested on CPUxxCMI in Monitor | | | | | | |
| S6802 | 21 | 0 | 21 | 0 | -- | -- |
| MC68B02 | 10 | 0 | 10 | 0 | -- | -- |

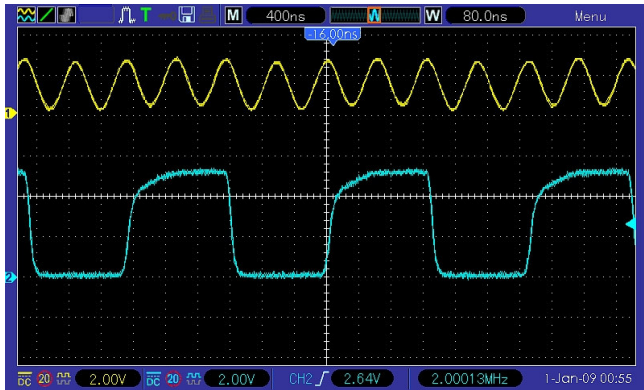
Pcs Fail Pcs Fail Pcs Fail
X Not working??? -- Not tested.

C1 and C2 on the CPUxxCMI are reduced to 15pF; the print traces will add 10pF.
For all test the CPUxxCMI with 16MHz modification and PAL CMI-4_1 is used.
All 09 CPU's are tested running FLEX, the HD63C09 is tested on many CPU09... cards.
All ACIA's are also tested on the CPU09SR4 running UniFLEX.

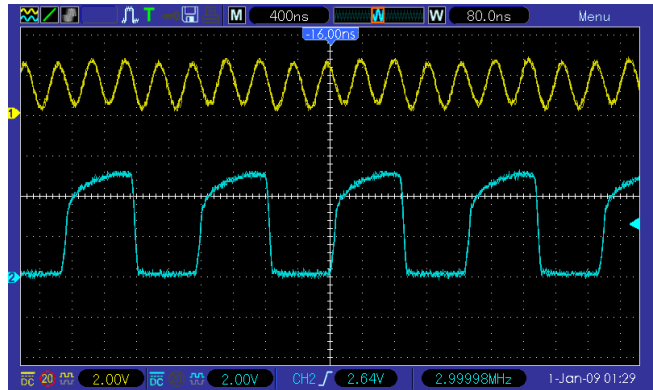
Remarkable even all the 1 MHz ACIA's are working on the 16MHz boards.

All pictures show xtal frequency and E clock.

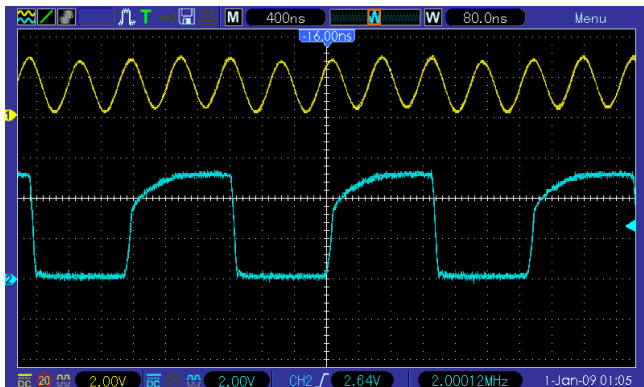
S6800 - 2MHz bus



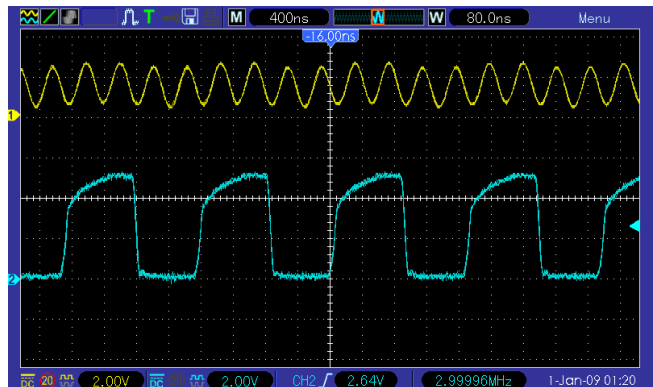
S6800 - 3MHz bus



MC68B02 - 2MHz bus

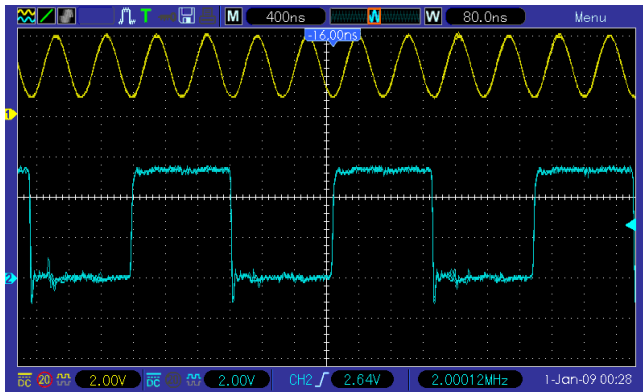


MC68B02 - 3MHz bus

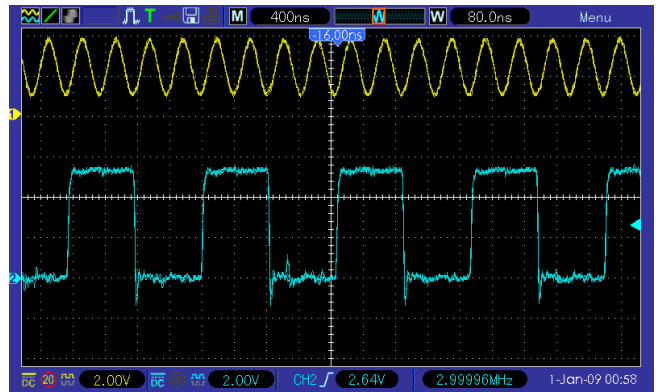


Chip stress test in the CPU_{xx}CMI

HD63C09 – 2MHz bus



HD63C09 – 3MHz bus



Remarks:

The “X” marked chip’s needs some more testing.
Probably the C1 and C2 values are incorrect for these chip’s.

CdeJ