

Modification to run CPU09CMI on 3MHz bus.

Replace xtal into 12MHz.

Replace C1 and C2 by 18 pF.

Place 10M 1206 smd between crystal pins.

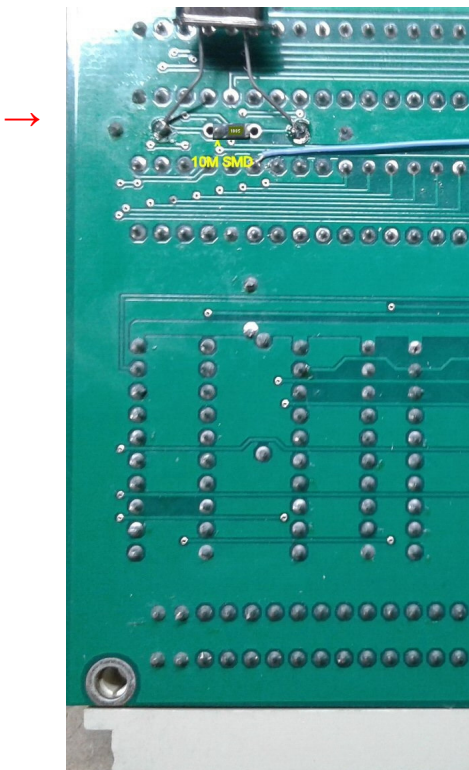
Use the HD63C09, M68B09.

You are done.....

But for the HD63C09 add the Q clock for a better timing.

See Modification to “RUN CPU09CMI at 16MHz”.

↓ Only 10M 1206 smd between crystal pins.



Remarks:

Works even fine with HD6350., MC6850.

Board also tested with MC68B02, S6802.

CdeJ