Modification to run CPUxxCMI on 3MHz bus.

Replace xtal into 12MHz.
Replace C1 and C2 by 15 pF.
Place 10M 1206 smd between crystal pins.
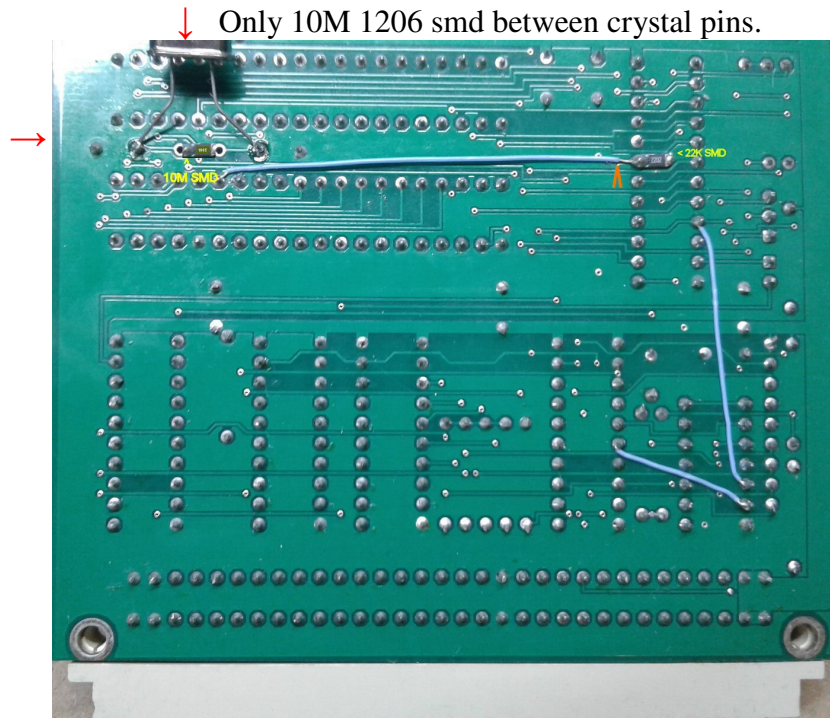Use the HD63C09, M68B09.
For the MC68B02. 22pF for C1 and C2, xtal 10MHz!

You are done…..
 But for the HD63C09 add the Q clock for a better timing.
See "Modification to run CPUxxCMI on 4MHz bus".

Only 10M 1206 smd between crystal pins.

Remarks:
    Works even fine with HD6350., MC6850.
    Board also tested with S6802.

CdeJ