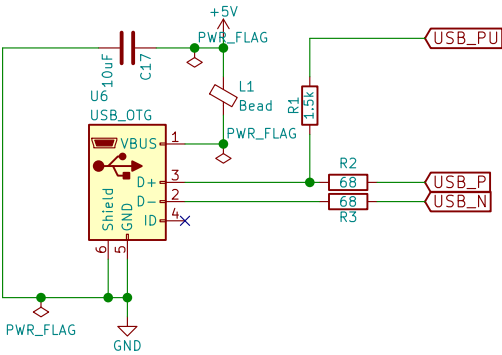
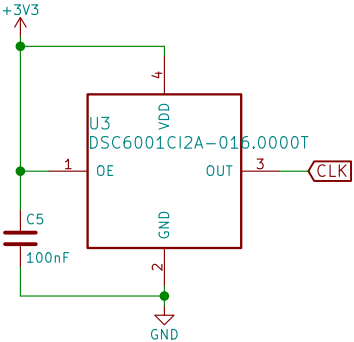


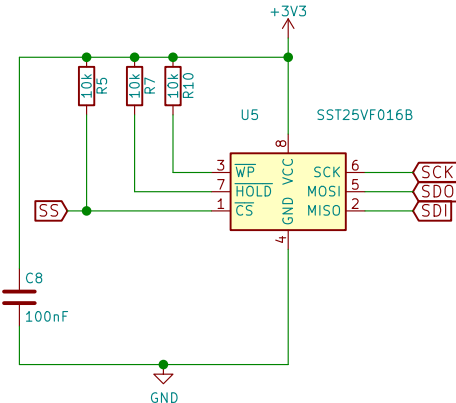
USB connection and power filtering



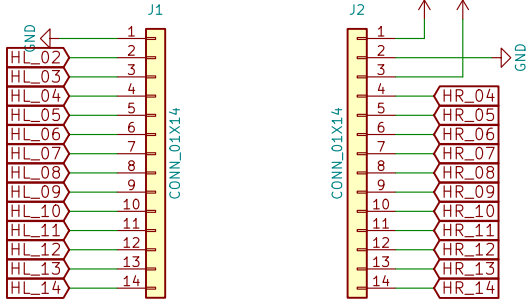
16MHz Clock



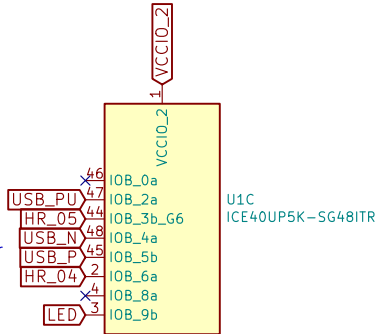
SPI flash memory for FPGA configuration



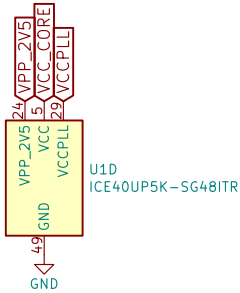
TinyFPGA Bx Pin Headers



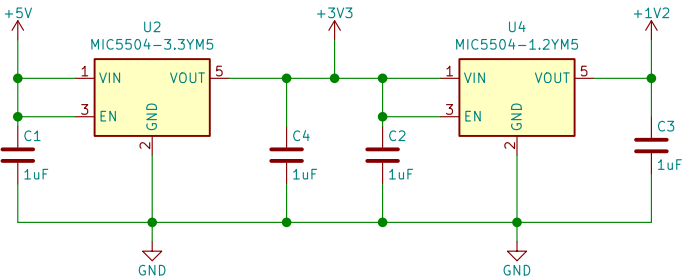
FPGA connections



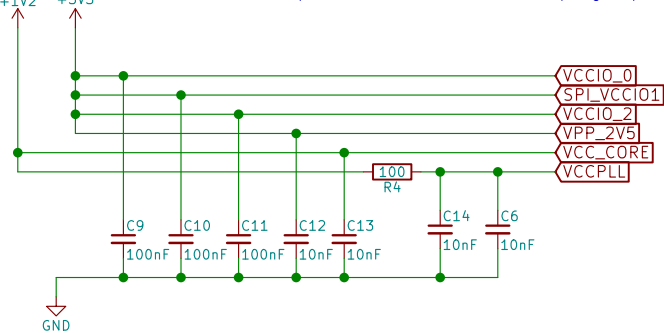
Differential pair



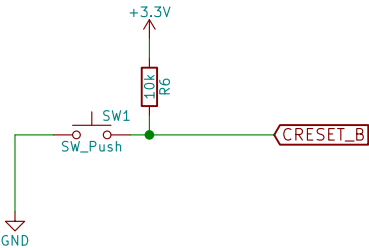
Voltage Regulation



FPGA core and IO power connections and decoupling capacitors



FPGA reset button and testpoint



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