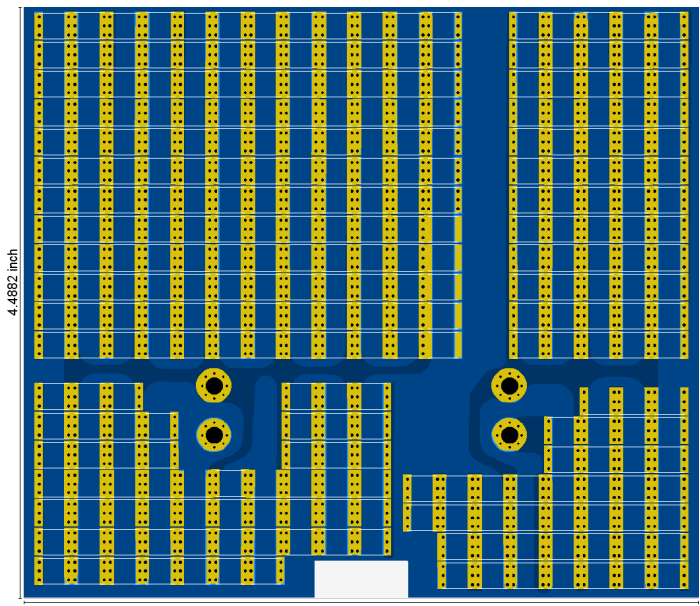
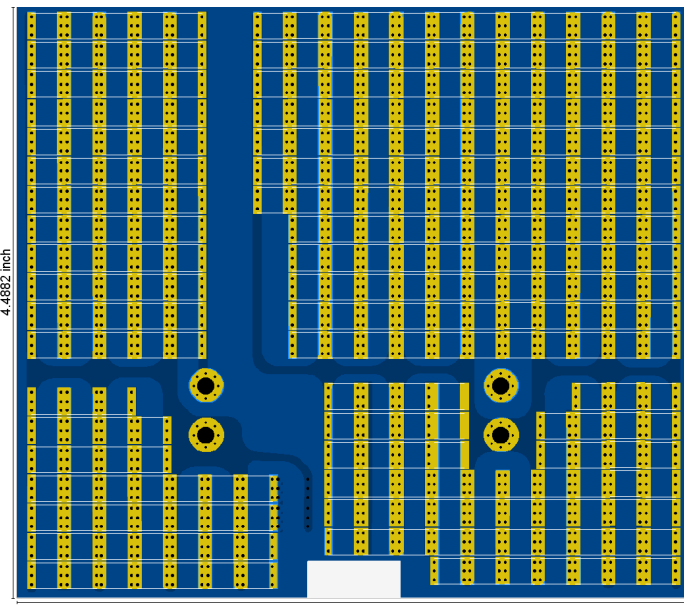


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Single PCB View - Original

Top View	Bottom View
	

Summary - General - Original

PCB Size	5.1181 inch x 4.4882 inch	Surface Finish	unknown
PCB Thickness	62.00 mil	Max. Aspect Ratio on PTH	3.1
Customer Panel Size		Number of Nets	10
Copper Layers	2	Electrical Test	Double Sided
Solder Mask	Both	Drilled SMD Pads	Yes
Solder Mask Color	Blue	SMD Pads Top	604
Legend	Both	SMD Pads Bottom	596
Legend Color	White	BGA Pads Top	0
Peeloff Mask	None	BGA Pads Bottom	0
Carbon Mask	None	Drill Hole Density	80 Holes/inch ²
Edge Connectors	No		

Summary - Copper Layers - Original

Layer Type	Min. Copper Width	Min. Critical Copper Width	Min. Trace Width	Min. Critical Trace Width	Min. Clr. Copper to Copper	Min. Clr. Trace to Trace	Min. Same Net Spacing	Min. Ring	Copper to Plated Clr.	Copper to NPTH Clr.	Copper to Outline Clr.
	mil	mil	mil	mil	mil	mil	mil	mil	mil	mil	mil
Outer	>16.00	>16.00	>16.00	>16.00	¹ 23.62	>20.00	>20.00	² 6.50	>32.00	³	23.94

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Summary - Sequences - Original

Type	Sequences	Tools	Min. End Dia.	Max. End Dia.	Holes	Moves	Min. Ring on Outer	Min. Ring on Inner	Min. Clr. Hole to Copper
			mil	mil			mil	mil	mil
Blind	0								
Buried	0								
PTH	1	3	19.70	126.00	1818	0	6.50		>32.00
Plated (Total)	1	3	19.70	126.00	1818	0	6.50		>32.00
NPTH	0								
Total	1	3	19.70	126.00	1818	0	6.50		>32.00

Summary Minimum Design Characteristics - Locations - Original

1

SSB_Cap_Board_-_Rev2-F.Cu_gbr
 \oplus x: 707.87 mil
y: 1756.1 mil

Copper to Copper Clr.
Outer Layers
23.62 mil

200 mil

2

SSB_Cap_Board_-_Rev2-B.Cu_gbr
 \oplus x: 618.11 mil
y: -173.23 mil

Min. Ring
Outer Layers
6.50 mil

100 mil

3

SSB_Cap_Board_-_Rev2-F.Cu_gbr
 \oplus x: -634.7 mil
y: 4200.63 mil

Copper to Outline Clr.
Outer Layers
23.94 mil

100 mil

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Stackup - Original



SSB_Cap_Board_-_Rev2-F.Cu_gbr

SSB_Cap_Board_-_Rev2-B.Cu_gbr

Pressing Stages

1

Copper Layer Minima & Area - Original

File	Pos.	Min. Copper Width	Min. Critical Copper Width	Min. Trace Width	Min. Critical Trace Width	Min. Clr. Copper to Copper	Min. Clr. Trace to Trace	Min. Same Net Spacing	Min. Ring	Copper to Plated Clr.	Copper to NPTH Clr.	Copper to Outline Clr.	Copper Area	
		mil	mil	mil	mil	mil	mil	mil	mil	mil	mil	mil	inch ²	%
SSB_Cap_Board_-_Rev2-F.Cu_gbr	1	>16.00	>16.00	>16.00	>16.00	23.62	>20.00	>20.00	10.98	>32.00		23.94	8.3408	37
SSB_Cap_Board_-_Rev2-B.Cu_gbr	2	>16.00	>16.00	>16.00	>16.00	>32.00	>20.00	>20.00	6.50	>32.00		23.94	8.5539	38

Drill Tools - Original

File	Tool Nr.	Span	Type	Method	FilledVia	Countere d	Dia.	Tol. Min	Tol. Plus	Holes (in PCB)	Moves (in PCB)	Doubl e Hits (in File)	Predril l Hits (in File)	Min. Ring on Outer	Min. Ring on Inner	Min. Pad Size
							mil	mil	mil					mil	mil	mil
SSB_Cap_Boar d_-_Rev2_drl	1	1-2	PTH	unknow n	unknown	unknown	19.70	0.00	0.00	32	0	0	0	21.65		63.00
SSB_Cap_Boar d_-_Rev2_drl	2	1-2	PTH	unknow n	unknown	unknown	22.00	0.00	0.00	1782	0	8	0	6.50		35.00
SSB_Cap_Boar d_-_Rev2_drl	3	1-2	PTH	unknow n	unknown	unknown	126.00	0.00	0.00	4	0	0	0	>32.00		>190.00

Sequences - Original

Span	Type	Tools	Min. End Dia.	Max. End Dia.	Holes	Min. Ring on Outer	Min. Ring on Inner	Min. Clr. Hole to Copper	Min. Clr. Hole to Outline	Min. Clr. Slot to Outline
			mil	mil		mil	mil	mil	mil	mil
1-2	PTH	3	19.70	126.00	1818	6.50		>32.00	63.80	disabled
All	Plated	3	19.70	126.00	1818	6.50		>32.00	63.80	disabled
All	All	3	19.70	126.00	1818	6.50		>32.00	63.80	disabled

Rout Tools - Original

File	Tool Nr.	Type	Tool Dia.	End Dia.	Draw Length	Nibble Count
			mil	mil	mil	

Routed Holes - Original

File	Hole Nr.	Instances	X Size	Y Size	Draw Length	Nibble Count
			mil	mil	mil	

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Solder Mask - Original

Side	Min. Ring on Cu Defined Pads	Min. Ring on SM Defined Pads	Min. Clr. Mask to Mask	Min. Web	Min. Clr. Mask to Copper	Fully Covered Via Holes	Partly Covered Via Holes	TH Via Holes Half Mask
	mil	mil	mil	mil	mil			
Top	7.30	>10.00	>10.00	7.48	>10.00	No	No	<div></div>
Bottom	7.30	>10.00	>10.00	3.30	>10.00	Yes	Yes	<div></div>
Both	7.30	>10.00	>10.00	3.30	>10.00	Yes	Yes	Yes

Files - Original

Initial	Renamed	Format	Function	Position	Color
SSB_Cap_Board_-_Rev2-F.SilkS.gbr	SSB_Cap_Board_-_Rev2-F.SilkS_gbr	ger274x	silk	top	white
SSB_Cap_Board_-_Rev2-F.Mask.gbr	SSB_Cap_Board_-_Rev2-F.Mask_gbr	ger274x	mask	top	blue
SSB_Cap_Board_-_Rev2-F.Cu.gbr	SSB_Cap_Board_-_Rev2-F.Cu_gbr	ger274x	outer	1	<div></div>
SSB_Cap_Board_-_Rev2-B.Cu.gbr	SSB_Cap_Board_-_Rev2-B.Cu_gbr	ger274x	outer	2	<div></div>
SSB_Cap_Board_-_Rev2-B.Mask.gbr	SSB_Cap_Board_-_Rev2-B.Mask_gbr	ger274x	mask	bottom	blue
SSB_Cap_Board_-_Rev2-B.SilkS.gbr	SSB_Cap_Board_-_Rev2-B.SilkS_gbr	ger274x	silk	bottom	white
SSB_Cap_Board_-_Rev2.drl	SSB_Cap_Board_-_Rev2_drl	excellon2	plated	1-2	<div></div>
SSB_Cap_Board_-_Rev2-Edge.Cuts.gbr	SSB_Cap_Board_-_Rev2-Edge.Cuts_gbr	ger274x	cad_outline	none	<div></div>

Comments - Original