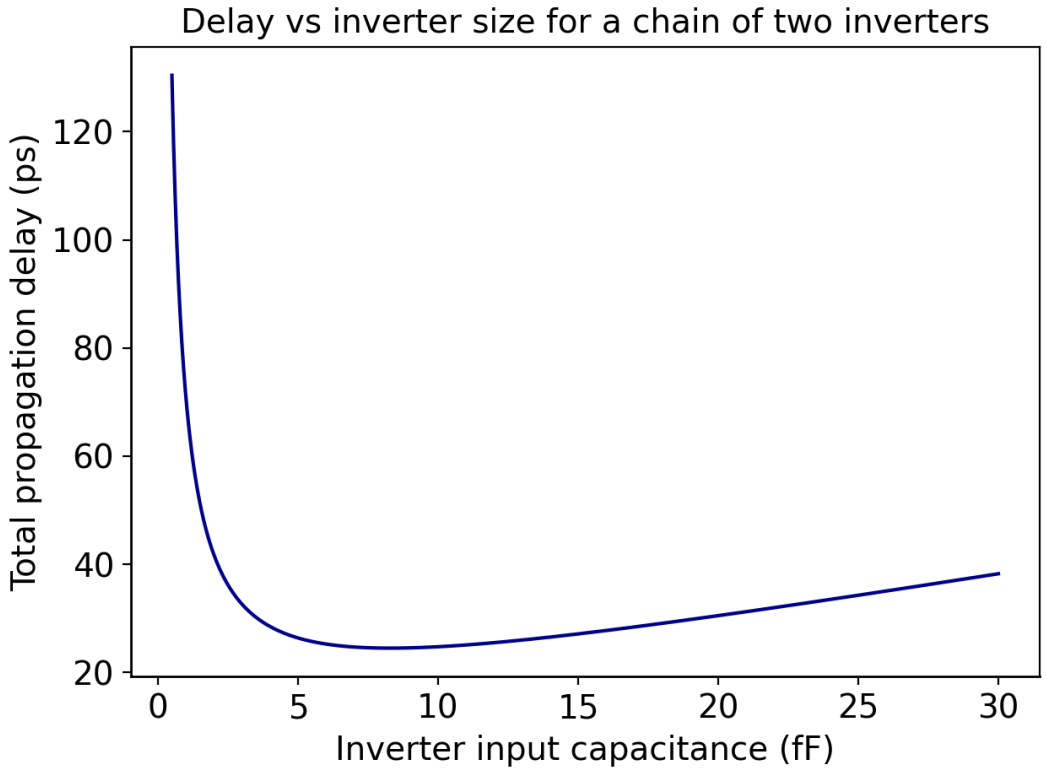
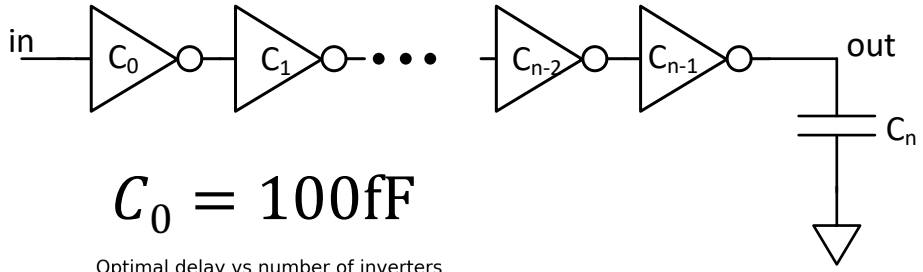
Practical sizing considerations



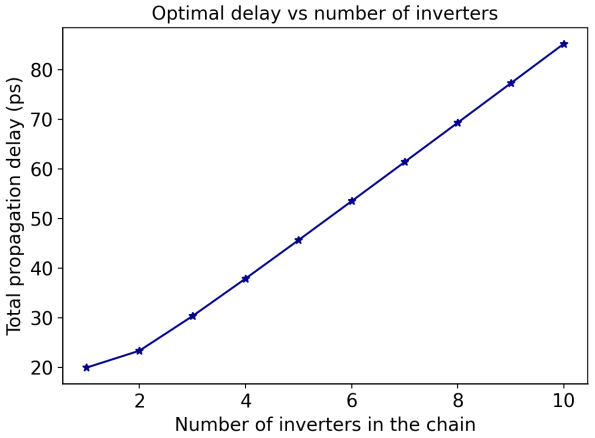
- Better to err on the side of larger drivers
- Min width issues (Allowed/Recommended for technology)
- Technique is great as an **initial starting point** for design

Optimal number of inverters

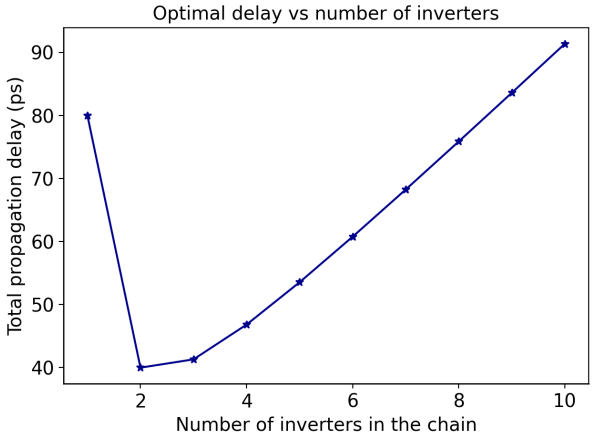
$C_{in} = 2fF, \tau_{INV} = 3ps, p_d = 5ns$



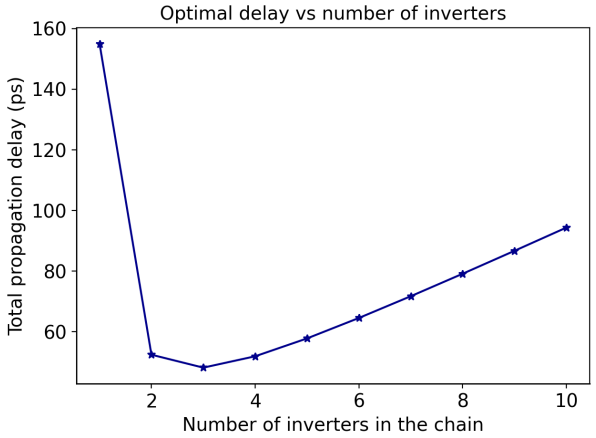
$C_0 = 10\text{fF}$



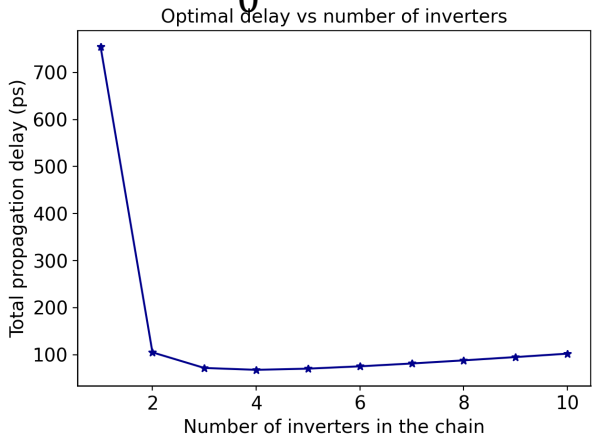
$C_0 = 50\text{fF}$



$C_0 = 100\text{fF}$



$C_0 = 500\text{fF}$



$C_0 = 1000\text{fF}$