Problem 1 (David P Babin)

$$\tilde{x}(s) = (s\mathbf{I} - A)^{-1}B \, \tilde{u}(s)$$

$$\begin{bmatrix} \tilde{v}(s) \\ \tilde{v}(s) \end{bmatrix} = \begin{bmatrix} s & 0 \\ 0 & s \end{bmatrix} - \begin{bmatrix} -\frac{R_L}{L} & -\frac{D'}{L} \\ \frac{D'}{C} & -\frac{1}{RC} \end{bmatrix}^{-1} \cdot \begin{bmatrix} \frac{V}{L} & \frac{1}{L} \\ -\frac{I}{C} & 0 \end{bmatrix} \cdot \begin{bmatrix} \tilde{d}(s) \\ \tilde{v}_{in}(s) \end{bmatrix}$$

$$\begin{bmatrix} \widetilde{\iota}(s) \\ \widetilde{v}(s) \end{bmatrix} = \frac{1}{CLRs^2 + (L + CRR_L)s + R_L + (D')^2R} \begin{bmatrix} V + D'IR + CRVs & CRs + 1 \\ -R \cdot (IR_L - D'V + ILs) & D'R \end{bmatrix} \cdot \begin{bmatrix} \widetilde{d}(s) \\ \widetilde{v}_{in}(s) \end{bmatrix}$$

The transfer function relating the small signal voltage to the small signal duty ratio:

$$G_{vd}(s) = \frac{\tilde{v}(s)}{\tilde{d}(s)} = \frac{-R \cdot (IR_L - D'V + ILs)}{CLRs^2 + (L + CRR_L)s + R_L + (D')^2R}$$

$$\downarrow \downarrow \text{Assume } R_L = 0$$

$$G_{vd}(s) = \frac{RD'V - RILs}{CLRs^2 + Ls + (D')^2 R} = \frac{RD'V}{(D')^2 R} \cdot \frac{1 - \frac{RIL}{RD'V}s}{1 + \frac{L}{(D')^2 R}s + \frac{CLR}{(D')^2 R}s^2}$$

$$G_{vd}(s) = \frac{V}{D'} \cdot \frac{1 - \frac{IL}{D'V}s}{1 + \frac{L}{(D')^2R}s + \frac{CL}{(D')^2}s^2} = G_o \frac{1 - \frac{s}{\omega_z}}{1 + \frac{s}{Q\omega_o} + \left(\frac{s}{\omega_o}\right)^2}$$

So we have:

$$\bullet \quad G_o = G_{d0} = \frac{V}{D'}$$

$$\bullet \quad \frac{1}{\omega_{z}} = \frac{IL}{D'V} \quad \rightarrow \quad \omega_{z} = \frac{D'V}{IL} = \frac{D'}{L} \cdot \frac{V}{I} = \frac{D'}{L} \cdot \frac{D'R \cdot \frac{V_{in}}{R_{L} + (D')^{2}R}}{\frac{V_{in}}{R_{L} + (D')^{2}R}} = \frac{D'}{L} \cdot D'R = \boxed{\frac{(D')^{2}R}{L} = \omega_{z}}$$

•
$$\frac{1}{\omega_o^2} = \frac{CL}{(D')^2} \rightarrow \omega_o^2 = \frac{(D')^2}{CL} \rightarrow \omega_o = \frac{D'}{\sqrt{LC}}$$

$$\bullet \quad \frac{1}{Q\omega_o} = \frac{L}{(D')^2 R} \quad \rightarrow \quad Q\omega_o = \frac{(D')^2 R}{L} \quad \rightarrow \quad Q = \frac{1}{\omega_o} \cdot \frac{(D')^2 R}{L} = \frac{\sqrt{LC}}{D'} \cdot \frac{(D')^2 R}{L} = \boxed{\boldsymbol{D'} \boldsymbol{R} \sqrt{\frac{C}{L}} = \boldsymbol{Q}}$$

Problem 2 Calculation

(a)
$$\Delta i_L = \frac{V_g D T_s}{L}$$
 (increase L to reduce current ripple)

(b)
$$P = 250 = \frac{V_{out}^2}{R} = \frac{1}{R} \left(\frac{V_g}{D'}\right)^2 \Rightarrow D' = \frac{V_g}{\sqrt{RP}}$$
 To keep V_out constant, adjust R instead of D:

$$D' = 0.5 = \frac{24}{\sqrt{R * 250}} = \frac{24}{\sqrt{R_{new} * 100}} \Rightarrow R = 9.216\Omega, R_{new,b} = \frac{23.04\Omega}{100}$$

(c)
$$f_{sw}$$
 doesn't affect $G_{vd}(s)$

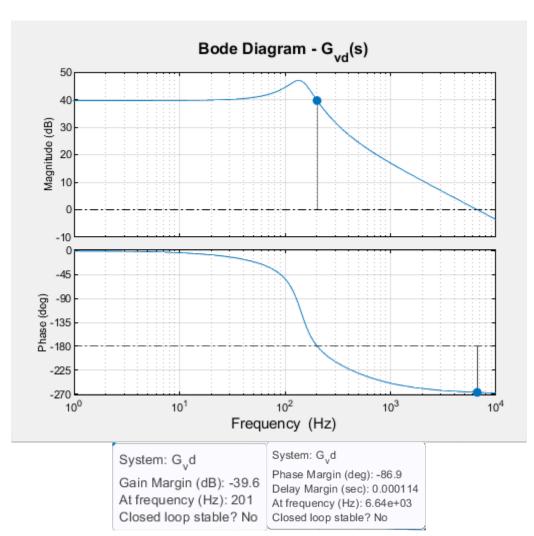
$$R_{new,d} = \frac{V_{out}^2}{P} = \frac{30^2}{250} = \frac{3.6\Omega}{0.8}$$
 $D_{new,d}' = \frac{V_g}{V_{out}} = \frac{24}{30} = \frac{0.8}{0.8}$

Problem 2 Bode Plots (Sergio Alexander Sunley Pocasangre)

Problem 2: We operate the boost converter of Problem 1 at the following nominal conditions

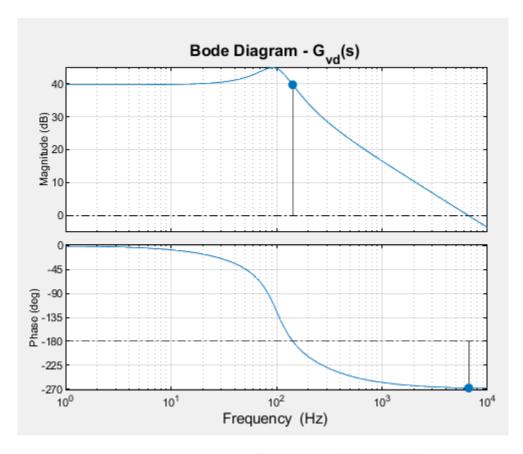
 V_{in} = 24V, output voltage = 48V, output power = 250 W, f_{sw} = 100 kHz, L = 1.25 mH, C = 250 μ F.

Bode plot with initial values



Does the stability of the system Gvd(s) with no compensation (controller, C(s) = 1) increases, decreases, or remains unaffected when

• You redesign inductance, L to reduce the current ripple of i(t) shown in Figure 1.



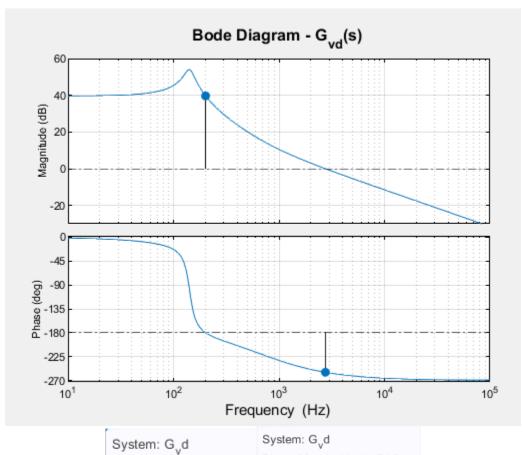
System: G_vd

System: G_vd

Gain Margin (dB): -39.6 At frequency (Hz): 142 Closed loop stable? No Phase Margin (deg): -88.1 Delay Margin (sec): 0.000114 At frequency (Hz): 6.63e+03 Closed loop stable? No

Inductance value used: L = 2.50 mH

You change the output power to 100 W.



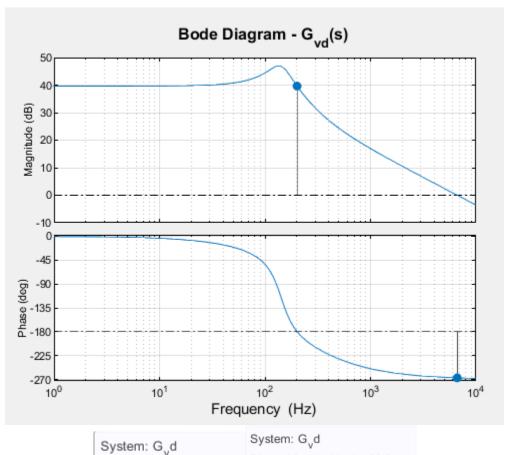
System: G_vd

Gain Margin (dB): -39.6

At frequency (Hz): 201

Closed loop stable? No

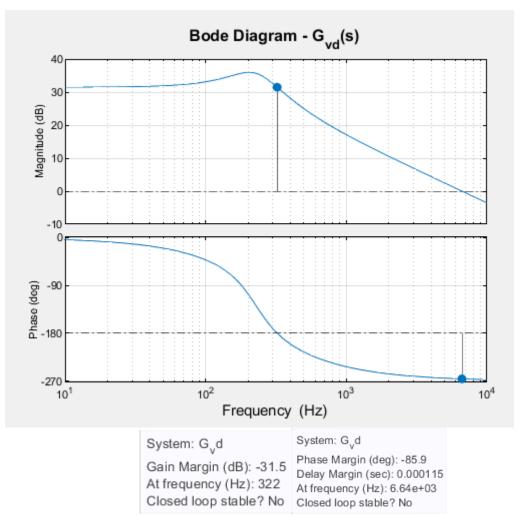
Phase Margin (deg): -74.5 Delay Margin (sec): 0.000288 At frequency (Hz): 2.75e+03 Closed loop stable? No You change the switching frequency to 10 kHz.



Gain Margin (dB): -39.6 At frequency (Hz): 201

Phase Margin (deg): -86.9 Delay Margin (sec): 0.000114 At frequency (Hz): 6.64e+03 Closed loop stable? No Closed loop stable? No

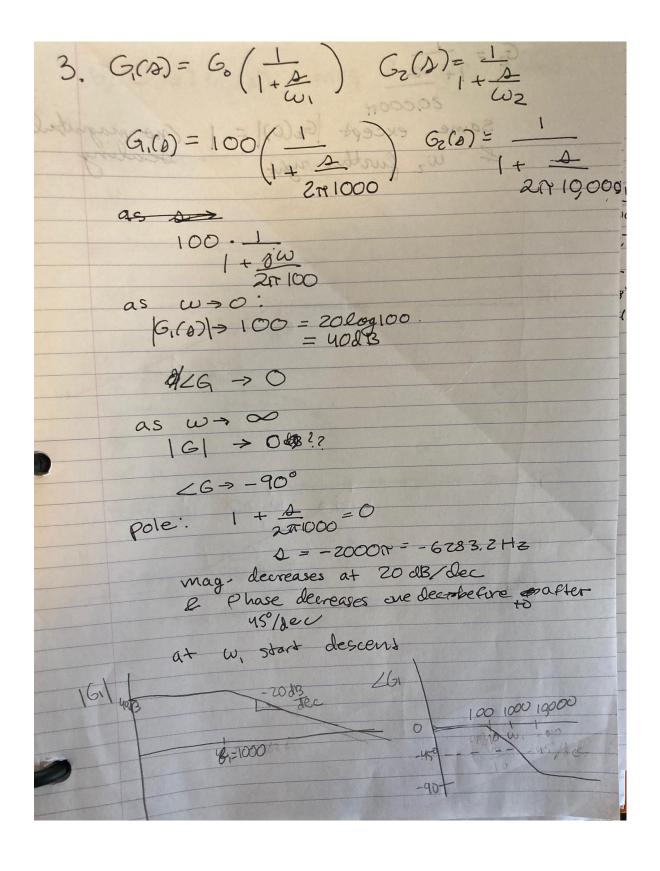
You change the output voltage to be at 30 V.

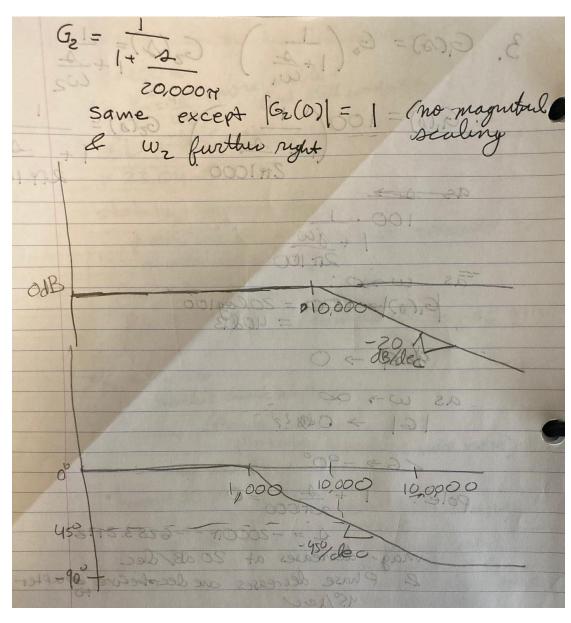


Summary:

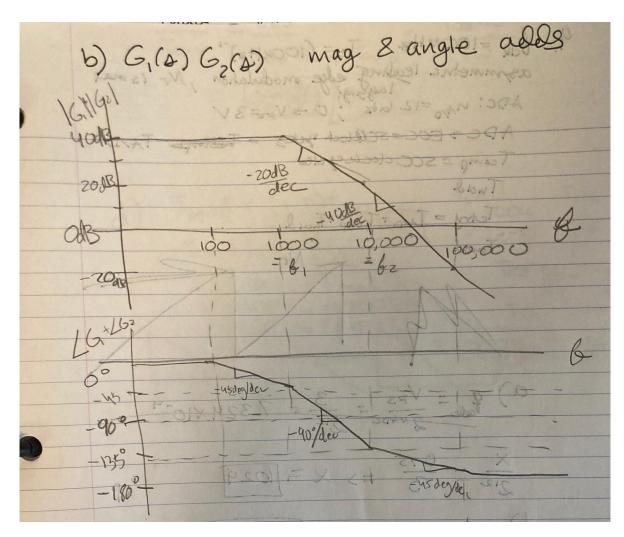
For the **original system** (system with given values), the **gain margin (GM) is -39.6 dB** and **phase margin (PM) is -86.9°**. Since we have a negative phase margin, system is not stable.

- By increasing L (to reduce current ripple) PM increases to -88.1°. Gain Margin is not altered. Hence, the stability of the system decreases with an increase of L (reducing current ripple)
- By decreasing power to 100 W, PM decreases to -74.5°. GM is not altered. Hence, the stability of the system increases when reducing output power.
- Changing switching frequency does not cause modification on stability (stability remains unaffected). Transfer function does not depend on switching frequency.
- Modifying the output voltage to 30 V (duty = 0.2) causes GM to decrease to -31.5 dB and PM to decrease to -85.9°. The stability of the system increases with the reduction of the output voltage





b)



Verify results: