Register	Value	Comments
//ADC		
EPwm1Regs.TBCTL.bit.HSPCLKDIV	0b000	
EPwm1Regs.TBCTL.bit.CLKDIV	0b000	
EPwm1Regs.TBCTL.bit.CTRMODE	0b10	
EPwm1Regs.TBPRD	5000	
EPwm1Regs.AQCTLA.bit.CAU	0b01	
EPwm1Regs.AQCTLA.bit.CAD	0b10	
EPwm1Regs.DBCTL.bit.IN_MODE	0b00	
EPwm1Regs.DBCTL.bit.POLSEL	0b10	
EPwm1Regs.DBCTL.bit.OUT_MODE	0b11	
EPwm1Regs.DBFED.bit.DBFED	10	
EPwm1Regs.DBFED.bit.DBRED	10	
//Sync PWM with ADC		
Pwm1Regs.ETSEL.bit.SOCAEN	0b1	
Pwm1Regs.ETSEL.bit.SOCASEL	0b011	
EPwm1Regs.ETPS.bit.SOCAPRD	0b01	
EPwm1Regs.ETPS.bit.INTPRD	0b01	
//GPIO		
GpioCtrlRegs.GPAMUX1.bit.GPI00	0b1	
GpioCtrlRegs.GPAPUD.bit.GPI00	0b1	
//		