

#### 10.1.2.3.5 Generating Pseudorandom Bits Using Hash\_DRBG (...)

The following process or its equivalent **shall** be used to generate pseudorandom bits. Let **Hash (...)** be the Approved hash function to be used.

**Hash\_DRBG (...):**

**Input:** integer (*state\_handle*, *requested\_no\_of\_bits*, *requested\_strength*, *additional\_input*, *prediction\_resistance\_request\_flag*, *mode*).

**Output:** string *status*, bitstring *pseudorandom\_bits*.

**Process:**

1. If  $((state\_handle > max\_no\_of\_states) \text{ or } (state(state\_handle) = \{Null, Null, 0, 0, 0, 0, Null\}))$ , then **Return** ("State not available for the *state\_handle*", *Null*).
2. Set up the *state* values, e.g.,  $V = state(state\_handle).V$ ,  $C = state(state\_handle).C$ ,  $reseed\_counter = state(state\_handle).reseed\_counter$ ,  $strength = state(state\_handle).strength$ ,  $seedlen = state(state\_handle).seedlen$ ,  $prediction\_resistance\_flag = state(state\_handle).prediction\_resistance\_flag$ ,  $old\_transformed\_entropy\_input = state(state\_handle).transformed\_entropy\_input$ .  
Comment: If  $reseed\_counter \geq reseed\_interval$ , then reseeding could not be done in step 12 (below) during the previous call.
3. If  $(requested\_no\_of\_bits > max\_length)$  then **Return** ("Too many bits requested", *Null*).
4. If  $(requested\_strength > strength)$ , then **Return** ("Invalid *requested\_strength*", *Null*).
5. If  $((prediction\_resistance\_request\_flag = Provide\_prediction\_resistance) \text{ and } (prediction\_resistance\_flag = No\_prediction\_resistance))$ , then **Return** ("Prediction resistance capability not instantiated", *Null*).
6. If  $((reseed\_counter \geq reseed\_interval) \text{ OR } (prediction\_resistance\_request\_flag = Provide\_prediction\_resistance))$ , then  
Comment: If a reseeding capability is not available).

**Return** ("DRBG can no longer be used. Please re-instantiate or reseed", *Null*).  
Comment: If a reseeding capability is readily available.

- 6.1  $status = \text{Reseed\_Hash\_DRBG\_Instantiation}(state\_handle, additional\_input, mode)$ .
- 6.2 If  $(status \neq \text{"Success"})$ , then **Return** (*status*, *Null*).
- 6.3  $additional\_input = Null$ .
7. If  $(additional\_input \neq Null)$ , then do
  - 7.1  $w = \text{Hash}(0x02 \parallel V \parallel additional\_input)$ .
  - 7.2  $V = (V + w) \bmod 2^{seedlen}$ .
8.  $pseudorandom\_bits = \text{Hashgen}(requested\_no\_of\_bits, V)$ .
9.  $H = \text{Hash}(0x03 \parallel V)$ .
10.  $V = (V + H + C + reseed\_counter) \bmod 2^{seedlen}$ .

11.  $reseed\_counter = reseed\_counter + 1$ .
12. Update the changed values in the *state*.
  - 12.1  $state(state\_handle).V = V$ .
  - 12.2  $state(state\_handle).reseed\_counter = reseed\_counter$ .
13. **Return** ("Success", *pseudorandom\_bits*).

**Hashgen (...):**

**Input:** integer *requested\_no\_of\_bits*, bitstring *V*.

**Output:** bitstring *pseudorandom\_bits*.

**Process:**

1.  $m = \left\lceil \frac{requested\_no\_of\_bits}{outlen} \right\rceil$ .
2.  $data = V$ .
3.  $W$  = the Null string.
4. For  $i = 1$  to  $m$ 
  - 4.1  $w_i = \text{Hash}(data)$ .
  - 4.2  $W = W \parallel w_i$ .
  - 4.3  $data = data + 1$ .
5. *pseudorandom\_bits* = Leftmost (*requested\_no\_of\_bits*) bits of  $W$ .
6. **Return** (*pseudorandom\_bits*).

If an implementation will never use *additional\_input*, then the *additional\_input* input parameter may be omitted, and steps 6.3 and 7 may be omitted.

If an implementation does not need the *prediction\_resistance\_flag*, then the *prediction\_resistance\_flag* and steps 5 may be omitted..

If an implementation does not have a reseeding capability, then step 6 does not need steps 6.1 - 6.3.

#### 10.1.3.3.6 Generating Pseudorandom Bits Using KHF\_DRBG (...)

The following process or an equivalent **shall** be used to generate pseudorandom bits:

**KHF\_DRBG(...):**

**Input:** integer (*state\_handle*, *requested\_no\_of\_bits*, *requested\_strength*, *additional\_input*, *prediction\_resistance\_request\_flag*, *mode*).

**Output:** string (*status*, *pseudorandom\_bits*).

**Process:**

1. If ((*state\_handle* > *max\_no\_of\_states*) or (*state* (*state\_handle*)) = {Null, Null, Null, 0, 0, 0, Null}), then **Return** ("State not available for the indicated *state\_handle*", Null).
2. If (**len** (*additional\_input* > *max\_additional\_input\_length*), then **Return** ("additional\_input too long").

Comment: Get the appropriate *state* values.

3.  $V = state(state\_handle).V$ ,  $K_0 = state(state\_handle).K_0$ ,  $K_1 = state(state\_handle).K_1$ ,  $strength = state(state\_handle).strength$ ,  $reseed\_counter = state(state\_handle).reseed\_counter$ ,  $prediction\_resistance\_flag = state(state\_handle).prediction\_resistance\_flag$ ,  $old\_transformed\_entropy\_bits = state(state\_handle).transformed\_entropy\_bits$ .

Comment: If *reseed\_counter*  $\geq$  *reseed\_interval*, then reseeding could not be done in step 14 (below) during the previous call because of no available entropy source.

4. If (*requested\_strength* > *strength*), then **Return** (“Invalid *requested\_strength*”, *Null*).
5. If (*requested\_no\_of\_bits* > *max\_length*), then **Return** (“Too many bits requested”, *Null*).
6. If ((*prediction\_resistance\_request\_flag* = *Provide\_prediction\_resistance*) and (*prediction\_resistance\_flag* = *No\_prediction\_resistance*)), then **Return** (“Prediction resistance capability not instantiated”, *Null*).
7. If ((*reseed\_counter*  $\geq$  *reseed\_interval*) OR (*prediction\_resistance\_request\_flag* = *Provide\_prediction\_resistance*)), then

Comment: If reseeding is not available.

**Return** (“DRBG can no longer be used. Please re-instantiate or reseed.”, *Null*).

Comment: If reseeding is readily available.

7.1 *status* = **Reseed\_KHF\_DRBG** (*state\_handle*, *additional\_input*, *mode*).

7.2 If (*status*  $\neq$  “Success”), then **Return** (*status*, *Null*).

Else

7.4 If (*additional\_input*  $\neq$  *Null*), then (*K*<sub>0</sub>, *K*<sub>1</sub>, *V*) = **Update** (*additional\_input*, *K*<sub>0</sub>, *K*<sub>1</sub>, *V*).

8. *temp* = *Null*.

9. While (**len** (*temp*) < *requested\_no\_of\_bits*) do:

9.1 *V* = **KHF** (*K*<sub>0</sub>, *K*<sub>1</sub>, *V*).

9.2 *temp* = *temp* || *V*.

10. *pseudorandom\_bits* = Leftmost (*requested\_no\_of\_bits*) of *temp*.

11. (*K*<sub>0</sub>, *K*<sub>1</sub>, *V*) = **Update** (*additional\_input*, *K*<sub>0</sub>, *K*<sub>1</sub>, *V*).

12. *reseed\_counter* = *reseed\_counter* + 1.

13. *state*(*state\_handle*) = {*V*, *K*<sub>0</sub>, *K*<sub>1</sub>, *strength*, *reseed\_counter*, *prediction\_resistance\_flag*, *transformed\_entropy\_bits*}.

14. **Return** (“Success”, *pseudorandom\_bits*).

If an implementation will never provide *additional\_input*, then the *additional\_input* input parameter, and steps 7.3 and 7.4 may be omitted, and a *Null* string replaces the *additional\_input* in step 7.1 .

If an implementation does not need the *prediction\_resistance\_flag*, then the *prediction\_resistance\_flag* may be omitted as an input parameter, and step 6 may be omitted.

If an implementation does not have a reseeding capability, then steps 7.1-7.3 may be omitted.

#### 10.1.4.3.6 Generating Pseudorandom Bits Using HMAC\_DRBG(...)

The following process or an equivalent **shall** be used to generate pseudorandom bits. Let **HMAC (...)** be an Approved HMAC function.

### **HMAC\_DRBG(...):**

**Input:** integer (*state\_handle*, *requested\_no\_of\_bits*, *requested\_strength*, *additional\_input*, *prediction\_resistance\_request\_flag*, *mode*).

**Output:** string (*status*, *pseudorandom\_bits*).

#### **Process:**

1. If ((*state\_handle* > *max\_no\_of\_states*) or (*state*(*state\_handle*) = {*Null*, *Null*, 0, 0, 0, *Null*}), then **Return** ("State not available for the indicated *usage\_class*", *Null*).

Comment: Get the appropriate *state* values.

2.  $V = \text{state}(\text{state\_handle}).V$ ,  $K = \text{state}(\text{state\_handle}).K$ ,  $\text{strength} = \text{state}(\text{state\_handle}).\text{strength}$ ,  $\text{reseed\_counter} = \text{state}(\text{state\_handle}).\text{reseed\_counter}$ ,  $\text{prediction\_resistance\_flag} = \text{state}(\text{state\_handle}).\text{prediction\_resistance\_flag}$ ,  $\text{old\_transformed\_entropy\_bits} = \text{state}(\text{state\_handle}).\text{transformed\_entropy\_bits}$ .
3. If (*requested\_strength* > *strength*), then **Return** ("Invalid *requested\_strength*", *Null*).
4. If (**len** (*additional\_input*) > *max\_additional\_input\_length*), then **Return**("*additional\_input* too long.", *Null*)
5. If (*requested\_no\_of\_bits* > *max\_length*), then **Return** ("Too many bits requested", *Null*).
6. If ((*prediction\_resistance\_request\_flag* = *Provide\_prediction\_resistance*) and (*prediction\_resistance\_flag* = *No\_prediction\_resistance*)), then **Return** ("Prediction resistance capability not instantiated", *Null*).
7. If ((*reseed\_counter* ≥ *reseed\_interval*) OR (*prediction\_resistance\_request\_flag* = *Provide\_prediction\_resistance*)), then

Comment: If reseeding is not available.

**Return** ("DRBG can no longer be used. Please re-instantiate or reseed.", *Null*).

Comment: If reseeding is readily available.

7.1  $\text{status} = \text{Reseed\_HMAC\_DRBG}(\text{state\_handle}, \text{additional\_input}, \text{mode})$ .

7.2 If (*status* ≠ "Success"), then **Return** (*status*, *Null*).

7.3  $\text{additional\_input} = \text{Null}$ .

Else

7.4 If (*additional\_input* ≠ *Null*), then (*K*, *V*) = **Update** (*additional\_input*, *K*, *V*).

8.  $\text{temp} = \text{Null}$ .

9. While (**len** (*temp*) < *requested\_no\_of\_bits*) do:

9.1  $V = \text{HMAC}(K, V)$ .

9.2  $\text{temp} = \text{temp} \parallel V$ .

10.  $\text{pseudorandom\_bits} = \text{Leftmost}(\text{requested\_no\_of\_bits}) \text{ of } \text{temp}$ .

11. (*K*, *V*) = **Update** (*seed\_material*, *K*, *V*).

12.  $\text{reseed\_counter} = \text{reseed\_counter} + 1$ .

13. *state(state\_handle) = {V, K, strength, reseed\_counter, prediction\_resistance\_flag, transformed\_entropy\_bits}.*

14. **Return** ("Success", *pseudorandom\_bits*).

If an implementation will never provide *additional\_input*, then the *additional\_input* input parameter, and steps 7.3 and 7.4 may be omitted, and a *Null* string replaces the *additional\_input* in step 7.1 .

If an implementation does not need the *prediction\_resistance\_flag*, then the *prediction\_resistance\_flag* may be omitted as an input parameter, and step 6 may be omitted.

If an implementation does not have a reseeding capability, then steps 7.1-7.3 may be omitted.