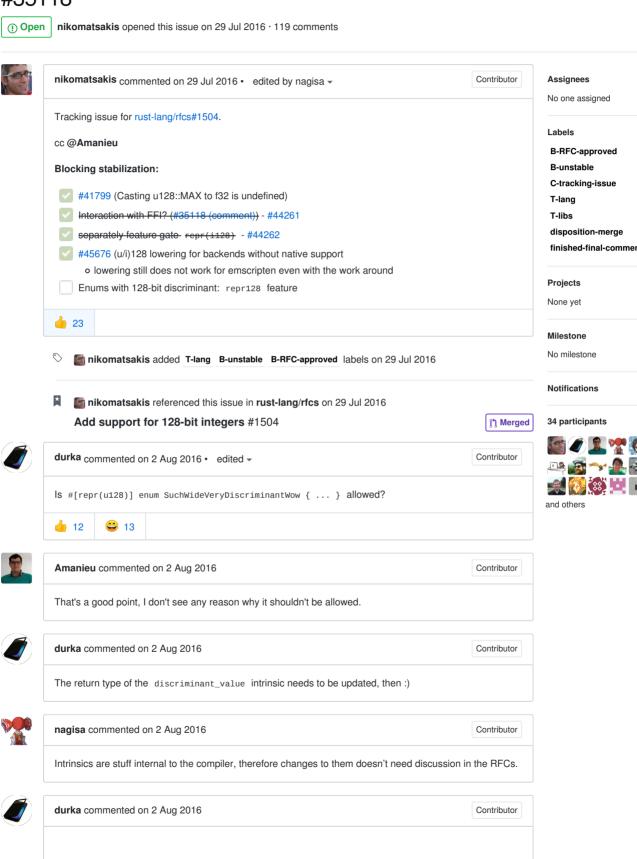
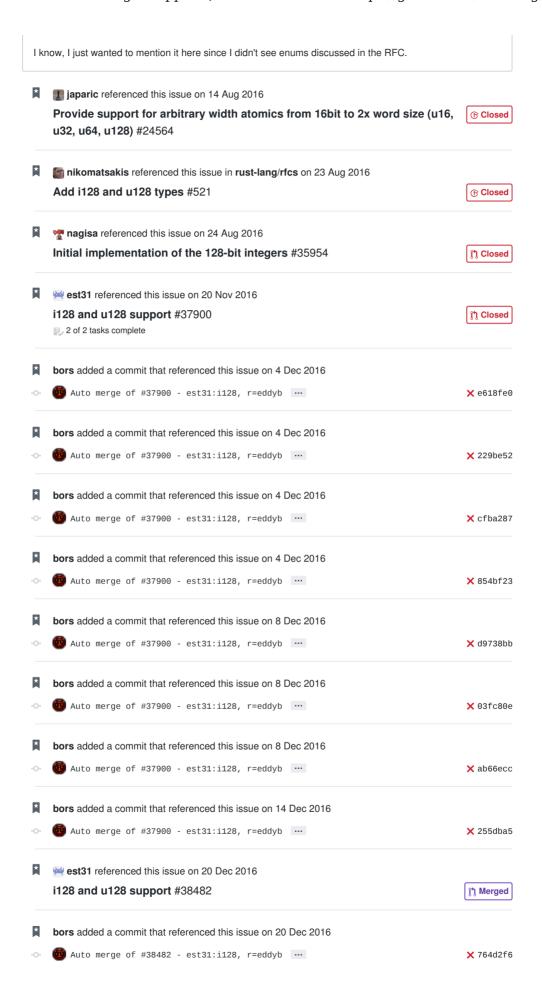
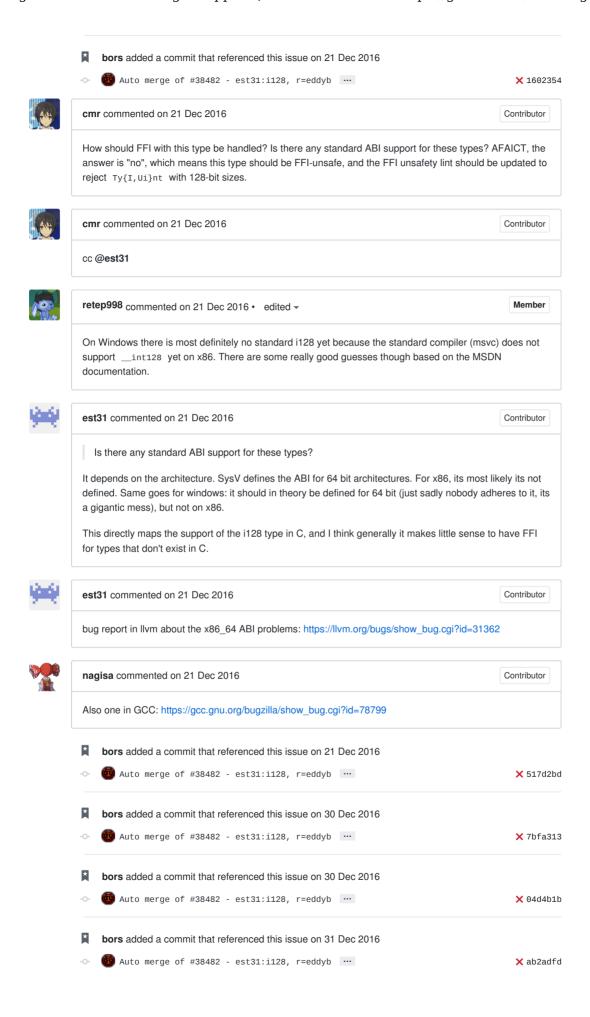
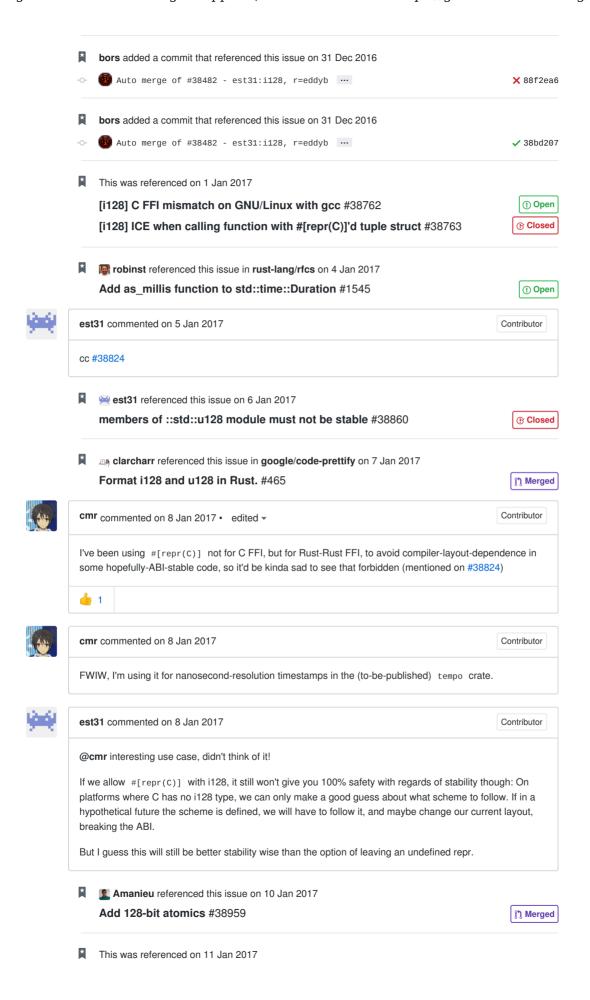
📮 rust-lang / rust

Tracking issue for 128-bit integer support (RFC 1504) #35118









(F) Closed Cannot write integer literal for the minimum i128 (Closed Constants seem to be broken in nightly Rust on powerpc-unknown-linuxgnu #39061 ① Open Built-in u128 performance compared to extprim crate #39078 clarcharr commented on 18 Jan 2017 Contributor @durka imho it makes the most sense to keep the bound at u64 and just let the compiler fit all of the variants into that u64 because while the user "theoretically" can put more than a 64-bit integer's worth of variants in, the compiler absolutely can't handle that because it's running on a 64-bit system. it may require the compiler not using the user's defined integer values for the enum as the discriminant values, though, at least in the case of u128 I honestly don't mind things being a tad slower for 128-bit enums whose variants are not unique in the first 64 bits because that's such an edge case that it doesn't really matter as long as it's reasonably fast durka commented on 18 Jan 2017 Contributor @clarcharr you don't need to have so many variants, as you could do #[repr(u128)] enum Foo { A = 0x_very_large_number_here } clarcharr commented on 18 Jan 2017 • edited ▼ Contributor @durka that was actually the point; for u128 you'd incur a slight performance penalty to allow the discriminant to not reflect the actual value of the enum, to avoid the performance penalty of the discriminant intrinsic using non-native integer arithmetic every single time on 64-bit CPUs (I could be totally wrong about the performance loss though; maybe it's not that much) durka commented on 18 Jan 2017 Contributor Well, my ideal solution to the issue is to use an intrinsic trait to let `discriminant_value` have a differing return value per enum :) est31 commented on 18 Jan 2017 Contributor @durka a trait would be ideal, but wouldn't that require an RFC? And, for backwards compatibility sake (see #39137) it would probably have to return u64 for all non 128-bit types. [Q clarcharr commented on 18 Jan 2017 Contributor @est31 see @nagisa's comment; intrinsics don't require RFCs. and the Discriminant struct is intentionally opaque to avoid backwards-compatibility problems est31 commented on 18 Jan 2017 Contributor @clarcharr apparently they do: rust-lang/rfcs#1696

clarcharr commented on 18 Jan 2017

Contributor

@est31 I think you're mistaken; that's not an intrinsic, that's a function in std::mem . intrinsics like discriminant_value are not stable and can be changed without RFC, whereas functions like that are on the path for stability so that users can use them. that's why additional care is put into making the struct returned by discriminant opaque so that users can't just inspect it and assume that it'll always be a u64

kkimdev referenced this issue in BurntSushi/byteorder on 21 Jan 2017 u128 i128 support #65

() Closed



clarcharr commented on 27 Jan 2017

Contributor

As brought up in #39324, I think that a generic cfg(has_i128) flag or something similar is necessary if we want to ensure forwards-compatibility for platforms which don't have 128-bit integer support.

Right now I'm thinking things like embedded devices. For example, in the PR I linked, it might be reasonable to assume that a system would have IPv6 support without needing full-blown 128-bit integer support.



clarcharr commented on 27 Jan 2017

Contributor

Additionally, also from that PR, I think that'd be nice if there were a better error for 128-bit integer literals that aren't annotated with u128 or i128 . Right now it just says that the integer is too big but doesn't clarify that it fits in a 128-bit integer.

Either that, or we could allow non-annotated integer literals to coerce to i128 and then add a lint-bydefault that warns that the literal is larger than u64.



nagisa commented on 27 Jan 2017

Contributor

You can always implement i128 operations in terms of operations on i64, and then operations on i64 in terms of operations on i32, and then i32 in terms of operations on i16. You can also in the other direction and implement i256 operations in terms of operations on i128, i512 in terms of i256 ad nauseam. They might not be fast, but certainly not impossible to support.

One may have concerns about ABI for i128 not being specified for some targets, but that does not prevent i128 from being used within Rust code with some arbitrary, but consistent ABI.

So... if rust isn't supposed to run on machines with literally less than 16 bytes of memory (plus the memory necessary to do the operations, of course) it is literally impossible to have a target which cannot support

If you don't know of any such machine, then please stop throwing portability concerns all over the place, thanks! (@brson, @alexcrichton)

There's #38824, which some may be inclined to cite as an example of platforms which do not support i128. They do support it just fine. LLVM backends for those targets are buggy, that's all.



<u></u> 8



brson commented on 28 Jan 2017

Contributor

Thanks for the clarifications @nagisa.

Here's what I'm thinking more precisely: when our original selection of atomics were conservative because of portability concerns. When we added additional atomics to the language, we put them behind CPU features (see here for an example).

128-bit integers strike me as a very similar case. They support a feature that is not universal and will need to be emulated on many architectures if they are not cpu-specific.

Often in Rust we put value on having the language closely represent the hardware it targets. There's e.g. no way to emulate atomics in Rust at all today.

So I hope you will see the similarity between these two cases and why I might expect 128-bit integers to be treated similarly.



brson referenced this issue on 28 Jan 2017

lpv6Addr <-> u128 #39324





retep998 commented on 28 Jan 2017

Member

I don't see atomics and i128 support as the same thing. Atomics are the kind of thing where you *need* hardware support or you simply cannot do the operation atomically. i128 meanwhile can very easily be emulated with smaller integer operations. Aside from LLVM failing hard, the worst that can happen is an i128 operation will run slow. We already emulate many i64 ops on 32-bit platforms, so what makes i128 so special?



<u></u> 13



est31 commented on 28 Jan 2017

Contributor

@brson

128-bit integers strike me as a very similar case. They support a feature that is not universal and will need to be emulated on many architectures if they are not cpu-specific.

I think the main use case for 128-bit integers is to be able to do operations on them without having to emulate them yourself or using libraries that do it for you. If your program then can't run on various targets its not very good I think.

I also agree with @retep998 that 128-bit integers are less similar to atomics, and more similar to 64-bit or 32-bit integers, which are currently already emulated on 32 bit and 16 bit CPUs.

In fact, in my PR to the soon-to-be-used compiler-builtins library, I was able to use the same generic macro (link goes to the multiplication, but the other operations were similar) for both i128 and 64 bit integer operations, for <code>mulo{s,d,t}i4</code> even for 32 bit integers.

Also, the main problem that breaks i128 integer support on these platforms is not missing emulation of operations. Its already available and well working for rust's tier1 32 bit targets. The problem is rather the missing support by LLVM to handle such large integers in the only task that is left to LLVM: hauling the value around. From heap to stack, from one function to the other, doing the calculations like how much to reserve in the stack. All these are tasks that should easily scale to 128 bit integers, all it needs is patching LLVM

The core reason for why LLVM doesn't support 128-bit integers on those targets is that C doesn't officially specify them, and only some compilers provide it unofficially. If we make 128-bit integers platform dependent, we won't improve much on C in this regard.



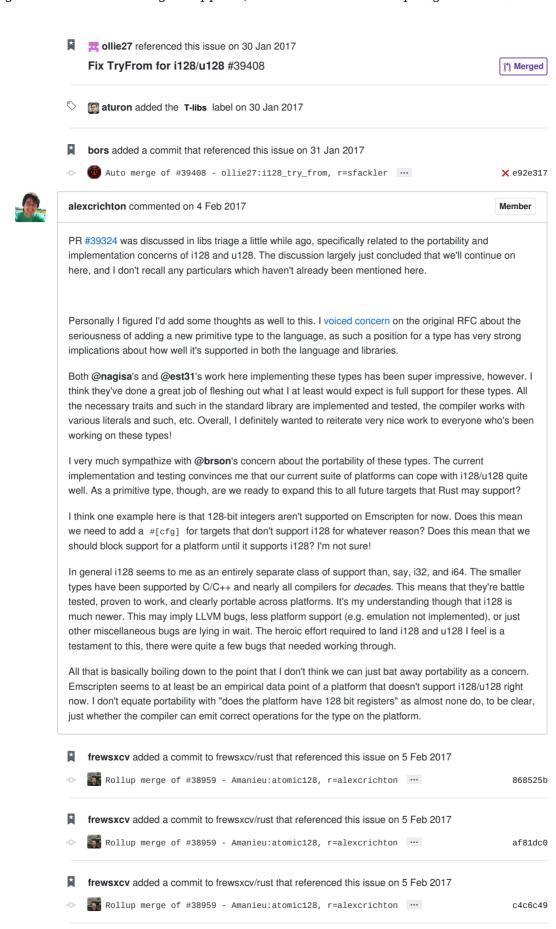


SimonSapin commented on 28 Jan 2017

Contributor

Aren't u64 and i64 already implemented "in software" on some of our supported targets? If so, why should 128 be any different?

√ fc02736



bors added a commit that referenced this issue on 5 Feb 2017

Auto merge of #39408 - ollie27:i128_try_from, r=alexcrichton



est31 commented on 7 Feb 2017

Contributor

I've filed a bug report for emscripten at kripken/emscripten-fastcomp#169



scottmcm commented on 7 Feb 2017

Member

128-bit integers aren't supported on Emscripten

Javascript targets already need to emulate the 64-bit types, right? It feels like gating types on any definition of "nice platform support" (be that registers, instructions, or what) means that the 64-bit ones also ought to be cfg d. (Silly thought experiment: if I made a rust to T-SQL stored procedure compiler, would the unsigned types need to be cfg d?) The restrictions the library imposes on usize assumptions (might be smaller than u16) suggest that perhaps even 32-bit stuff ought to be cfg d under that gate criteria.

I think that cfg hiding i32 (or even u64) is pretty crazy, so that implies that things can be primatives if they're broadly reasonable things to use. And are integers bigger than 64-bit (but not BigInteger) reasonable? I think they are; for example std::time::Duration is already emulating u96 (well, partially, and more like u93.897 ...) in the standard library. ZFS, with >64-bit storage, is over 10 years old now.

Let's not add 256-bit integers, though :P

(Hmm, a three-i53 Javascript emulation of u128 would probably be no worse, and plausibly would be better, than the emulated-u64 plus u32 stuff it needs to do now for Duration—not that duration math is going to be anyone's performance bottleneck.)





nikomatsakis commented on 7 Feb 2017

Contributor

@scottmcm I think that @alexcrichton's point was not whether some emulation is required, but whether emulation exists and is reliable:

I don't equate portability with "does the platform have 128 bit registers" as almost none do, to be clear, just whether the compiler can emit correct operations for the type on the platform.

As he wrote:

In general i128 seems to me as an entirely separate class of support than, say, i32, and i64. The smaller types have been supported by C/C++ and nearly all compilers for decades. This means that they're battle tested, proven to work, and clearly portable across platforms.



nikomatsakis commented on 7 Feb 2017

Contributor

My take: I am persuaded by the analogy to emulating u64 on a 32-bit system, but I am also persuaded that 128-bit support is going to be less widespread and solid than 64-bit support. I want to be clear on what exactly we are debating about:

- Whether a i128 type is available in the lang at all?
- Whether a i128 type is required for all platforms?

I tend to think of things these days in terms of the portability RFC that @aturon has pending. Basically, there are "mainstream" platforms that code targets by default -- if you wish to target something else, you would "opt into" that in some way. This includes both a narrower range of features ("I only want to use things appropriate for 8-bit ATARI CPUs") as well as a wider range of features ("I am focused on Windows, so I don't care about unix compatibility").

In this case, I would think that we should definitely make it possible to eschew using i128 if it seems inappropriate. The RFC leaves that case a bit under-specified, and naturally it is focused on libstd and not the lang, but it seems like the basic idea would be to have a "target feature" for 128-bit support.

Anyway, under this perspective, the main question is whether this target feature ought to be part of our default configuration. It seems to me that this is a fairly straight-forward question -- in theory. =) That is, it follows somewhat mechnically from how well-supported i128 is on the "main platforms". I think historically that's been basically "common desktop/laptop CPUs", but I feel like this is something which isn't totally decided.



alexerichton commented on 7 Feb 2017

Member

Yes to be clear I am personally ok with emulation of i128 on a platform, e.g. x86_64. To support a platform we just have to have working emulation! Emscripten for example I believe is an empirical example of where the emulation does not work (or just isn't implemented).

@nikomatsakis I think you raise some good points. I do think that i128 should be in the language itself, as it has clear benefits with hardware support in various instructions. Even if it's emulated, the compileremulated version has historically purportedly been superior to library emulation. To me this is a question of how we talk about platform compatibility of i128.

I do think you also raise a good point with @aturon's RFC, and in that case it's just a question (for now) as to whether i128 is in the mainstream "std" scenario or not. Put another way, does this compile by default?

```
fn main() {
   println!("{}", 1i128);
```

My gut says that "yes", we want i128 in the mainstream scenario. The impressive work done to pass all the test suites on all our tier 1 platforms I think is a testament to that! I do think, however, that we'll want to document that maximally portable code may not wish to use i128. New Rust platforms may not have the emulation working quite yet or may not just be battle tested much.

In that sense I see i128 in the same class of support as std::thread . It's available for mainstream use cases and we'll test to make sure it works. If you want to work everywhere (or as many places as possible) you may be best off avoiding it. We wouldn't require i128 support to add a target to the compiler, just as some targets don't have std::thread or even much float support.



<u>6</u> 2



aturon commented on 7 Feb 2017

Member

@alexcrichton

My gut says that "yes", we want i128 in the mainstream scenario. The impressive work done to pass all the test suites on all our tier 1 platforms I think is a testament to that! I do think, however, that we'll want to document that maximally portable code may not wish to use i128. New Rust platforms may not have the emulation working quite yet or may not just be battle tested much.

This is precisely my view as well. In particular, that means we should feel free to use the type where appropriate in std; I believe there are some methods we'd like to add to Duration, for example, that would benefit from this type.

So I personally would say that once we feel completely confident in the implementation on tier 1 platforms, we can go forward with stabilization.

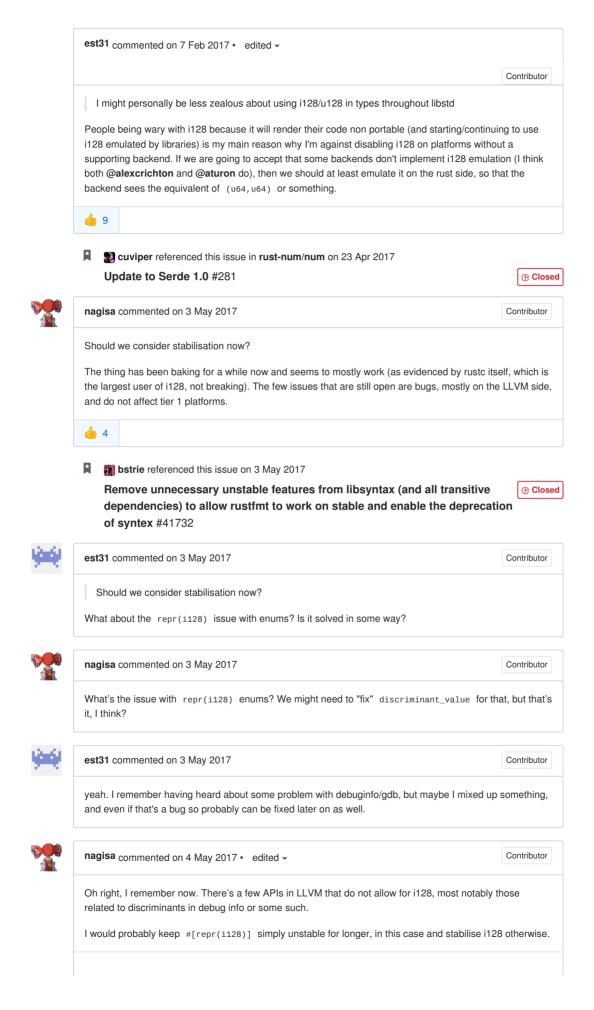


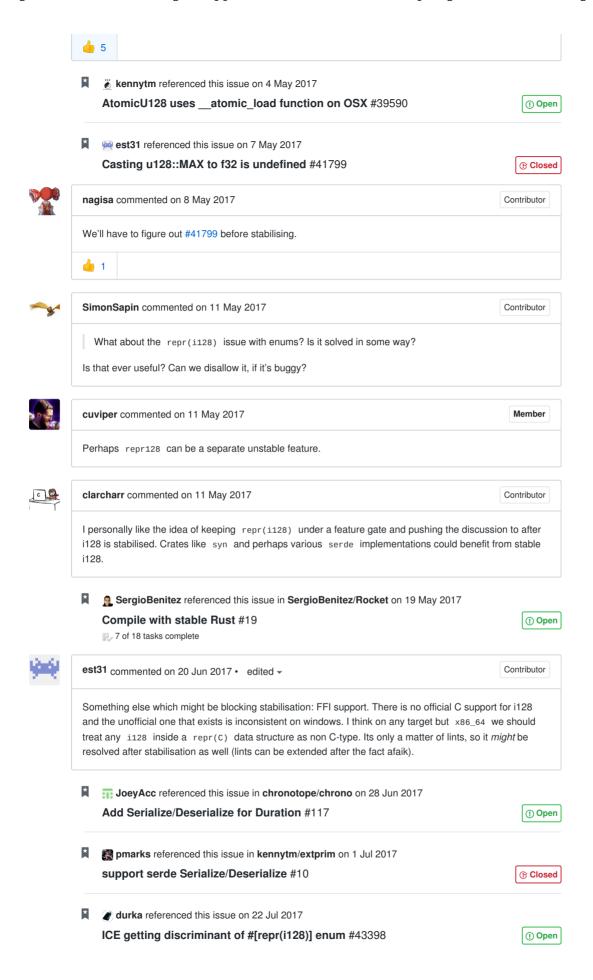


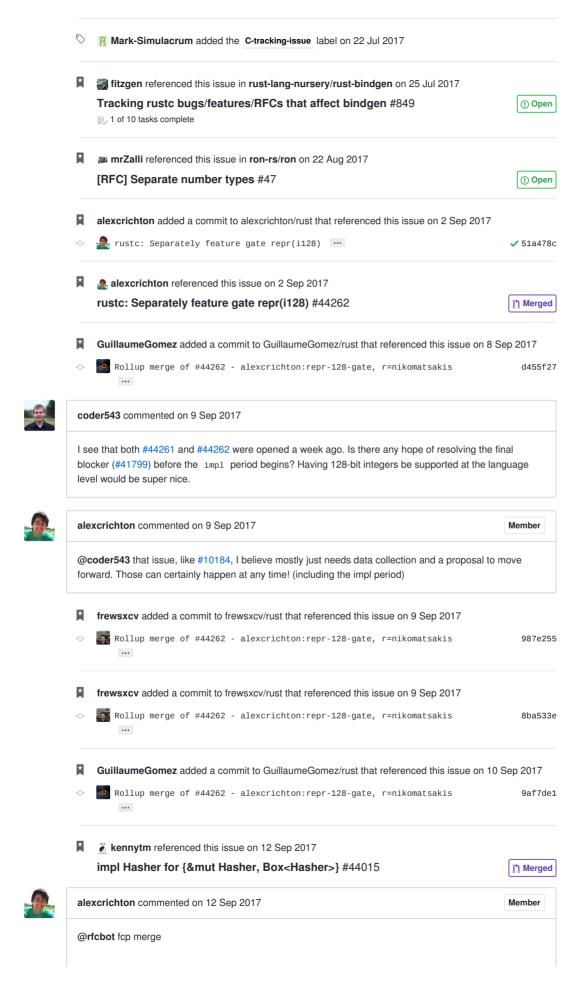
alexcrichton commented on 7 Feb 2017

Member

@aturon I might personally be less zealous about using i128/u128 in types throughout libstd (due to the possible portability concerns), but we can always cross that bridge when we get there :)







Ok this has been sitting for some time now and hasn't seen a whole lot of activity, but that being said I don't think we've seen many surprises or bugs with the 128-bit integers so far. We've long had to write our own support in compiler-rt but that's now done in the compiler-builtins project where we write many of the intrinsics ourselves (and hopefully is a bit more cross-platform!).

As expected there are LLVM targets that have yet to implement support for i128/u128, ranging from embedded targets like AVR to "weird" targets like NVPTX to larger ones like Emscripten. Despite this, however, the presence of 128-bit integers I feel doesn't preclude Rust working on these targets. It's already the case that an arbitrary library won't work on one of these targets today, and I don't think i128/u128 will make the situation worse or better! Some points on this though:

- Right now libcore doesn't have a compilation profile where 128-bit integers are omitted, but I personally feel this is pretty reasonable to add in the future.
- Maximally portable code will likely still not use 128-bit integers, for example we likely won't use it in the standard library for these reasons (e.g. we want libstd working with Emscripten). Many codebases, however, don't need that level of portability, and can certainly benefit from i128/u128!

Of the remaining blockers listed for stabilization on this issue only one remains, #41799. This to me is an open question in Rust that's *already* a problem with issues like #10184 and #15536. Like the portability issue, I don't think i128/u128 are making this story worse than it is today, and presumably whatever solution we come up with in the future will naturally extend to 128-bit integers as well. Along these lines, I'd propose stabilizing 128-bit integer support before requiring this to be fixed.

So concretely what's being proposed for stabilization is:

- The i128 and u128 types
- Language level support for mathematical operations on these types and casts
- All various API support in libstd/libcore, e.g. the i64/u64 API but reflected on 128-bit types
 - o i128 primitive type
 - o u128 primitive type
 - o i128 module
 - o u128 module
 - o impls like From<u128> for Ipv6Addr

What is not being stabilized is:

- #[repr(i128)] on an enum, this is behind a separate feature gate.
- 128-bit integers as "ffi safe types" so we have the freedom to tweak the API in the future as necessary.

I'm curious to hear what others think about this!



rfcbot commented on 12 Sep 2017 · edited by aturon -

Team member @alexcrichton has proposed to merge this. The next step is review by the rest of the tagged teams:

- @BurntSushi
- @Kimundi
- @alexcrichton
- @aturon
- @cramertj
- @dtolnay
- @eddyb
- @nikomatsakis
- @nrc
- @pnkfelix
- @sfackler
- @withoutboats

No concerns currently listed.

Once these reviewers reach consensus, this will enter its final comment period. If you spot a major issue that hasn't been raised at any point in this process, please speak up!

See this document for info about what commands tagged team members can give me.

 \Diamond

fricbot added the proposed-final-comment-period label on 12 Sep 2017



dtolnay commented on 12 Sep 2017

Member

What does it look like when this fails on "weird" targets? So if I add u128 impls to Serde, does that mean Serde no longer compiles on many targets? Is there a target_feature or similar gate to do this correctly without a Cargo cfg?



SimonSapin commented on 12 Sep 2017

Contributor

As expected there are LLVM targets that have yet to implement support for i128/u128

Isn't there a "software" fallback for architectures that do not have CPU instructions for 128-bit arithmetic?



rkruppe commented on 12 Sep 2017 • edited ▼

Contributor

@SimonSapin Yes, that's the compiler-rt/compiler_builtins thing mentioned above. However, targets still need to handle 128 bit types in some contexts (e.g., in ABI lowering) and emit calls to the software implementation of various operations. See #41132 for an example of what it looks like when the backend doesn't handle i128 (in short: a very ugly and nonsensical crash deep in the backend).



est31 commented on 12 Sep 2017

Contributor

@SimonSapin I'm not aware of a target that has native 128-bit integer arithmetic support (native meaning in this context that common operations like addition, multiplication, division, etc are possible in one single instruction). Instead, operations are expressed through their smaller counterparts (example). For the platforms that have support for that emulation, the backend is compatible and either lowers operations to calls to compiler_builtins functions (provided by us), or figures out its own best way to do lowering if its very smart. However, the backend needs to provide *some* support of its own, like specifying how something should be returned, and obviously it shouldn't also give an assertion failure when being given 128 bit integers.

Thanks to register allocation, each target that has enough memory (as in RAM memory, not register memory) to hold 128 bit integer operands is generally able to provide such support. There is nothing *preventing* AVR or emscripten backends to implement 128 integer support, its more a question of doing the work.

In fact, this emulation is already present for 32 bit integers on targets that have no native 32 bit instructions. On such targets, 64 bit integers are expressed through 16 bit instructions as well! The actual algorithms we provide in libcompiler_builtins are generic and handle the one operation on 2 * x bits -> multiple operations on x bits step.

@alexcrichton

Right now libcore doesn't have a compilation profile where 128-bit integers are omitted, but I personally feel this is pretty reasonable to add in the future.

I want to say that the proposal is sensible; wasm is obviously of higher priority than support for 128 bit integers. However if i128 is not guaranteed, it will have a chilling effect on adoption, where people avoid the language-native feature, in order to be cross-platform. Those who have a pressing need for 128 bit integers would instead choose emulation via external crates (there are ones already on crates.io for this). That's obviously not a good outcome for 128 bit integers!

In order to fight this, we have two options:

- Try to do some compiler-side emulation, where we give the backend the same stuff we'd give it for (u64, u64). The advantage is that we could always provide i128 support to users. But this is likely a big amount of work, as it might consist of adding a lot of special cases.
- · Fix the backends.



nagisa commented on 12 Sep 2017 · edited -

Contributor

,I'm super strongly opposed on support, conditional on the target. I'd much rather "fix" the shoddy LLVM backends somehow. If we manage to fix all the backends and slip in a test with a comment that Rust requires i128 to be properly supported to LLVM upstream, it would be ideal.

Otherwise the best next approach seems to just invent our own ABI for those bad targets and lower to compiler-rt calls ourselves during translation rather than relying on LLVM to do it. Hopefully just passing i128 by reference would be enough.





alexcrichton commented on 12 Sep 2017

Member

@est31 yeah it's true that it may hinder adoption if we don't guarantee that, and that's a good question for stabilization! I'm personally proposing stabilization based on the condition it'd still have a warning "this may not be available on all platforms" as it, to me, doesn't seem like it should preclude usage on tier 1 platforms that have the support.

Note that a crates.io fallback though may not be the end of the world, it could presumably use emulation on any target that doesn't support i128 and use i128 natively on any target that does support it, presumably achieving the same level of performance?

I definitely agree that ideally rustc and/or LLVM would fix everything here for us, and this may be the question that makes or breaks the proposal for stabilization here. If we'd like to require that then we can't stabilize this as there's work yet to be done!



est31 commented on 13 Sep 2017

Contributor

this may be the question that makes or breaks the proposal for stabilization here

Feel the same, I think we should do what @nagisa is suggesting. I think now that I have asked too early for stabilization...

Note that a crates.io fallback though may not be the end of the world, it could presumably use emulation on any target that doesn't support i128 and use i128 natively on any target that does support it, presumably achieving the same level of performance?

A library solution would probably achieve the same performance level, but I think the whole point of a "native" i128 type is that its nicer. E.g. you can directly have literals like for any other integer type, or you have all the functions implemented that are implemented for normal integers, etc. Also libraries like serde will more likely give support to i128 if its part of the language...



sfackler commented on 18 Sep 2017

Member

I'm uncomfortable landing this if it's not supported in some way on all platforms.

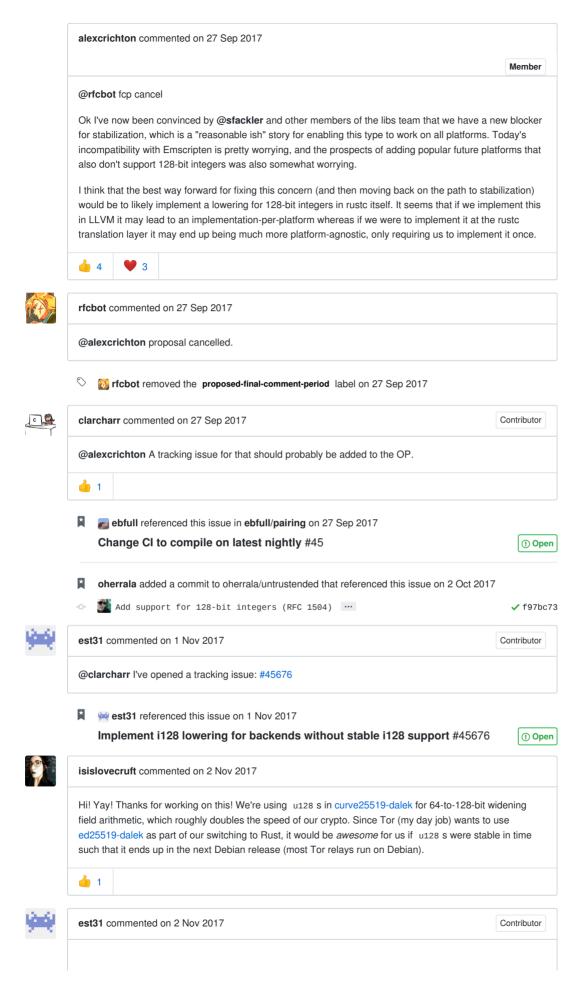


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asterite referenced this issue in crystal-lang/crystal on 22 Sep 2017

Increase the precision of Time and Time::Span to nanoseconds #5022

Merged



@isislovecruft I'm not familiar with inner debian workings, but stretch has entered soft freeze 8 months after the release of the previous version. So probably soft freeze will be somewhere in February. From that deadline you also need to subtract the delay created by the debian packaging team for the compiler. It takes 7-13 weeks for a change in the compiler on the master branch to appear inside an official release of Rust. It will be very tight...



fschutt commented on 7 Nov 2017

Contributor

A question: Is multiplying a i64 as i128 * i64 as i128 = i128 stable? I am looking to replace this:

https://github.com/fschutt/clipper-rs/blob/master/src/cpp/clipper.cpp#L354-L378

... and wanted to ask if (at least on x86 and x64) this is stable enough or if I should roll my own i128.



sfackler commented on 7 Nov 2017

Member

@fschutt the i128 type is not stable.



Natim referenced this issue in skade/rust-three-days-course on 8 Nov 2017

Remove the soon for u128 in the basic-types.chapter #108

Merged



bstrie commented on 11 Nov 2017

Contributor

@isislovecruft Is it extraordinarily imperative that this get into the next Debian? Normally unstable features will spend a full release cycle in FCP, which would mean that i128 would hit stable on Feb 15 at the absolute earliest. To hit the prior stable release on Jan 4 we'd need to promote this to beta during our next cutoff on Nov 23, giving us 13 days to make a decision (and note I speak on behalf of neither the lang team nor the libs team). Normally I'd say that's a completely unreasonable target, and it is *kinda* unreasonable (PP), but given how much we like Tor (and seeing Rust get used in big-name OSS projects in general, of course!), it *might* be possible to rush given that 1) this feature is, conceptually, minor; 2) the blocker at #41799, despite being a soundness bug, is just a subset of literally Rust's oldest known soundness bug and so it *might* be argued that this adds no new unsoundness; and 3) the blocker at #45676 doesn't affect any tier-1 platforms and so it *might* be argued that we could weather a short period of this feature being somewhat non-portable in the wild. But rushing something like this would take a lot of convincing, so I'd only bother if it were incredibly imperative to Tor's ongoing use of Rust.:)



vitiral commented on 28 Nov 2017

Contributor

it looks like #41799 has recently been closed + merged, which leaves just the > tier-1 platforms as the blocker (not saying this means this should be stabilized yet, just a heads up).



nagisa commented on 28 Nov 2017

Contributor

We still need to figure out what platforms need the manual lowering.

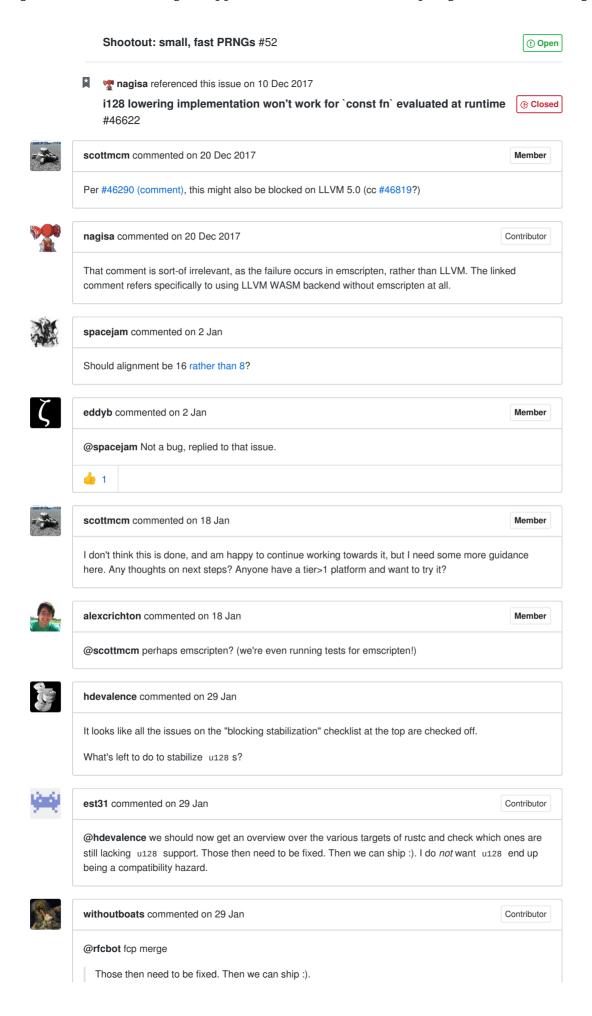


est31 commented on 29 Nov 2017

Contributor

We also need to check whether the stuff we implemented for those platforms is enough, or whether more work to support them is needed. We can only consider this to be finished if i128 is not commented out in libcore on any platform any more.

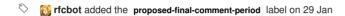
Mardy referenced this issue in dhardy/rand on 29 Nov 2017



Since shipping takes roughly 3 months, it seems like a good idea to mobilize now and light a fire under ourselves to actually finish this.

We knew emscripten needed this, does the new wasm target need it as well? Comments on this thread make it seem like these are the only platforms we consider supported that this is a problem for, if there are others, add them to the list.







rfcbot commented on 29 Jan • edited by cramertj •

Team member @withoutboats has proposed to merge this. The next step is review by the rest of the tagged teams:

- @BurntSushi
 - @Kimundi
- @KodrAus
- @alexcrichton
- @aturon
- @cramerti
- dtolnay
- @eddyb
- @nikomatsakis
- @nrc
- @pnkfelix
- @sfackler
- @withoutboats

No concerns currently listed.

Once a majority of reviewers approve (and none object), this will enter its final comment period. If you spot a major issue that hasn't been raised at any point in this process, please speak up!

See this document for info about what commands tagged team members can give me.



Est31 commented on 30 Jan • edited ▼

I know of the following backends:

NVPTX support (#38824, #41132)

emscripten support (kripken/emscripten-fastcomp#169)

atmel backend support (avr-rust#36, avr-rust#57, avr-rust/libcore#5, avr-rust#94)

verify that unknown wasm support is working (verified: #35118 (comment))

mips (#41222)

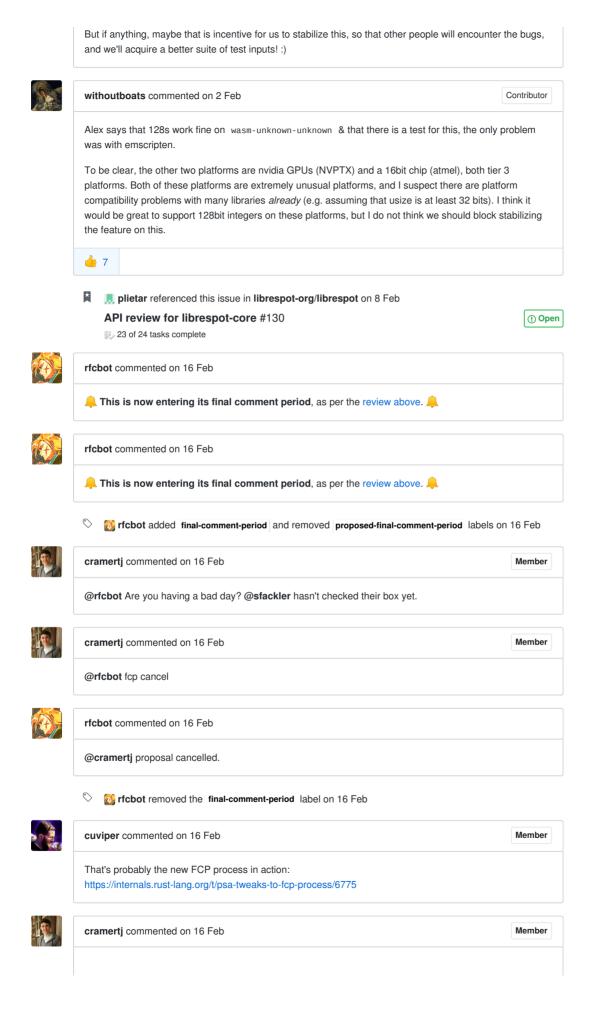
But maybe there are more.

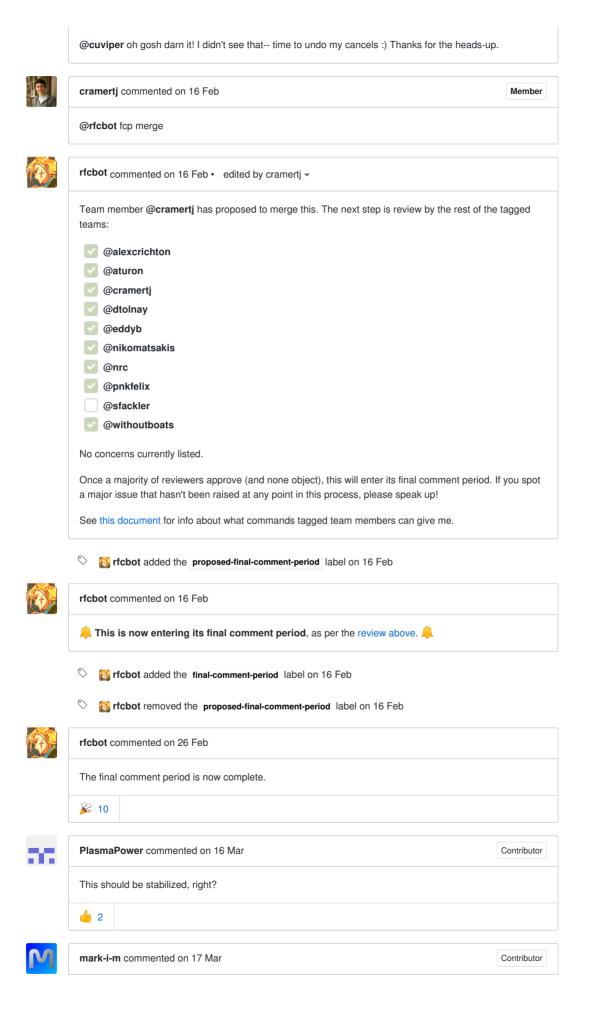


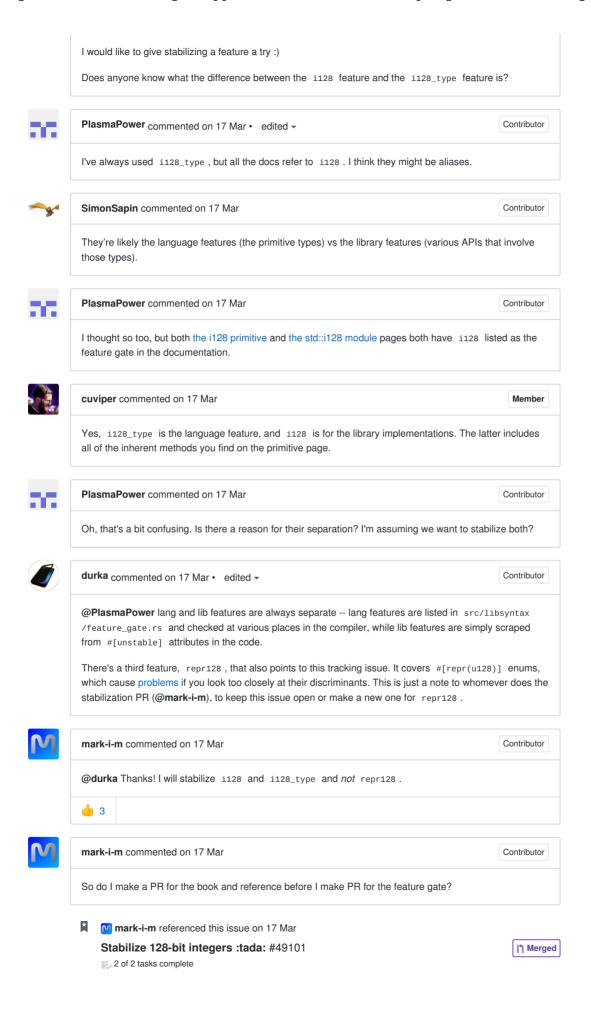
pnkfelix commented on 31 Jan

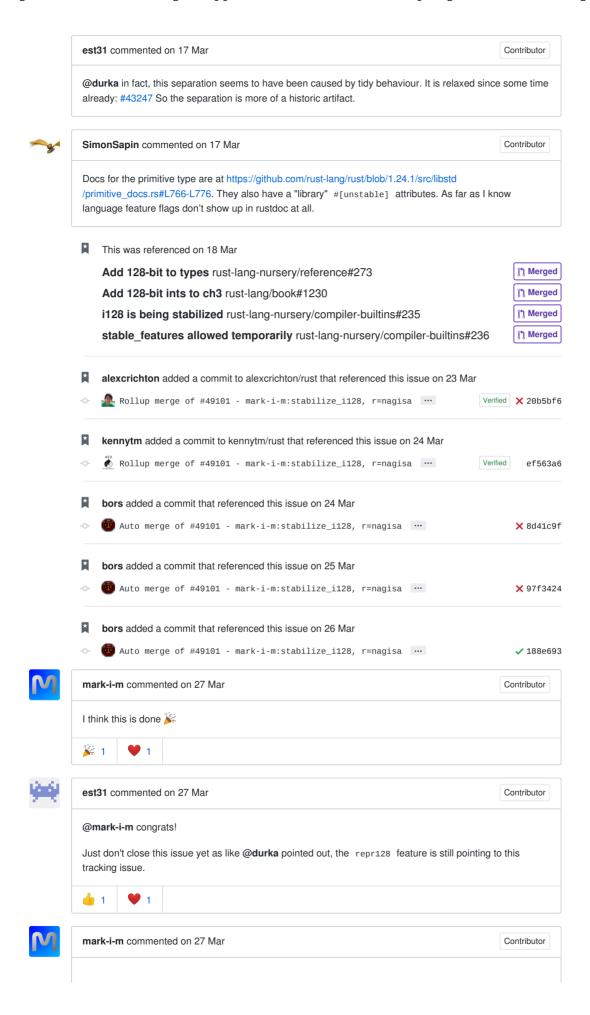
Member

I am a little worried that I know I've been wrestling with a code gen bug in the code generated for <code>rustc</code> itself that seems somewhat likely to be related to issues with LLVM and <code>i128</code>. (See #47381)









Contributor

Could someone update the OP too create more check boxes for repr128? Contributor ebfull commented on 29 Mar The checkbox for #45676 is checked in this issue but #45676 is not actually closed. What's the status of lowering on other platforms? nagisa commented on 29 Mar Contributor I think that is fixed, although not in the nicest way. I closed the issue. est31 commented on 29 Mar Contributor @ebfull see my comment here: #35118 (comment) i128 works on the entire x86 family as well as the ARM family. This covers all of the tier 1 platforms. The platforms where we lack i128 support are tier 2 or 3. What you are doing in ebfull/pairing#80 is exactly what I wanted to prevent by blocking stabilisation until all platforms support i128 lol, but people thought otherwise. ebfull commented on 30 Mar Contributor @est31 I'm not doing ebfull/pairing#80 until it works on all platforms, which I mention in that issue. In the mean time all I will do is remove the i128_type feature flag, but still require users to opt into usage of u128 . Contributor est31 commented on 31 Mar @ebfull not blaming you. I'm partially blaming myself, thought that lowering worked already. Partially I'm blaming those people who decided to stabilize before all platforms support it. And the backend vendors who refuse to take {u,i}128 seriously. Sadly, this is more of a "stable beta" release of i128 than an arrival of a feature that can be relied upon. <u>d</u> 1 hdevalence commented on 2 Apr It would be nice if there was a (documented) way to set a cargo feature as the default on a given architecture. From what I can tell this isn't quite possible, since the architecture-selection code happens with #[cfg(...)] s while the crate is being compiled, by which point the crate's features are already selected. Am I missing something? The motivation is that even if u128 s are available, it may not be desirable to use them, unless they actually correspond to instructions available on that platform.

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durka commented on 2 Apr

You can set cfg flags from the build script.

