Kelsey Ball

kelseytaylorball@gmail.com • kelseyball.com

SKILLS

Building statistical models for machine learning and NLP

Proficient in: Python, Java, C

Previous experience: Scala, C++, SQL, Splunk

AWARDS

U.S. Fulbright Student Finalist, 2017-2018

NSF Graduate Research Fellowship Award, 2018

Teaching Award, Cockrell School of Engineering, 2017

Engineering Honors, UT

SERVICE

Vice President, Women in Electrical and Computer Engineering, 2013-2014

Panelist, CS Research Week, Office of Undergraduate Research

Citizen Science Committee, Longhorn Stream Team 2015-2016

RESEARCH

U.S. Fulbright Scholar — *Hyderabad, India*

FALL 2017 - SPRING 2018

Kelsey Ball, Dan Garrette. "Part-of-speech tagging for code-switched and transliterated text without explicit language identification." In *Proc. of EMNLP*, 2018.

Research Assistant — *University of Texas at Austin*

FALL 2014 - FALL 2015

- Wrote a language tagger to help analyze code-switching in bilingual texts
- Presented at two conferences on language contact and transfer

EXPERIENCE

Texas 4000 for Cancer — *Ride Director, Head Mechanic*

FALL 2015 - FALL 2017

- Cycled over 4,000 miles from Austin, TX to Anchorage, AK
- Co-led team of 25 riders; trained and led crew of 4 mechanics
- Logged over 2,000 training miles and 50 volunteer hours in preparation
- Highest fundraising team in Texas 4000 history with over $800,000 raised for cancer research and support services

Qualcomm Inc. — *Software Development Intern*

SUMMER 2015

- Built an internal testing tool to track optimized kernel parameters between builds
- Top 3 finalist in IdeaQuest competition with BikeSmart, an IoT-connected bike

Epic Systems — *Software Development Intern*

SUMMER 2014

- Prototyped media-sharing web application for radiologists

EDUCATION

University of Texas, Austin — *B.S. Electrical & Computer Engineering*

FALL 2013 - SPRING 2017 • GPA: 3.8/4.0

Relevant Coursework: Computational Linguistics, Probability, Data Mining, Algorithms, Data Structures, Software Testing, Software Design and Implementation, Computer Architecture, Operating Systems, Embedded Systems