

Homework 1

1.

- a) The code fails to comply with the C standard because it attempts to shift beyond the word size. In many systems the shift amount will be applied *mod* the word size so an attempt to shift by 32 with result in a $32 \bmod 32 = 0$ shift.

b) _____

```
int int_size_is_32() {  
    int set_msb = 1 << 31;  
    int beyond_msb = 2 << 31;  
    return set_msb && !beyond_msb;  
}
```

c) _____

```
int int_size_is_32() {  
    int i = 1 << 15;  
    i = i << 15; // 0 if less than 32 bit  
    int j = 4 << 15;  
    j = j << 15;  
    return i && !j;  
}
```

2.

- a) Doesn't perform sign extension.

b) _____

```
int xbyte(packed_t word, int bytenum) {  
    word = word >> (bytenum << 3);  
    word = word << 24;  
    word = word >> 24;  
    return word;  
}
```

3.

- a) The conditional test always succeeds because *sizeof()* returns type *size_t* which is an unsigned data type. So the subtraction will result in an unsigned number which will always be less than or equal to 0.

b) _____

```
void copy_int(int val, void *buf, int maxbytes) {  
    if (maxbytes >= (int)sizeof(val))  
        memcpy(buf, (void *) &val, sizeof(val));  
}
```

4.

- a) $(x \ll 4) + x$
b) $x - (x \ll 3)$
c) $(x \ll 6) - (x \ll 2)$
d) $(x \ll 4) - (x \ll 7)$

5.

- a) $\sim 0 \ll k$

b)

```
int create_val(int i, int j) {  
    r = 0;  
    for (int i = j + 1; i <= j + k; i++) {  
        r += 1 << i;  
    }  
    return i;  
}
```

6.

a) $(x < y) == (-x > -y)$:

When $x = TMin_w$ the first condition will evaluate to true and the second will be false. So this will return 0.

b) $((x + y) << 4) + y - x == 17 * y + 15 * x$:

We know that a bit shift is equivalent to multiplication by powers of 2 and since multiplication is distributive $((x + y) << 4) == (x << 4) + (y << 4)$. So we can replace the left side of the equality with $(y << 4) + y + (x << 4) - x$. On the right side we can rewrite the multiplications as sums of powers of 2. So $17 * y$ becomes $y << 4) + y$ and $15 * x$ becomes $x << 4 - x$. We now see that the left side is equivalent to the right side.

c) $\sim x + \sim y + 1 == \sim (x + y)$:

When $x = y = \sim 0$ (all bits set to 1), the left side of the expression $\sim x + \sim y + 1$ evaluates to 1 but the left side of the equation $\sim (x + y)$ evaluates to ~ 0 (all bits set to 1).

d) $(ux - uy) == -(unsigned)(y - x)$:

e) $((x >> 2) << 2) <= x$:

In any case (where the most significant bit of x is 0 or 1) when we shift left and then right we will fill in 0's on the left side and any added bits will overflow on the right side. So this expression is always true.

7.

8.

9.

10.