Draft Flowchart University at Buffalo

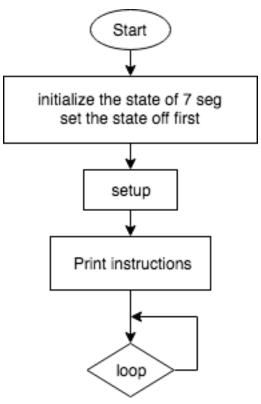
Keming Kuang (kemingku)

Eric Li (eli9)

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1 Flow Chart

1.1 lab5.s Main code

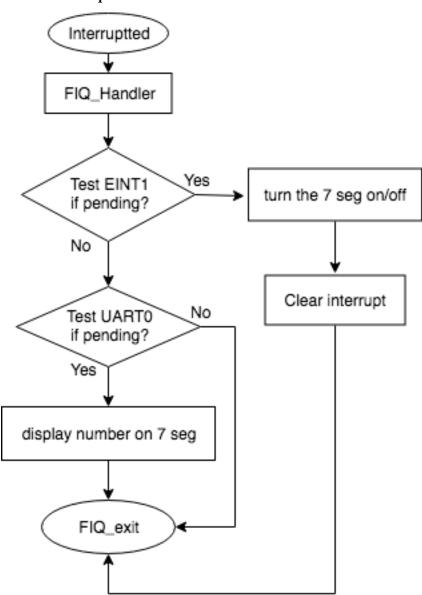


1.2 Initializing interrupt

Initialized EINT1 Initialized UART0

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1.3 Interrupt



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