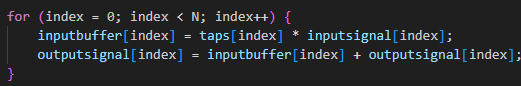
SOC Design

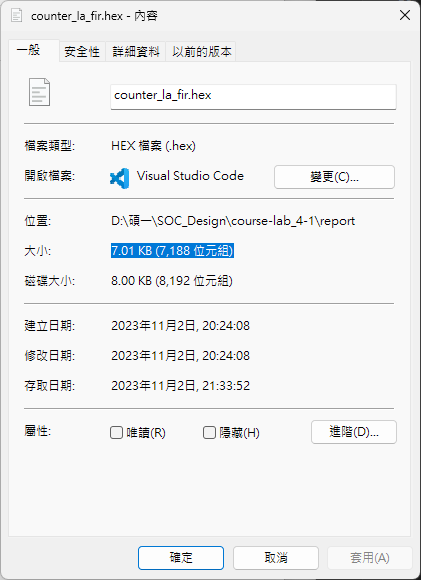
Lab4-1 Execute Code in User Memory

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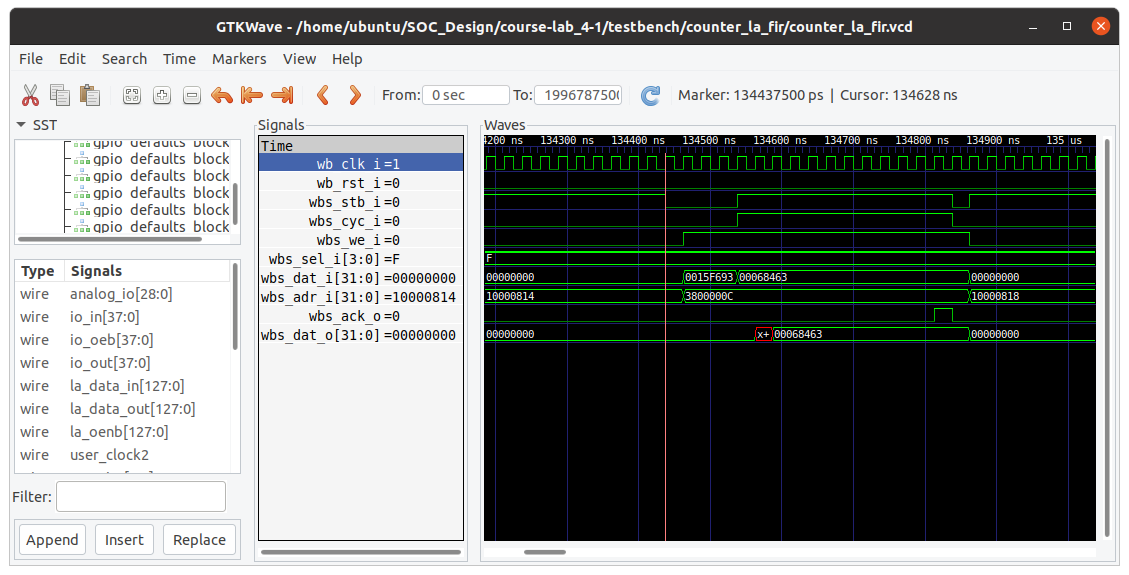
* 1. How does it execute a multiplication in assembly code?



* 1. What address allocate for user project and how many spaces is required to allocate to firmware code?



1. Waveform from XSIM



Delay between and is 10 clock cycle.

1. Synthesis report

