Dirty Details Beyond Standard C++

CppCon 2019
presented by Milian Wolff



The Qt, OpenGL and C++ Experts

- How to Write a Heap Profiler
 - Introduction
 - Preloading
 - Stack Unwinding
 - Symbol Resolution
 - Runtime Attaching

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This Talk

This talk

- Is highly platform specific
 - ELF, DWARF, ld-linux, ...
- Shows lots of dirty tricks
 - Language lawyers beware!
- Contains information I had to learn the hard way

How I Learned All This

- heaptrack: heap memory profiler for linux
 - https://github.com/KDE/heaptrack
 - In-process backtrace generation with libunwind
 - Runtime attaching



- perfparser & hotspot: Linux perf GUI
 - https://github.com/KDAB/hotspot
 - Out-of-process symbol resolution with elfutils / libdwfl



- Tracing is a useful debugging and profiling technique
 - perf trace, strace, ltrace, heaptrack, printf-debugging, ...
- Requirements for a useful tracer:
 - Cope with thousands or even millions of trace events per second
 - Close to zero overhead when not used
 - Runtime attaching or similar

Need to build a domain specific custom tracer?

- Prefer to use existing tracing frameworks:
 - Ittng-ust
 - perf with sdt / uprobe
 - LLVM XRay

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Memory Allocations

What is allocating dynamic storage (i.e. heap memory)?

- In libc:
 - malloc, free
 - realloc, calloc
 - posix memalign, aligned alloc, valloc
- In libstdc++ / libc++:
 - operator new, operator new[]
 - operator delete, operator delete[]
 - std::align val t overloads
- In custom allocator implementations:
 - sbrk, mmap with MAP ANONYMOUS

Use the dynamic linker to inject custom library code:

```
LD_PRELOAD=$(readlink -f path/to/libfoo.so) some_app
```

- Dynamic linker resolves library calls
- First library with a suitable exported symbol wins
 - Make sure the mangled name matches
- LD_PRELOAD wins over dynamically linked libc
- ODR?
 - C++ standard does not define how linking works

Intercepting Library Calls

Build a library with the symbols you want to intercept

- Use dlsym from libdl.so to find the original function
- Casting of void * to a function pointer is valid on POSIX

preload.cpp:

```
1 #include <dlfcn.h> // dlsym
 3 extern "C"
 5 void* malloc(size t size) noexcept
 6
        static auto original_malloc = dlsym(RTLD_NEXT, "malloc");
        assert(original malloc);
 8
        auto original malloc fn = reinterpret_cast<decltype(&::malloc)>(original_malloc);
10
       auto *ret = original malloc fn(size);
11
       fprintf(stderr, "malloc intercepted: %zu -> %p\n", size, ret);
12
13
       return ret:
14 }
15 }
 1 $ nm test_clients/one_malloc | grep " malloc"
 2 U malloc
 3 $ nm /usr/lib/libc.so.6 | grep " malloc"
 4 00000000000875d0 T malloc
 5 $ nm preload/libpreload.so | grep " malloc"
 6 00000000000875d0 T malloc
```

Intercepting Library Calls (cont'd)

Now we can leverage LD_PRELOAD to inject our custom code:

preload.sh:

```
1 #!/bin/sh
2
3 SCRIPT_PATH=$(readlink -f "$0")
4 SCRIPT_DIR=$(dirname "$SCRIPT_PATH")
5 LIBPRELOAD_PATH=$(readlink -f "$SCRIPT_DIR/libpreload.so")
6
7 # important: we must specify the fully resolved, absolute path
8 # LD_PRELOAD will reject symlinks and relative paths
9 LD_PRELOAD="$LIBPRELOAD_PATH" $@
```

Use the above script to inject the code into arbitrary applications:

one_malloc.cpp:

```
#include <cstdlib>
int main()
{
    auto *buffer = malloc(100);
    free(buffer);
    return buffer ? 0 : 1;
}

1 $ ./preload/preload.sh ./test_clients/one_malloc
    malloc intercepted: 72704 -> 0x564d8f706260
    malloc intercepted: 100 -> 0x564d8f717e70
```

Inspecting Dynamic Linking: Without LD_PRELOAD

Inspecting Dynamic Linking: With LD_PRELOAD

```
$ LD DEBUG=bindings ./preload/preload.sh ./test clients/one malloc |& grep -P "\bmalloc\b"
 1 binding file /usr/lib/libc.so.6 [0] to .../preload/libpreload.so [0]: \
       normal symbol `malloc` [GLIBC 2.2.5]
   binding file /usr/lib/libgcc_s.so.1 [0] to .../preload/libpreload.so [0]: \
       normal symbol `malloc` [GLIBC 2.2.5]
   binding file /usr/lib/libstdc++.so.6 [0] to .../preload/libpreload.so [0]: \
       normal symbol `malloc` [GLIBC 2.2.5]
   binding file /lib64/ld-linux-x86-64.so.2 [0] to .../preload/libpreload.so [0]: \
       normal symbol `malloc` [GLIBC 2.2.5]
 8
   binding file .../preload/libpreload.so [0] to /usr/lib/libc.so.6 [0]: \
       normal symbol `malloc`
10
11
   malloc intercepted: 72704 -> 0x5607802f8260
13
   binding file ./test clients/one malloc [0] to .../preload/libpreload.so [0]: \
       normal symbol `malloc` [GLIBC 2.2.5]
15
16
17 malloc intercepted: 100 -> 0x560780309e70
```

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Stack Unwinding

- We need to unwind the stack to get a backtrace
- Approaches:
 - Frame pointer unwinding (requires -fno-omit-frame-pointer)
 - Use exception unwind tables (.eh_frame or .ARM.exidx sections)
 - DWARF debug information (.debug_frame section)
 - Alternatives:
 - Intel LBR (often too shallow)
 - Shadow Stack (<u>https://github.com/nokia/not-perf</u>)

Backtrace Libraries

- libc's backtrace depends on frame pointers
- elfutils dwfl_thread_getframes is complex to use
- libunwind is easy to use, fast and feature rich
 - https://github.com/libunwind/libunwind

Using libunwind

Using libunwind is trivial:

```
#define UNW_LOCAL_ONLY
#include #in
```

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```
#define UNW_LOCAL_ONLY
#include #in
```

But the output isn't really useable as-is:

```
$ ./backtrace/preload_backtrace.sh ./test_clients/vector
```

```
1  0x7f37c70ea63c
2  0x7f37c6f53ac9
3  0x562c61aa1ca1
4  0x562c61aa1ad0
5  0x7f37c6bb4ee2
6  0x562c61aa1b5d
```

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 - elfutils
 - Demangling
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- Instruction pointer: 0x55b20bc95d9f
- Corresponding ELF map:

- Mapped address: 0x55b20bc95d9f 0x55b20bc95000 = 0xd9f
- Symbol resolution:

```
$ addr2line -p -e .../test_clients/delay -a 0xD9F

1  0x00000000000000009f:
2  /usr/include/c++/9.1.0/ostream:570
```

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- Symbol resolution:

```
$ addr2line -p -f -e .../test_clients/delay -a 0xD9F

1  0x0000000000000000f:
2    _ZStlsISt11char_traitsIcEERSt13basic_ostreamIcT_ES5_PKc
3    at /usr/include/c++/9.1.0/ostream:570
```

- Instruction pointer: 0x55b20bc95d9f
- Corresponding ELF map:

- Mapped address: 0x55b20bc95d9f 0x55b20bc95000 = 0xd9f
- Symbol resolution:

```
$ addr2line -p -f -C -e .../test_clients/delay -a 0xD9F

1  0x000000000000000009f:
2    std::basic_ostream<...>& std::operator<< <...>(...)
3    at /usr/include/c++/9.1.0/ostream:570
```

- Instruction pointer: 0x55b20bc95d9f
- Corresponding ELF map:

- Mapped address: 0x55b20bc95d9f 0x55b20bc95000 = 0xd9f
- Symbol resolution:

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dl_iterate_phdr

Iterate over DSO mappings with dl_iterate_phdr from libdl.so / link.h:

```
1 #include <link.h>
 2
 3 void dumpMappings(FILE *out)
        dl iterate phdr([](dl phdr info *info, size t /*size*/, void *data) -> int {
           auto *name = info->dlpi_name;
           if (!name || !name[0])
 8
                name = "exe";
 9
10
           auto out = reinterpret_cast<FILE *>(data);
           fprintf(out, "%s is mapped at: 0x%zx\n", name, info->dlpi addr);
11
12
           return 0;
       }, out);
13
14 }
```

Preload Mappings

Integrated into LD_PRELOAD library:

- Also intercept dlopen and dlclose
 - Mark mappings as dirty whenever one of these is called
- Before writing a backtrace, check if mapping cache is dirty
 - If so, iterate the current mappings and output it
- On startup, also output the executable path once
 - std::filesystem::canonical("/proc/self/exe")

./mappings/preload_mappings.sh ./test_clients/one_malloc

Preload Mappings Output

```
1 exe: .../test clients/vector
   begin modules
 3
       module: 0x5569a6bce000 exe
       module: 0x7fffc0fff000 linux-vdso.so.1
 4
       module: 0x7f81ed72d000 .../mappings/libpreload mappings.so
       module: 0x7f81ed4f5000 /usr/lib/libstdc++.so.6
       module: 0x7f81ed3af000 /usr/lib/libm.so.6
       module: 0x7f81ed395000 /usr/lib/libgcc s.so.1
       module: 0x7f81ed1d2000 /usr/lib/libc.so.6
       module: 0x7f81ed1cd000 /usr/lib/libdl.so.2
10
11
       module: 0x7f81ed1a9000 /usr/lib/libunwind.so.8
12
       module: 0x7f81ed735000 /lib64/ld-linux-x86-64.so.2
13
       module: 0x7f81ecf83000 /usr/lib/liblzma.so.5
14
       module: 0x7f81ecf62000 /usr/lib/libpthread.so.0
   end modules
15
   malloc(72704) = 0x5569a8081620
16
17
       ip: 0x7f81ed72f26c
18
       ip: 0x7f81ed593aea
19
       ip: 0x7f81ed745799
       ip: 0x7f81ed7458a0
20
21
       ip: 0x7f81ed737139
22 malloc(4) = 0x5569a8080400
23 ...
```

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Symbol Resolution with elfutils

- Doing symbol resolution is complex
 - Symbol table
 - Debug information interpretation
 - Compressed debug information
 - Split debug info
- Let's use libdwfl from elfutils for this task

Basic libdwfl setup:

```
1 #include <libdwfl.h>
   namespace {
   const Dwfl Callbacks s callbacks = {
       &dwfl build id find elf,
 5
       &dwfl standard find debuginfo,
       &dwfl_offline_section_address,
       nullptr
 9 };
10 }
11
12 Symbolizer::Symbolizer()
        : m_dwfl(dwfl_begin(&s_callbacks))
13
14 {
15 }
16
17 Symbolizer::~Symbolizer()
18 {
       dwfl_end(m_dwfl);
19
20 }
```

libdwfl: ELF Reporting

Reporting mapped ELF objects:

```
1 void Symbolizer::beginReportElf()
 2
   {
 3
       dwfl report begin(m dwfl);
 4
   }
   void Symbolizer::reportElf(const std::string &path, uint64_t addr)
 7
 8
       if (!dwfl_report_elf(m_dwfl, path.c_str(), path.c_str(), -1, addr, false)) {
           fprintf(stderr, "failed to report elf %s at %zx: %s\n",
                    path.c_str(), addr, dwfl_errmsg(dwfl_errno()));
10
        }
11
12 }
13
14 void Symbolizer::endReportElf()
15 {
       if (dwfl report end(m dwfl, nullptr, nullptr) != 0) {
16
           fprintf(stderr, "failed to end elf reporting: %s\n",
17
                    dwfl errmsq(dwfl errno()));
18
19
20 }
```

libdwfl: Symbol Resolution

Resolve symbol of address:

```
1 Symbol Symbolizer::symbol(uint64 t ip)
 2
       auto *mod = dwfl addrmodule(m dwfl, ip);
       if (!mod) {
           fprintf(stderr, "failed to find module for ip %zx: %s\n",
                    ip, dwfl errmsq(dwfl errno()));
           return {};
 8
        }
 9
       Symbol symbol;
10
11
       setDsoInfo(symbol, mod, ip);
       setSymInfo(symbol, mod, ip);
12
13
       setFileLineInfo(symbol, mod, ip);
       return symbol;
14
15 }
 1 void setDsoInfo(Symbol &symbol, Dwfl_Module *mod, Dwarf_Addr ip)
 2
   {
 3
        Dwarf Addr moduleStart = 0;
        symbol.dso = dwfl_module_info(mod, nullptr, &moduleStart, nullptr,
 5
                                      nullptr, nullptr, nullptr, nullptr);
 6
        symbol.dso offset = ip - moduleStart;
 7 }
```

libdwfl: Resolving Symbol Names

Resolving symbol names from symbol table or DWARF:

libdwfl: Resolving Source Code Locations

Resolving source code file name and line number from DWARF:

```
1 void setFileLineInfo(Symbol &symbol, Dwfl Module *mod, Dwarf Addr ip)
 2
   {
       Dwarf Addr bias = 0;
       auto die = dwfl module addrdie(mod, ip, &bias);
       if (!die)
           return;
       auto srcloc = dwarf getsrc die(die, ip - bias);
       if (!srcloc)
 8
           return;
       auto srcfile = dwarf_linesrc(srcloc, nullptr, nullptr);
10
11
       if (!srcfile)
12
           return;
13
14
        symbol.file = srcfile;
15
        dwarf lineno(srcloc, &symbol.line);
16
        dwarf linecol(srcloc, &symbol.column);
17 }
```

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Demangling: c++filt

Demangling C++ function with c++filt:

```
$ c++filt _ZStlsISt11char_traitsIcEERSt13basic_ostreamIcT_ES5_PKc

1  std::basic_ostream<char, std::char_traits<char>>&
2   std::operator<< <std::char_traits<char>>
3   (std::basic_ostream<char, std::char_traits<char>>&, char const*)
```

Demangling: cxxabi.h

Manual demangling via cxxabi.h:

symbolizer.cpp

```
1 #include <cxxabi.h>
   std::string Symbolizer::demangle(const std::string &symbol) const
       if (symbol.size() < 3 || symbol[0] != ' ' || symbol[1] != 'Z')</pre>
 6
            return symbol;
        auto demangled = abi::__cxa_demangle(symbol.c_str(), nullptr,
                                              nullptr, nullptr);
 9
        if (!demangled)
            return symbol;
10
        std::string ret = demangled;
11
12
        free(demangled);
        return ret;
13
14 }
```

Symbolizing Allocation Backtraces

Putting it all together:

\$./mappings/preload_mappings.sh ./test_clients/vector |& ./symbolization/symbolization

```
1 ...
 2 \text{ malloc}(4) = 0 \times 55 \times 5390225400
   ip: 0x7fbaebf6d26c
        intercept::malloc(unsigned long)@2c
        .../mappings/libpreload mappings.so@226c
        .../src/shared/alloc hooks.h:85:18
   ip: 0x7fbaebdd5ac9
 8
        operator new(unsigned long)@19
 9
        /usr/lib/libstdc++.so.6@a2ac9
        /build/gcc/src/gcc/libstdc++-v3/libsupc++/new op.cc:50:22
10
11 ip: 0x55f38ef73ca1
12
        void std::vector<int, std::allocator<int> >:: M realloc insert...
13
        .../test clients/vector@ca1
        /usr/include/c++/9.1.0/ext/new allocator.h:114:41
14
   ip:0x55f38ef73ad0
16
       main@70
        .../test clients/vector@ad0
17
        /usr/include/c++/9.1.0/bits/vector.tcc:121:4
18
19 ip: 0x7fbaeba36ee2
       libc start main@f2
20
       /usr/lib/libc.so.6@26ee2
21
22 ip: 0x55f38ef73b5d
23
       start@2d
        .../test clients/vector@b5d
24
```

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Find compilation unit (CU) debug information entry (DIE):

```
auto *mod = dwfl_addrmodule(m_dwfl, ip);
if (!mod)
    return {};

Dwarf_Addr bias = 0;
// CU DIE: Compilation Unit Debug Information Entry
auto cuDie = dwfl_module_addrdie(mod, ip, &bias);
if (!cuDie)
return {};
```

libdwfl: Scope DIE

Find innermost scope DIE:

```
1  // innermost scope DIE
2  Dwarf_Die scopeDie;
3  {
4     Dwarf_Die *scopes = nullptr;
5     const auto nscopes = dwarf_getscopes(cuDie, ip - bias, &scopes);
6     if (nscopes == 0)
7        return {};
8     scopeDie = scopes[0];
9     free(scopes);
10 }
```

libdwfl: Inlined DIEs

Find inlined frames, i.e. other DIEs that contain scope DIE:

```
Dwarf_Die *scopes = nullptr;
const auto nscopes = dwarf_getscopes_die(&scopeDie, &scopes);

Dwarf_Files *files = nullptr;
dwarf_getsrcfiles(cuDie, &files, nullptr);

std::vector<Symbol> symbols;
for (int i = 0; i < nscopes; ++i) {
    const auto scope = &scopes[i];
    if (dwarf_tag(scope) == DW_TAG_inlined_subroutine)
        symbols.push_back(inlinedSubroutineSymbol(scope, files));

free(scopes);</pre>
```

libdwfl:Symbol Resolution for Inlined Frames

Resolve symbol name for inlined frame scope:

```
const char *dieName(Dwarf_Die *die)

Warf_Attribute attr;
Dwarf_Attribute *result = dwarf_attr_integrate(die, DW_AT_MIPS_linkage_name, &attr);

if (!result)
    result = dwarf_attr_integrate(die, DW_AT_linkage_name, &attr);

auto *name = dwarf_formstring(result);
if (name)
    return name;

return dwarf_diename(die);
}

return dwarf_diename(die);
```

libdwfl: Information for inlined DIE

Resolve source file and line number for inlined frame scope:

```
1 Symbol inlinedSubroutineSymbol(Dwarf Die *scope, Dwarf Files *files)
 2
   {
       Dwarf Attribute attr;
       Dwarf Word val = 0;
        Symbol symbol:
 8
        symbol.name = dieName(scope);
 9
       const char *file = nullptr;
10
       if (dwarf_formudata(dwarf_attr(scope, DW_AT_call_file, &attr), &val) == 0)
11
12
           file = dwarf filesrc(files, val, nullptr, nullptr);
        symbol.file = file ? file : "??";
13
14
15
       if (dwarf formudata(dwarf attr(scope, DW AT call line, &attr), &val) == 0)
16
            symbol.line = static cast<int>(val);
17
       if (dwarf formudata(dwarf attr(scope, DW AT call column, &attr), &val) == 0)
18
            symbol.column = static cast<int>(val);
19
20
21
        return symbol;
22 }
```

Symbolizing Allocation Backtraces

Putting it all together:

```
1 \text{ malloc}(4) = 0 \times 55 \text{ de} 17384400
 2 ip: 0x7fb788cd026c (.../mappings/libpreload mappings.so@226c)
       intercept::malloc(unsigned long)@2c
   ip: 0x7fb788b38ac9 (/usr/lib/libstdc++.so.6@a2ac9)
       operator new(unsigned long)@19
   ip: 0x55de15b09ca1 (.../test clients/vector@ca1)
       8
       inline: std::allocator traits<std::allocator<int> >::allocate...
       inline: std:: Vector base<int, std::allocator<int> >:: M allocate(unsigned long)
       void std::vector<int, std::allocator<int> >:: M realloc insert<int>(...
10
   ip: 0x55de15b09ad0 (.../test clients/vector@ad0)
11
       inline: int& std::vector<int, std::allocator<int> >::emplace back<int>(int&&)
12
       inline: std::vector<int, std::allocator<int> >::push back(int&&)
13
       inline: std::back insert iterator<std::vector<int, std::allocator<int> > >::operator=...
14
       inline: generate n<std::back insert iterator<std::vector<int> >, int, ...
15
16
       main@70 /usr/include/c++/9.1.0/bits/vector.tcc:121:4
17 ip: 0x7fb788799ee2 (/usr/lib/libc.so.6@26ee2)
       libc start main@f2
18
19 ip: 0x55de15b09b5d (.../test clients/vector@b5d)
20
       start@2d
```

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Clang: Missing .debug_aranges

- Clang does not emit .debug_aranges section by default
 - This breaks CU DIE lookup via dwfl module addrdie
 - Symbolization fails to find source file information, inline frames
- Option 1:
 - Recompile everything with clang++ -gdwarf-aranges ...
- Option 2: workaround by building the mapping manually
 - Based on https://github.com/bombela/backward-cpp
 - dwfl module nextcu to find all CU DIEs in a module
 - dwarf child to find all DIEs in the CU DIE
 - dwarf ranges to find ranges for DIE
 - See moduleAddrDie in symbolization_clang/symbolizer.cpp

How to Write a Heap Profiler

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 - Code Injection
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Code Injection

Q: How can we inject and execute our code into a running application?

Code Injection

Q: How can we inject and execute our code into a running application?

A: With a debugger and dlopen!

Caveat: only works for dynamically linked applications

GDB injection

- Attach to any application via gdb -p PID
 - Auto loading of symbols is often quite slow
 - Thus disable that and resolve necessary symbols manually
- Call dlopen to load your own code
 - dlopen is only available when application links against libdl.so
 - Use libc.so internal libc dlopen mode instead
- Optionally call custom init function with tracing arguments

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Runtime Attaching

- Code Injection
- Intercepting Library Calls

Short Introduction to GOT / PLT

- Calls to dynamically shared objects require relocations
 - See: https://www.akkadia.org/drepper/dsohowto.pdf
- GOT: Global Offset Table
 - Writable section for the linker
- PLT: Procedure Linkage Table

Short Introduction to GOT / PLT (cont'd)

```
1 int main() {
2     auto *buffer = malloc(100);
3 }
```

Relocations can be seen with readelf:

Indirect calls to malloc are visible in the disassembly:

```
$ objdump -S test_clients/one_malloc
```

```
1 Disassembly of section .plt:
   0000000000000720:
 4 720:
         ff 25 da 18 00 00
                                         *0x18da(%rip)
                                                             # 2000
                                  jmpq
 5 726:
         68 00 00 00 00
                                  pushq
                                         $0x0
 6 72b:
         e9 e0 ff ff ff
                                         710
                                  jmpq
   0000000000000839:
 9
   . . .
10
       auto *buffer = malloc(100);
11 841:
          bf 64 00 00 00
                                         $0x64,%edi
                                  mov
         e8 d5 fe ff ff
12 846:
                                  callq
                                         720
13 84b:
          48 89 45 f8
                                         %rax,-0x8(%rbp)
                                  mov
```

- Iterate over all relocations in all DSOs
- Check if the symbol name matches one of our trace points
- If so, overwrite the address in the GOT with the address to our trace point

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```
1 void *foo()
 3
        return malloc(100);
 4 }
   int main()
        auto f = foo();
 8
        overwritePhdrs();
        auto f2 = foo();
10
11
12
       free(f);
       free(f2);
13
        return f != f2;
14
15 }
```

- Iterate over all relocations in all DSOs
- Check if the symbol name matches one of our trace points
- If so, overwrite the address in the GOT with the address to our trace point

```
1 void overwritePhdrs()
 2 {
 3
        dl_iterate_phdr([](dl_phdr_info* info, size_t /*size*/, void* /*data*/) {
           if (strstr(info->dlpi name, "/ld-linux")) {
 5
                // don't touch anything in the linker itself
 6
                return 0;
 7
            }
 8
 9
           for (int i = 0; i < info->dlpi phnum; ++i) {
10
                const auto &phdr = info->dlpi phdr[i];
                if (phdr.p_type == PT_DYNAMIC) {
11
                    const auto dynEntriesAddr = phdr.p vaddr + info->dlpi addr;
12
                    const auto dynEntries = reinterpret cast<const Elf::Dyn *>(dynEntriesAddr);
13
                    const auto base = info->dlpi addr;
14
15
                    overwriteDynEntries(dynEntries, base);
16
17
18
           return 0;
       }, nullptr);
19
20 }
```

- Iterate over all relocations in all DSOs
- Check if the symbol name matches one of our trace points
- If so, overwrite the address in the GOT with the address to our trace point

```
1 void overwriteDynEntries(const Elf::Dyn *dynEntries, Elf::Addr baseAddr)
 2 {
 3
        Elf::SymbolTable symbols;
        Elf::JmprelTable imprels;
 5
        Elf::StringTable strings;
 6
 7
       // initialize the elf tables
 8
        for (auto dyn = dynEntries; dyn->d tag != DT NULL; ++dyn) {
            symbols.consume(dyn) || strings.consume(dyn) || jmprels.consume(dyn);
10
        }
11
12
        overwriteGotEntries(jmprels, symbols, strings, baseAddr);
13 }
```

- Iterate over all relocations in all DSOs
- Check if the symbol name matches one of our trace points
- If so, overwrite the address in the GOT with the address to our trace point

```
1 template <typename T, Elf::Sxword AddrTag, Elf::Sxword SizeTag>
 2 struct Table
 3 {
       using type = T;
       T* table = nullptr;
 5
 6
        Elf::Xword size = {};
 7
 8
        bool consume(const Elf::Dyn* dyn) noexcept
 9
       {
10
           if (dyn->d tag == AddrTag) {
                table = reinterpret cast<T*>(dyn->d un.d ptr);
11
12
                return true:
           } else if (dyn->d tag == SizeTag) {
13
                size = dyn->d_un.d_val;
14
15
                return true;
16
           return false;
17
18
        }
19
        const T* begin() const noexcept { return table; }
20
21
        const T* end() const noexcept { return table + size / sizeof(T); }
22 };
```

- Iterate over all relocations in all DSOs
- Check if the symbol name matches one of our trace points
- If so, overwrite the address in the GOT with the address to our trace point

```
using Addr = ElfW(Addr);
using Dyn = ElfW(Dyn);
using Rel = ElfW(Rel);
using Sym = ElfW(Rela);
using Sym = ElfW(Sym);
using Sxword = ElfW(Sxword);
using Xword = ElfW(Xword);

using StringTable = Table<const char, DT_STRTAB, DT_STRSZ>;
using SymbolTable = Table<Elf::Sym, DT_SYMTAB, DT_SYMENT>;
using RelTable = Table<Elf::Rel, DT_REL, DT_RELSZ>;
using RelaTable = Table<Elf::Rela, DT_RELA, DT_RELASZ>;
using JmprelTable = Table<Elf::Rela, DT_JMPREL, DT_PLTRELSZ>;
```

- Iterate over all relocations in all DSOs
- Check if the symbol name matches one of our trace points
- If so, overwrite the address in the GOT with the address to our trace point

```
1 void overwriteGotEntries(const Elf::JmprelTable &relocations,
                             const Elf::SymbolTable &symbols,
 2
 3
                               const Elf::StringTable &strings,
 4
                             Elf::Addr baseAddr)
 5
   {
 6
        for (const auto &relocation : relocations) {
 7
            const auto index = ELF_R_SYM(relocation.r_info);
 8
            const char* symname = strings.table + symbols.table[index].st name;
 9
            const auto gotAddr = relocation.r offset + baseAddr;
10
            overwriteGotEntry(symname, gotAddr);
11
12 }
```

- Iterate over all relocations in all DSOs
- Check if the symbol name matches one of our trace points
- If so, overwrite the address in the GOT with the address to our trace point

```
1 void *intercept malloc(size t size)
 2 {
 3
        static auto original malloc = dlsym(RTLD NEXT, "malloc");
        assert(original malloc);
 5
 6
       auto original malloc fn = reinterpret cast<decltype(&::malloc)>(original malloc);
 7
        auto ret = original malloc fn(size);
 8
 9
       fprintf(stderr, "malloc intercepted: %zu -> %p\n", size, ret);
10
       return ret;
11 }
12
13 void overwriteGotEntry(const char *symname, Elf::Addr gotAddr)
14 {
15
       if (strcmp(symname, "malloc") == 0) {
           auto ptr = reinterpret cast<void **>(gotAddr);
16
           fprintf(stderr, "relocation: %s: %zx | %p\n", symname, gotAddr, *ptr);
17
18
           *ptr = reinterpret cast<void *>(&intercept malloc);
       }
19
20 }
```

- Iterate over all relocations in all DSOs
- Check if the symbol name matches one of our trace points
- If so, overwrite the address in the GOT with the address to our trace point got_overwriting
 - 1 \$./got_overwriting/got_overwriting
 - 2 relocation: malloc: 5604cc5c0038 | 0x7fec64a0e5d0
 - 3 malloc intercepted: 100 -> 0x5604cd0a8ee0

Road Towards A Real Heap Profiler

- Efficient output data format
 - Consider zstd compression
 - Consider binary data format (e.g. protobuf or similar)
 - Graphing essentially requires a good time series data format
- API for custom allocators
- Analysis GUI
 - FlameGraph visualization is crucial
- Handling other quirks
 - forking, sub processes, clean shutdown, ...

Please contribute to heaptrack instead of writing your own!

Questions?

Milian Wolff

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- Example source code and slides
- Heap Memory Profiler: heaptrack
- Linux Perf GUI: hotspot



The Qt, OpenGL and C++ Experts