Ken Baierl

Lake Forest, CA, 92630 • (949) 769-1553 • kenneth.baierl@gmail.com • https://www.linkedin.com/in/kenneth-baierl/

Summary

Earned a Bachelor's degree of Science in Electrical Engineering with specialized experience in computer hardware design. Seeking an entry-level full-time position related to hardware design where I can leverage my knowledge in designing transistor chips in Cadence, test engineering or designing electronics in CAD software.

Skills

PCB Design, MATLAB and Simulink, AutoCAD, Excel, Soldering Iron, Eagle Schematics, Cadence Virtuoso, Wolfram Mathematica, HTML, CSS, Logisim, Arduino, Oscilloscope, Fusion 360, Prusa MK3S+

Experience - https://kenbaierl.github.io/kenbaierlportfolio/

Electrical Drafter/Designer Intern 08/2024 to Current

Sky Parsi Engineering & Consulting – Lake Forest, CA

- Designed electrical drawings in AutoCAD for floor plans, lighting plans, and power plans for buildings and PV units.
- Drafted electrical power plans to find the most efficient design for multiple projects.
- Quickly became proficient in AutoCAD.
- Conducted site visits to examine electrical units and parts to translate onto electrical drawings.
- Coordinated with clients and companies to find the correct and most efficient parts for a project.
- Worked together with colleagues as well as architects to coordinate deadlines for projects.

Education

Chapman University, Orange, CA

Bachelor of Science, Major in Electrical Engineering

Chapman University Lab Assignments Orange, California

Chapman University

- Designed a schematic for a robot using NMOS transistors and infrared sensors to follow a black line without microcontrollers.
    - Entered Chapman Engineering design competition.
- Created a control systems project for a wind turbine using a PID controller to find the error values to account for disturbances and measurement errors in MATLAB.
- Designed in Cadence a fully static true single phase clocked dual edge triggered flip flop that passed both DRC and LVS checks.
- Created multiple RTL design projects with Verilog code to work on an Arduino.
- Created a single cycle processor and arithmetic logic unit using Logisim.
- Created a circuit to filter the high, mid, and low frequency bands to different colors when given an input sound.

Activities

Chapman University Football Team

- Starting place-kicker as an incoming freshman.
- First Team All-SCIAC awarded for 2021-2022 and 2022-2023 season.
- First Team All-Region awarded for 2022-2023 season.
- Special teams player of the year awarded for 2021-2022 season as well as multiple records broken.
- Balanced time between life as a full-time engineering student and athlete while averaging around 24 hours per week for practice, conditioning, training, and games.
- Volunteered in Be The Match to help connect patients who need bone marrow or blood stem cells.

References available at request