Program13 | main.asm

```
;Homework
.include "m328Pdef.inc"
.def var_i  = r16
.def var_j  = r17
.def templ  = r18
.def temph  = r19

.cseg
.org 0x00
     rjmp main
main:   ldi yl,low(SARARY)
        ldi yh,high(SARARY)
        ldi templ,$56    ;0x1F56
        ldi temph,$1F
        clr var_i
        clr var_j
for_i:    cpi var_i,10    ;i<10
         brsh end    ;break when i>=10
        ;rjmp end.
for_j:    cpi var_j,50
         brlo loop
         inc var_i
         clr var_j
         rjmp for_i

loop:  st y+, templ
       st y+, temph
       inc var_j
       adiw x,1
       rjmp for_j

end:   rjmp end

.dseg
.org 0x100
SARARY: .byte 2*500
```

Memory 3

Memory: data IRAM

data 0x0490	1f56	1f56	1f56	1f56	1f56	1f56	V.V.V.V.V.V.
data 0x049C	1f56	1f56	1f56	1f56	1f56	1f56	V.V.V.V.V.V.
data 0x04A8	1f56	1f56	1f56	1f56	1f56	1f56	V.V.V.V.V.V.
data 0x04B4	1f56	1f56	1f56	1f56	1f56	1f56	V.V.V.V.V.V.
data 0x04C0	1f56	1f56	1f56	1f56	1f56	1f56	V.V.V.V.V.V.
data 0x04CC	1f56	1f56	1f56	1f56	1f56	1f56	V.V.V.V.V.V.
data 0x04D8	1f56	1f56	1f56	1f56	1f56	1f56	V.V.V.V.V.V.
data 0x04E4	1f56	1f56	0000	0000	0000	0000	V.V.....
data 0x04F0	0000	0000	0000	0000	0000	0000	.....
data 0x04FC	0000	0000	0000	0000	0000	0000	.....
data 0x0508	0000	0000	0000	0000	0000	0000	.....
data 0x0514	0000	0000	0000	0000	0000	0000	.....
data 0x0520	0000	0000	0000	0000	0000	0000	.....
data 0x052C	0000	0000	0000	0000	0000	0000	.....
data 0x0538	0000	0000	0000	0000	0000	0000	.....

Processor Status

Name	Value
Program Counter	0x00000013
Stack Pointer	0x08FF
X Register	500
Y Register	0x04E8
Z Register	0x0000
Status Register	I T H S V N Z C
Cycle Counter	6090
Frequency	1.000 MHz
Stop Watch	6,090.00 µs
Registers	

Watch 1

Name	Value
var_i	10
var_j	0
templ	86
temph	31