Program 10

;signed char a,b;

;if (a>b) ; (b<a)

; a++;

;else

; a=a+b;

;b++;

.include "m328Pdef.inc"

.def var\_a = r16

.def var\_b = r17

.cseg

.org 0x00

rjmp main

main: ldi var\_a,-5

ldi var\_b, -1

cp var\_b,var\_a

brlt true

;rjmp false

false: add var\_a,var\_b

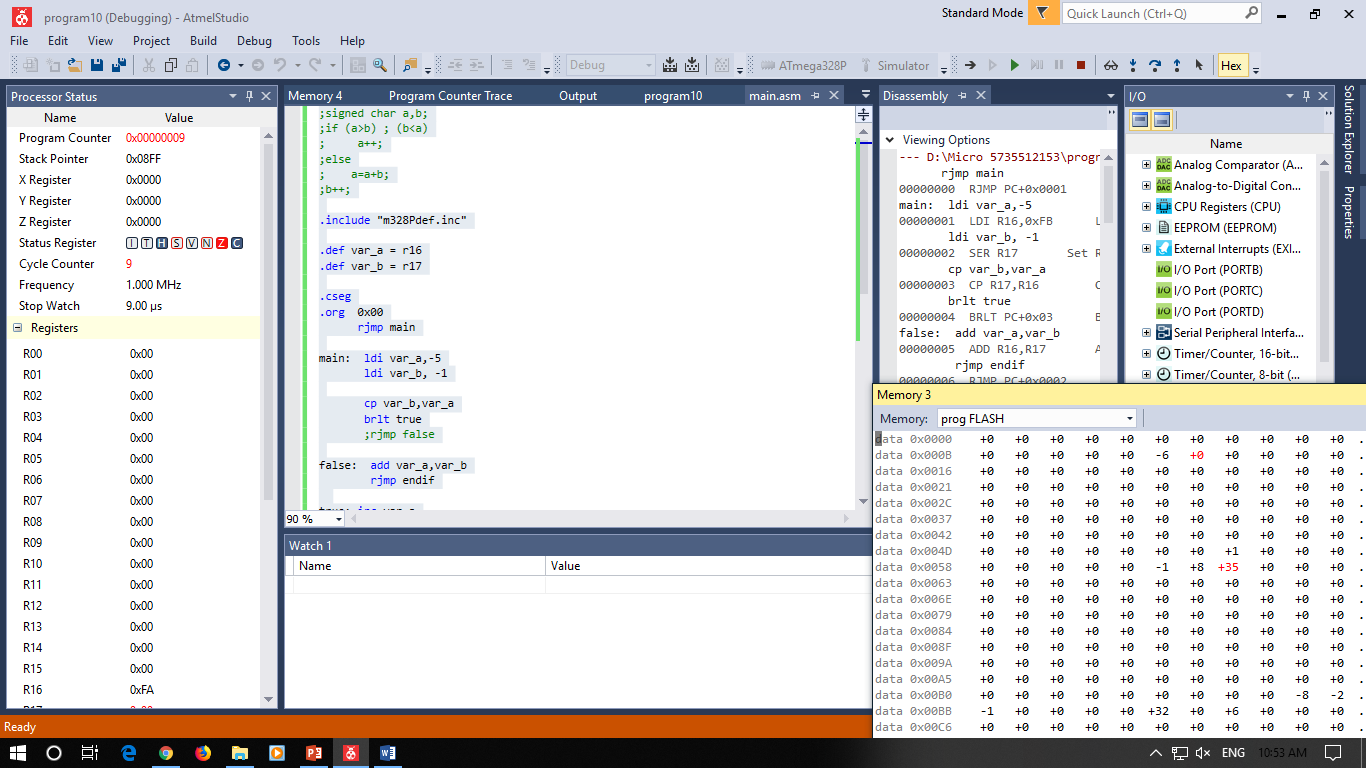
rjmp endif

true: inc var\_a

;rjmp endif

endif: inc var\_b

end: rjmp end



;signed char a,b;

;if (a>b) ;

; a++;

;else

; a=a+b;

;b++;

.include "m328Pdef.inc"

.def var\_a = r16

.def var\_b = r17

.cseg

.org 0x00

rjmp main

main: ldi var\_a,-5

ldi var\_b, -5

cp var\_b,var\_a

breq false

brge true

;rjmp false

false: add var\_a,var\_b

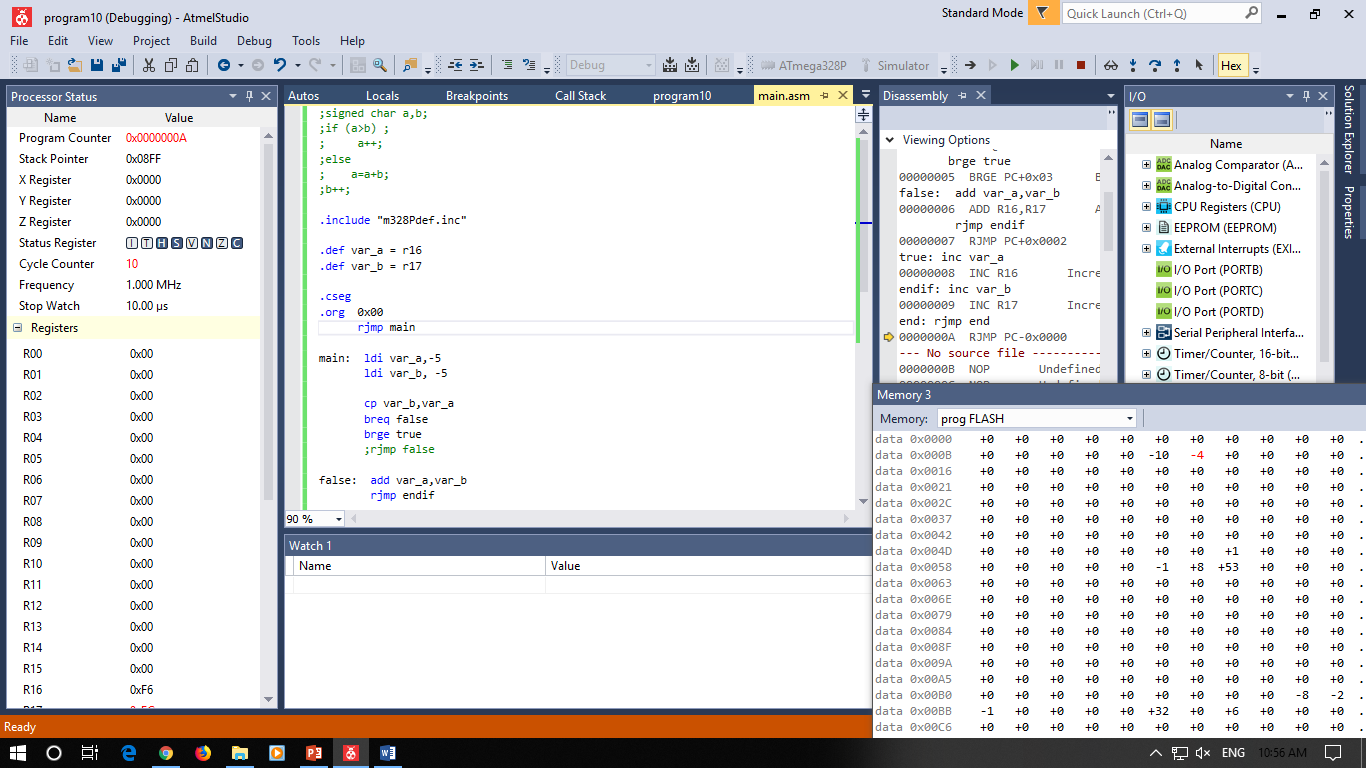
rjmp endif

true: inc var\_a

;rjmp endif

endif: inc var\_b

end: rjmp end



program12

;signed char a, b, c;

; if (a<b)

; c = -1;

; else if (a > b)

; c = 1;

; else

; c = 0;

; a = b\*c;

.include "m328Pdef.inc"

.def var\_a = r16

.def var\_b = r17

.def var\_c = r18

.cseg

.org 0x00

rjmp main

main: ldi var\_a,-10

ldi var\_b, -5

cp var\_a,var\_b

brlt LESS

breq EQUAL

;rjmp MORE

;brge MORE

MORE: ldi var\_c,1

rjmp endif

LESS: ldi var\_c,-1

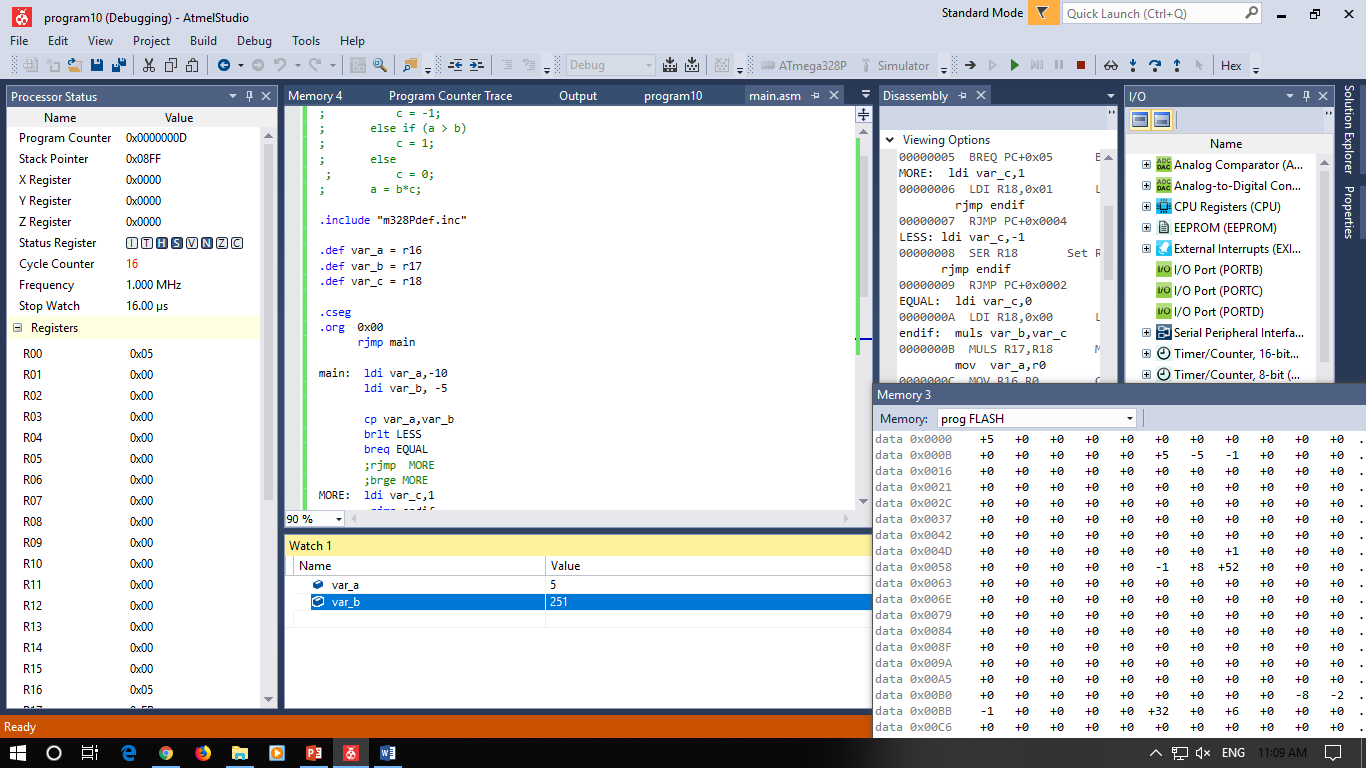
rjmp endif

EQUAL: ldi var\_c,0

endif: muls var\_b,var\_c

mov var\_a,r0

end: rjmp end



Program 14

;unsigned char a, b=0;

;for (a=0; a<10; a++)

; b++;

;a = b+50;

.include "m328Pdef.inc"

.def var\_a = r16

.def var\_b = r17

.cseg

.org 0x00

rjmp main

main: clr var\_b

clr var\_a

for: cpi var\_a,10

brlo loop

rjmp endfor

loop: inc var\_b

inc var\_a

rjmp for

endfor: ldi var\_a,50

add var\_a,var\_b

