Program 35

.include "m328Pdef.inc"

.def var\_a = r16

.def var\_b = r17

.cseg

.org 0x00

rjmp reset

reset: ldi var\_a,0x00 ;input

out DDRC, var\_a

ldi var\_a,0xff ;output

out DDRB,var\_a

main: in var\_a,PINC

andi var\_a, 0b01110000

lsr var\_a

lsr var\_a

lsr var\_a

lsr var\_a

ldi zl,low(TB\*2)

ldi zh,high(TB\*2)

clr var\_b

add zl,var\_a

adc zh,var\_b

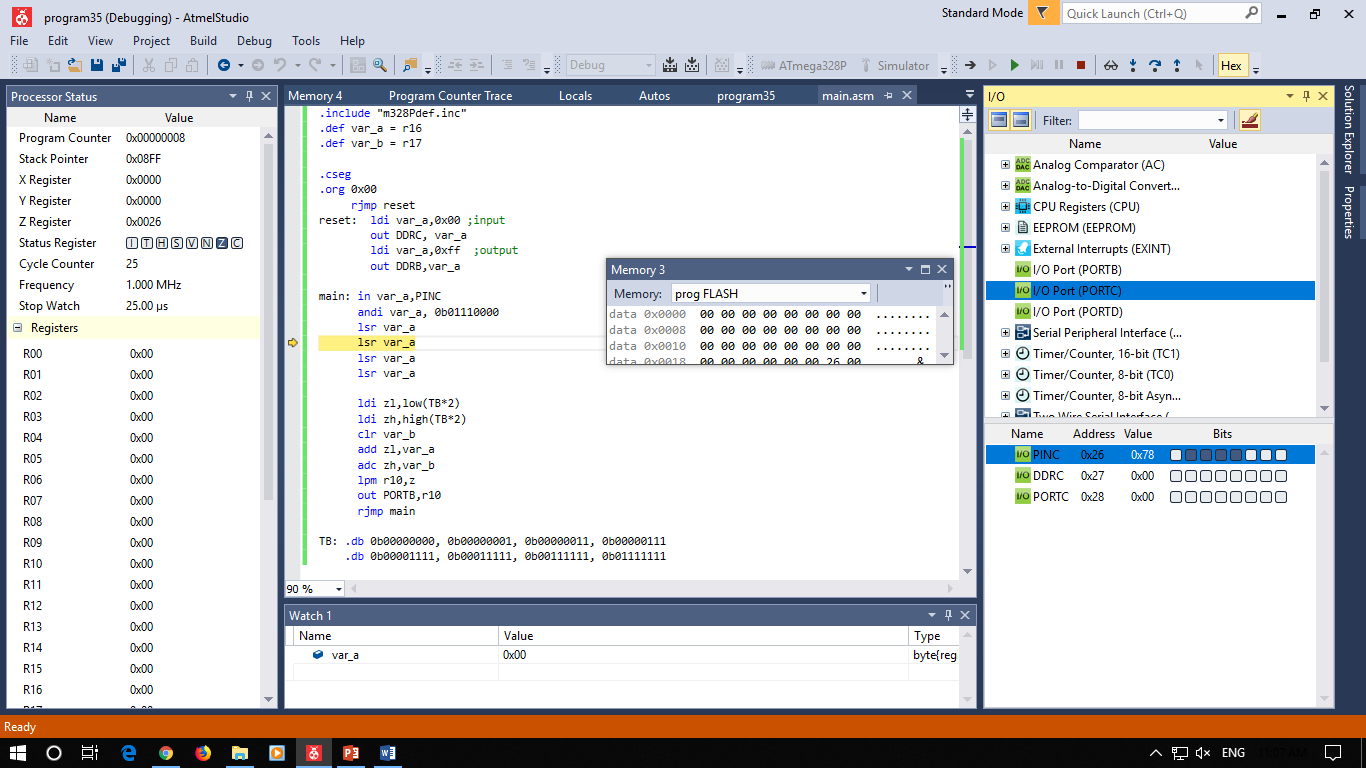
lpm r10,z

out PORTB,r10

rjmp main

TB: .db 0b00000000, 0b00000001, 0b00000011, 0b00000111

.db 0b00001111, 0b00011111, 0b00111111, 0b01111111



Program 33