

SMEIL Language Reference

Grammar

$\langle module \rangle$	$::= \{ \langle import-stm \rangle \}$ $\{ \langle type-def \rangle \}$ $\langle entity \rangle \{ \langle entity \rangle \}$
$\langle import-stm \rangle$	$::= \text{'import' } \langle import-name \rangle [\langle qualified-specifier \rangle] \text{' ;'}$ $ \text{'from' } \langle import-name \rangle$ $\text{'import' } \langle ident \rangle \{ \text{' , ' } \langle ident \rangle \} [\langle qualified-specifier \rangle]$ ' ;'
$\langle import-name \rangle$	$::= \langle ident \rangle \{ \text{' . ' } \langle ident \rangle \}$
$\langle qualified-specifier \rangle$	$::= \text{'as' } \langle ident \rangle$
$\langle type-def \rangle$	$::= \text{'type' } \langle ident \rangle \text{' : '}$ $\langle type-name \rangle \text{ (type alias)}$ $ \langle bus-signal-decls \rangle \text{ (bus definition)}$ ' ;'
$\langle entity \rangle$	$::= \langle network \rangle$ $ \langle process \rangle$
$\langle network \rangle$	$::= \text{'network' } \langle ident \rangle \text{' (' } [\langle params \rangle] \text{') '}$ $\text{' { ' } \{ \langle network-decl \rangle \} \text{' } \}$
$\langle process \rangle$	$::= [\text{'clocked' }] \text{'proc' } \langle ident \rangle$ $\text{' (' } [\langle params \rangle] \text{') ' } \{ \langle process-decl \rangle \}$ $\text{' { ' } \{ \langle statement \rangle \} \text{' } \}$
$\langle network-decl \rangle$	$::= \langle inst-decl \rangle$ $ \langle bus-decl \rangle$ $ \langle const-decl \rangle$ $ \langle gen-decl \rangle$ $ \langle connect-decl \rangle$

$\langle process-decl \rangle$	$::=$ $\langle var-decl \rangle$ $ $ $\langle const-decl \rangle$ $ $ $\langle bus-decl \rangle$ $ $ $\langle enum-decl \rangle$ $ $ $\langle func-decl \rangle$ $ $ $\langle inst-decl \rangle$ $ $ $\langle gen-decl \rangle$
$\langle params \rangle$	$::= \langle param \rangle \{ , \langle param \rangle \}$
$\langle param \rangle$	$::= [' [' [\langle integer \rangle] '] \langle direction \rangle \langle ident \rangle [':' \langle type-name \rangle$ $]$
$\langle direction \rangle$	$::=$ $'in'$ (input signal) $ $ $'out'$ (output signal) $ $ $'const'$ (constant input value)
$\langle var-decl \rangle$	$::=$ $'var'$ $\langle ident \rangle ':'$ $\langle type-name \rangle ['=' \langle expression \rangle] [\langle range \rangle] ';'$
$\langle range \rangle$	$::= 'range' \langle expression \rangle 'to' \langle expression \rangle$
$\langle enum \rangle$	$::=$ $'enum'$ $\langle ident \rangle$ $'\{ ' \langle enum-field \rangle \{ ',' \langle enum-field \rangle \} '}' ';' $
$\langle enum-field \rangle$	$::= \langle ident \rangle ['=' \langle integer \rangle]$
$\langle const-decl \rangle$	$::= 'const' \langle ident \rangle ':' \langle type-name \rangle '=' \langle expression \rangle ';' $
$\langle bus-decl \rangle$	$::= ['clocked'] 'bus' \langle ident \rangle$ $'\{ ' \langle bus-signal-decls \rangle '}' ';' $
$\langle func-decl \rangle$	$::=$ $'function'$ $\langle ident \rangle '(' \langle params \rangle ') ' \{ ' \langle statement \rangle \}$ $'}' ';' $
$\langle bus-signal-decls \rangle$	$::= \langle bus-signal-decl \rangle \{ \langle bus-signal-decl \rangle \}$
$\langle bus-signal-decl \rangle$	$::= \langle ident \rangle ':' \langle type-name \rangle ['=' \langle expression \rangle] [\langle range \rangle]$ $','$
$\langle connect-entry \rangle$	$::= \langle ident \rangle '->' \langle ident \rangle$
$\langle connect-decl \rangle$	$::= connect \langle connect-entry \rangle \{ \langle connect-entry \rangle \} ';' $
$\langle inst-decl \rangle$	$::=$ $'instance'$ $\langle instance-name \rangle 'of' \langle ident \rangle$ $'(' [\langle param-map \rangle \{ ',' \langle param-map \rangle \}] ')' ';' $

$\langle instance-name \rangle$	$::= \langle ident \rangle \text{'['} \langle expression \rangle \text{']'}$ (indexed instance) $ \langle ident \rangle$ (named instance) $ \text{'_'} \text{'}$ (anonymous instance)
$\langle param-map \rangle$	$::= [\langle ident \rangle \text{' : ' } \langle expression \rangle]$
$\langle gen-decl \rangle$	$::= \text{'generate' } \langle ident \rangle \text{' = ' } \langle expression \rangle \text{' to ' } \langle expression \rangle$ $\text{'{' } \{ \langle network-decl \rangle \} \text{'}'}$
$\langle statement \rangle$	$::= \langle name \rangle \text{' = ' } \langle expression \rangle \text{' ; '}$ (assignment) $ \langle ident \rangle \text{' (' } \langle param-map \rangle \text{') ' ; '}$ (function call) $ \text{'if' } \text{' (' } \langle expression \rangle \text{') ' ' {' } \{ \langle statement \rangle \} \text{'}'}$ $\{ \langle elif-block \rangle \} [\langle else-block \rangle]$ $ \text{'for' } \langle ident \rangle \text{' = ' } \langle expression \rangle \text{' to ' } \langle expression \rangle$ $\text{'{' } \{ \langle statement \rangle \} \text{'}'}$ $ \text{'switch' } \langle expression \rangle$ $\text{'{' } \langle switch-case \rangle \{ \langle switch-case \rangle \} [\text{'default' } \text{'{' } \langle statement \rangle$ $\{ \langle statement \rangle \} \text{'}'] \text{'}'}$ $ \text{'trace' } \text{' (' } \langle format-string \rangle \{ \text{' , ' } \langle expression \rangle \} \text{') ' ; '}$ $ \text{'assert' } \text{' (' } \langle expression \rangle [\text{' , ' } \langle string-literal \rangle] \text{') ' ; '}$ $ \text{'break' } \text{' ; '}$
$\langle switch-case \rangle$	$::= \text{'case' } \langle expression \rangle \text{' {' } \{ \langle statement \rangle \} \text{'}'}$
$\langle elif-block \rangle$	$::= \text{'elif' } \text{' (' } \langle expression \rangle \text{') ' ' {' } \{ \langle statement \rangle \} \text{'}'}$
$\langle else-block \rangle$	$::= \text{'else' } \text{' {' } \{ \langle statement \rangle \} \text{'}'}$
$\langle format-string \rangle$	$::= \text{'"'} \{ \langle format-string-part \rangle \} \text{'"}$
$\langle format-string-part \rangle$	$::= \text{'{' } \text{'}'}$ (placeholder string) $ \langle string-char \rangle$
$\langle expression \rangle$	$::= \langle name \rangle$ $ \langle literal \rangle$ $ \langle expression \rangle \langle bin-op \rangle \langle expression \rangle$ $ \langle un-op \rangle \langle expression \rangle$ $ \text{' (' } \langle expression \rangle \text{') '}$
$\langle bin-op \rangle$	$::= \text{'+'}$ (addition) $ \text{'-'}$ (subtraction) $ \text{'*'}$ (multiplication) $ \text{'/'}$ (division) $ \text{'%'}$ (modulo) $ \text{'=='}$ (equal) $ \text{'!='}$ (not equal) $ \text{'<<'}$ (shift left)

		'>>' (shift right)
		'<' (less than)
		'>' (greater than)
		'>=' (greater than or equal)
		'<=' (less than or equal)
		'&' (bitwise-and)
		' ' (bitwise-or)
		'^' (bitwise-xor)
		'&&' (logical conjunction)
		' ' (logical disjunction)
$\langle un-op \rangle$::=	'-' (negation)
		'+' (identity)
		'!' (logical negation)
		'~' (bitwise-not)
$\langle literal \rangle$::=	$\langle integer \rangle$
		$\langle floating \rangle$
		$\langle string-literal \rangle$
		'[' $\langle integer \rangle$ { ',' $\langle integer \rangle$ } ']' (Array literal)
		'true'
		'false'
		'U' (Undefined value)
$\langle string-literal \rangle$::=	'"{ $\langle string-char \rangle$ }"'
$\langle intrinsic-type \rangle$::=	'i' $\langle integer \rangle$ (signed integer)
		'int' (arbitrary-width signed integer)
		'u' $\langle integer \rangle$ (unsigned integer)
		'uint' (arbitrary-width unsigned integer)
		'f32' (single-precision floating point)
		'f64' (double-precision floating point)
		'bool' (boolean value)
$\langle type-name \rangle$::=	$\langle intrinsic-type \rangle$
		$\langle name \rangle$ (type definition)
		'[' [$\langle expression \rangle$] ']' $\langle type-name \rangle$ (array of type)
$\langle ident \rangle$::=	$\langle letter \rangle$ { $\langle letter \rangle$ $\langle number \rangle$ '_' '-' } (identifier)
$\langle name \rangle$::=	$\langle ident \rangle$
		$\langle name \rangle$ '.' $\langle name \rangle$ (hierarchical accessor)
		$\langle name \rangle$ '[' $\langle array-index \rangle$ ']' (array element access)
$\langle array-index \rangle$::=	'*' (wildcard)
		$\langle expression \rangle$ (element index)

$\langle integer \rangle$	$::= \langle number \rangle \{ \langle number \rangle \}$ (decimal number) $ $ $\text{'0x' } \langle hex-digit \rangle \{ \langle hex-digit \rangle \}$ (hexadecimal number) $ $ $\text{'0o' } \langle octal-digit \rangle \{ \langle octal-digit \rangle \}$ (octal number)
$\langle floating \rangle$	$::= \{ \langle number \rangle \} \text{'.'} \langle number \rangle \{ \langle number \rangle \}$
$\langle number \rangle$	$::= \text{'0' - '9'}$
$\langle letter \rangle$	$::= \text{'a' - 'z'}$ $ $ 'A' - 'Z'
$\langle hex-digit \rangle$	$::= \langle number \rangle$ $ $ 'a' - 'f' $ $ 'A' - 'F'
$\langle octal-digit \rangle$	$::= \text{'0' - '8'}$
$\langle string-char \rangle$	$::=$ (ISO-8859-1 char with value > 26)

Operator precedence

Precedence	Operators
0	$+ - ! \sim$ (unary)
1	$* / \%$
2	$+ -$
3	$<< >>$
4	$< > <= >=$
5	$== !=$
6	$\& \wedge $
7	$\&\&$
8	$ $

Keywords

- as
- async
- await
- barrier
- break
- bus
- case
- const
- connect
- clocked
- default
- elif
- else
- enum
- exposed
- for
- from
- func
- generate
- if
- import

- in
- instance
- network
- of
- out
- proc
- range
- return
- switch
- sync
- to
- unique
- var
- wait
- where