## SMEIL Language Reference

## Grammar

```
\langle module \rangle
                                           ::= \{ \langle import\text{-}stm \rangle \}
                                                    \{ \langle module\text{-}decl \rangle \}
                                                    \{\langle entity \rangle\}
                                           ::= 'import' \( \langle import-name \) [ \( \langle qualified-specifier \) ] ';'
\langle import\text{-}stm \rangle
                                             | 'from' \langle import-name \rangle
                                                   \verb|`import'| \langle ident \rangle \ \{ \ \verb|`,' \ \langle ident \rangle \ \} \ [ \ \langle qualified\textit{-specifier} \rangle \ ]
                                          ::= \langle ident \rangle \{ '.' \langle ident \rangle \}
\langle import\text{-}name \rangle
\langle qualified\text{-}specifier \rangle
                                         ::= 'as' \langle ident \rangle
\langle module-decl \rangle
                                           ::= \langle type\text{-}def \rangle
                                                   \langle const-decl \rangle
                                                    \langle enum\text{-}decl \rangle
                                                    \langle func\text{-}decl \rangle
\langle type\text{-}def \rangle
                                          ::= 'type' \(\langle ident\) ':'
                                                    \langle type\text{-}name \rangle (type alias)
                                                    |\langle bus\text{-}signal\text{-}decls\rangle (bus definition)
\langle entity \rangle
                                          ::= \langle network \rangle
                                                    \langle process \rangle
                                           ::= 'network' \langle ident \rangle '(' [ \langle params \rangle ] ')'
\langle network \rangle
                                                   ``\{' \{ \langle network\text{-}decl \rangle \} `\}"
                                           ::= \ [ \ \texttt{`clocked'} \ ] \ \texttt{`proc'} \ \langle ident \rangle
\langle process \rangle
                                                    '(' [ \langle params \rangle ] ')' { \langle process-decl \rangle }
                                                   \langle network\text{-}decl \rangle
                                           ::= \ \langle \mathit{inst-decl} \rangle
                                                   \langle bus\text{-}decl \rangle
```

```
\langle const-decl \rangle
                                                    \langle gen\text{-}decl \rangle
                                                    \langle connect\text{-}decl \rangle
\langle process-decl \rangle
                                           ::= \langle var-decl \rangle
                                                    \langle const-decl \rangle
                                                    \langle bus\text{-}decl \rangle
                                                    \langle enum-decl \rangle
                                                    \langle func\text{-}decl \rangle
                                                    \langle inst\text{-}decl \rangle
                                                    \langle gen\text{-}decl \rangle
\langle func\text{-}decls \rangle
                                           ::= \langle var\text{-}decl \rangle
                                                    \langle const-decl \rangle
                                                    \langle enum-decl \rangle
\langle params \rangle
                                           ::= \langle param \rangle \{ , \langle param \rangle \}
                                           ::= \langle direction \rangle \langle ident \rangle [ :: \langle type-name \rangle ]
\langle param \rangle
\langle direction \rangle
                                           ::= 'in' (input signal)
                                                    'out' (output signal)
                                                    'const' (constant input value)
\langle signal\_direction \rangle
                                           ::= 'normal'
                                             | 'inverse'
                                           ::= 'var' \langle ident \rangle ':'
\langle var\text{-}decl \rangle
                                                    \langle type\text{-}name \rangle [ '=' \langle expression \rangle ] ';'
\langle enum\text{-}decl \rangle
                                           ::= 'enum' \langle ident \rangle
                                                    '{' \langle enum\text{-}field \rangle { ',' \langle enum\text{-}field \rangle } '}' ';'
\langle enum\text{-}field \rangle
                                           ::= \langle ident \rangle \ [ '=' \langle integer \rangle \ ]
                                           ::= 'const' \langle ident \rangle ':' \langle type\text{-}name \rangle '=' \langle expression \rangle ';'
\langle const-decl \rangle
                                           ::= [ `clocked' ] `bus' \langle ident \rangle \langle bus-decl-content \rangle `; `
\langle bus\text{-}decl \rangle
                                           ::= 'function' \langle ident \rangle '(' \langle params \rangle ')' { \langle func\text{-}decls \rangle } '{'
\langle func\text{-}decl \rangle
                                                    \{ \langle statement \rangle \} '\}'
                                           ::= `\{` \langle bus\text{-}signal\text{-}decls \rangle `\}`
\langle bus\text{-}decl\text{-}content \rangle
                                                   \langle type\text{-}name \rangle
\langle bus-signal-decls\rangle
                                           ::= \langle bus\text{-}signal\text{-}decl \rangle \{ \langle bus\text{-}signal\text{-}decl \rangle \}
```

```
::= \langle ident \rangle \text{ `:' } \langle type\text{-}name \rangle \text{ [ `=' } \langle expression \rangle \text{ ] [ `,' } \langle signal\_direction \rangle \text{ ] `;' }
\langle bus-signal-decl\rangle
                                           ::= \langle name \rangle '-> ' \langle name \rangle
\langle connect\text{-}entry \rangle
                                           ::= \ connect \ \langle \mathit{connect\text{-}entry} \rangle \ \{ \ \text{`,'} \ \langle \mathit{connect\text{-}entry} \rangle \ \} \ \text{';'}
\langle connect\text{-}decl \rangle
                                           ::= 'instance' \langle instance\text{-}name \rangle 'of' \langle ident \rangle
\langle inst\text{-}decl \rangle
                                                    '(' [ \langle param-map \rangle { ', ' \langle param-map \rangle } ] ')' ';'
\langle instance-name \rangle
                                           ::= \langle ident \rangle '[' \langle expression \rangle ']' (indexed instance)
                                                   \langle ident \rangle (named instance)
                                                   '_' (anonymous instance)
                                           ::= [\langle ident \rangle :: ] \langle expression \rangle
\langle param-map \rangle
                                           ::= 'generate' \langle ident \rangle '=' \langle expression \rangle 'to' \langle expression \rangle
\langle gen\text{-}decl \rangle
                                                    `\{' \{ \langle network\text{-}decl \rangle \} `\}'
                                            ::= \langle name \rangle \text{ `=' } \langle expression \rangle \text{ `;' } (assignment) \\ | \langle name \rangle \text{ `(' } \langle param-map \rangle \text{ `)'';' } (function call) 
\langle statement \rangle
                                                  'if' \langle expression \rangle '{' { \langle statement \rangle } '}'
                                                    \{ \langle elif\text{-}block \rangle \} [ \langle else\text{-}block \rangle ]
                                              'for' \(\langle ident \rangle \) '=' \(\langle expression \rangle \) 'to' \(\langle expression \rangle \)
                                                   '{' { \( \statement \) \\ \}'}'
                                              'switch' (simple-expression)
                                                    ``\{` \langle switch\text{-}case \rangle \ \{ \ \langle switch\text{-}case \rangle \ \} \ [ ``default' ``\{' \ \langle statement \rangle \ \} \ ]
                                                    'trace' '(' \langle format-string \rangle \text{ ',' \langle expression \rangle \rangle ')'';' 
'assert' '(' \langle expression \rangle \rangle \text{ ',' \langle expression \rangle \rangle ',' \langle string-literal \rangle \rangle \rangle ')'';'
\langle switch\text{-}case \rangle
                                           ::= 'case' \langle simple-expression \rangle ' \{ ' \{ \langle statement \rangle \} ' \} '
\langle elif\text{-}block \rangle
                                           ::= 'elif <expression> '{' { \langle statement \rangle }'}'
                                           ::= 'else' '{' { \langle statement \rangle } '}'
\langle else-block \rangle
                                          ::= '"' { \langle format\text{-}string\text{-}part \rangle } '"'
\langle format\text{-}string \rangle
⟨format-string-part⟩ ::= '{}' (placeholder string)
                                              \langle string\text{-}char \rangle
\langle simple-expression \rangle ::= \langle literal \rangle
                                              |\langle name \rangle|
```

```
\langle expression \rangle
                              ::= \langle simple-expression \rangle
                                    \langle expression \rangle \langle bin-op \rangle \langle expression \rangle
                                    \langle un\text{-}op\rangle \langle expression\rangle
                                    (((expression)))
                                    '(' \langle type\text{-}name \rangle ')' \langle expression \rangle (type cast)
                              ::= '+' (addition)
\langle bin-op \rangle
                                    '-' (subtraction)
                                    '*' (multiplication)
                                    '/' (division)
                                    "," (modulo)
                                    '==' (equal)
                                    '!=' (not equal)
                                    '<<' (shift left)
                                    '>>' (shift right)
                                    '<' (less than)
                                    '>' (greater than)
                                    '>=' (greater than or equal)
                                    <=' (less than or equal)
                                    \mbox{`\&'} (bitwise-and)
                                    'l' (bitwise-or)
                                    '^' (bitwise-xor)
                                    '&&' (logical conjunction)
                                    'll' (logical disjunction)
                              := '-' (negation)
\langle un-op \rangle
                                    '+' (identity)
                                    '!' (logical negation)
                                    '~' (bitwise-not)
\langle literal \rangle
                              ::= \langle integer \rangle
                                    \langle floating \rangle
                                    \langle string\text{-}literal \rangle
                                    '[' \(\langle integer \rangle \) \(\langle i\), '\(\langle integer \rangle \) \(\langle i\) ']' (Array literal)
                                    'true'
                                    'false'
                                    "U" (Undefined value)
                              ::= '"'{ \langle string\text{-}char \rangle }'"'
\langle string\text{-}literal \rangle
                              ::= 'i' \langle integer \rangle (signed integer)
\langle intrinsic-type \rangle
                                    'int' (arbitrary-width signed integer)
                                    'u' \(\langle integer\rangle\) (unsigned integer)
                                    'uint' (arbitrary-width unsigned integer)
                                    'float' (arbitrary-width floating point)
                                    'f8' (8 bit floating point)
```

```
'f16' (16 bit floating point)
                                          'f32' (single-precision floating point)
                                          'f64' (double-precision floating point)
                                          'bool' (boolean value)
\langle type\text{-}name \rangle
                                   ::= \langle intrinsic-type \rangle
                                     |\langle name \rangle| (type definition)
                                          '[' [ \( \langle expression \rangle \) ] ']' \( \langle type-name \rangle \) (array of type)
                                   ::= \langle letter \rangle \{ \langle letter \rangle \mid \langle number \rangle \mid `\_' \mid `-' \}  (identifier)
\langle ident \rangle
\langle name \rangle
                                   ::= \langle ident \rangle
                                    |\langle name \rangle '.' \langle name \rangle (hierarchical accessor)
                                     | \( \langle name \rangle \) '[' \( \langle array-index \rangle \) ']' (array element access)
                                   ::= `*' (wildcard)
\langle array\text{-}index \rangle
                                    |\langle expression \rangle  (element index)
\langle integer \rangle
                                   ::= \langle number \rangle \{ \langle number \rangle \}  (decimal number)
                                          '0x' \langle hex\text{-}digit \rangle { \langle hex\text{-}digit \rangle } (hexadecimal number)
                                          '00' \langle octal-digit \rangle { \langle octal-digit \rangle } (octal number)
                                   ::= \{ \langle number \rangle \} '.' \langle number \rangle \{ \langle number \rangle \}
\langle floating \rangle
                                   ::= '0' - '9'
\langle number \rangle
\langle letter \rangle
                                   ::= 'a' - 'z'
                                    | 'A' - 'Z'
\langle hex\text{-}digit \rangle
                                   ::= \langle number \rangle
                                    | 'a' - 'f'
| 'A' - 'F'
\langle octal\text{-}digit \rangle
                                  ::= '0' - '8'
\langle string\text{-}char \rangle
                                  ::= (ISO-8859-1 \text{ char with value} > 26)
```

## Operator precedence

Precedence	Operators
0	+ - ! ~ (unary)
1	* / %
2	+ -
3	<< >>
4	< > <= >=
5	== !=
6	& ^
7	&&
8	11

## Keywords

•	as

• enum

 $\bullet$  of

 $\bullet$  exposed

 $\bullet$  out

• await

 $\bullet$  for

• proc

 $\bullet$  barrier

 $\bullet$  from

• return

breakbus

functiongenerate

• switch

• case

• if

• sync

 $\bullet$  const

• import

• to

 $\bullet$  connect

• in

 $\bullet$  unique

 $\bullet$  clocked

instanceinverse

• var

• default

• network

 $\bullet$  wait

elifelse

• normal

 $\bullet$  where