```
setup> cirread -v design/SoCV/vending/vending.v
Converted 0 1-valued FFs and 48 DC-valued FFs.

setup> print info -verbose
Modules in current design: \vendingMachine(242 wires, 71 cells)
==================================================
    MUX                                          7
    AND                                          3
    ADD                                         22
    SUB                                          9
    MUL                                          8
    EQ                                           9
    NOT                                          2
    LT                                           1
    GE                                           8
--------------------------------------------------
    PI                                           7
    PO                                           7
==================================================
```

```
setup> cirread -blif design/SoCV/vending/vending.blif

setup> print info -verbose
Modules in current design: \vendingMachine(550 wires, 177 cells)
==================================================
    MUX                                         58
    AND                                          2
    ADD                                         22
    SUB                                          9
    MUL                                          6
    EQ                                           7
    NOT                                          7
    LT                                           1
    GE                                           8
--------------------------------------------------
    PI                                          12
    PO                                          17
==================================================
```

```
setup> cirprint

Circuit Statistics
==================
  PI          60
  PO          17
  LATCH       49
  AIG       3036
------------------
  Total     3162

setup> S
```