

- FID1 Front Fiducial
- FID2 Front Fiducial
- FID3 Front Fiducial
- FID4 Back Fiducial
- FID5 Back Fiducial
- FID6 Back Fiducial

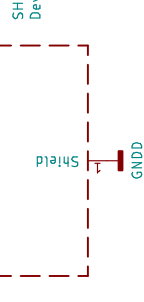
Sheet: Sheet05A7B86
File: Coax Cable and Diagnostic DUT.sch

Blue dashes indicate traces that ought to be guarded using similar voltage guard ring. Remove solder mask from the guard rings to guard against leakage and contamination. In addition to vias in the input net on the other side of the board as well.

With triax cabling employed (not shown) GNDd will be a shieldable guard conductor

I NOT VOLTAGE

Stock shield symbol below slightly incorrect: it is the top that connects to GND while the bottom is not used



When used as designed (body soldered to the shield and removed), the height of this shield is 10mm and not hermetically sealed.

Instead, hermetic sealing is suggested to provide you with the ultimate in long term sensitivity stability in scientific applications. This is achieved by which will also benefit from J1 being a triax connector instead of the phone connector shown.

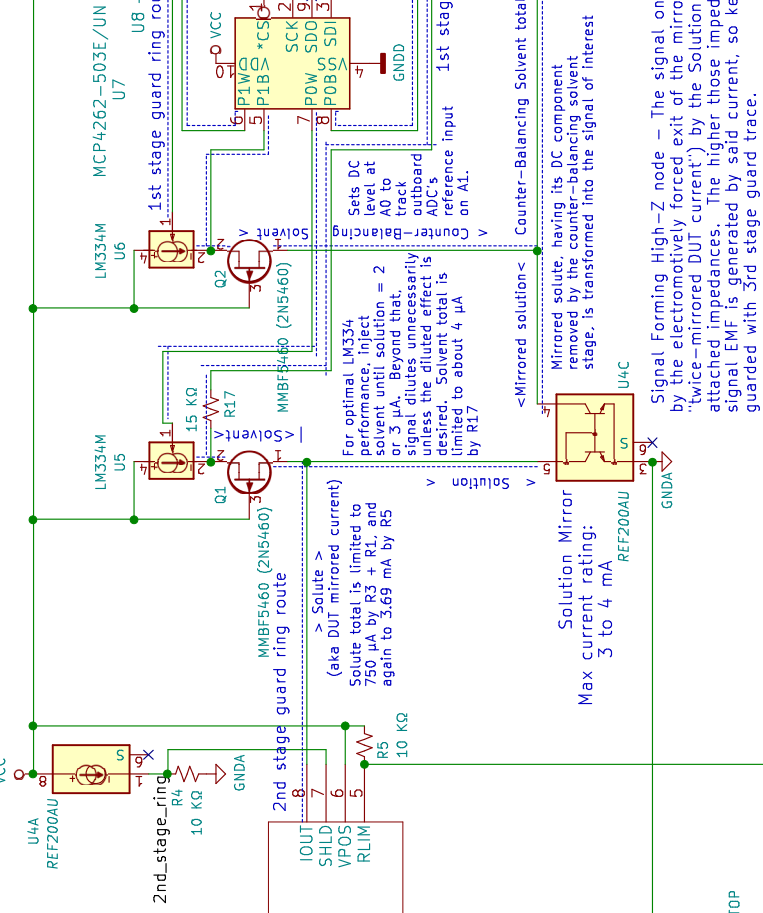
Hermetic sealing is achieved by: #1) Solder sealing all micro-vias of the PCB under the shield, and

#2) not using the shield body as a triax connector. Instead, the triax connector is used to connect the shielded package (supplied by manufacturer with U4) stuffed inside to minimize moisture degradation. U4 performance slightly over time in humid environments.

NOTE: Vcc net will remain labeled +5V until +3.3V operation is fully validated. Operation currently at +3.3V is a little sticky, likely due to the following note.
NOTE FOR 3.3V OPERATION — In 3.3V operation, the voltage drop that Q2 adds for the sake of not allowing the current to flow through the diode is not that is found to be the case, that JFET may be shorted across D to S with the expected result to be reduced signal formation.

MMBF5460 (2N5460) NOTE: SMD Source & Drain MUST BE interchangeable due to starting off this project with a SMD package. Since the manufacturer should not be prohibited by the manufacturer states that specific interchangeability is present in the MMBF5460 SMD package anyway. Also note: Q1 contributes nothing to functionality, it is only there to provide a path for current to flow from other guard traces, and their guard trace weaves between board sides leaving no room for another.

2nd stage guard ring source



NOTE THE ABOVE UNUSUAL USE OF, AND DISTINCTION BETWEEN CHEMISTRY TERMS "SOLUTE", "SOLVENT", AND "SOLUTION" ANALOGOUSLY APPLIED TO ELECTRICAL CURRENT

Q1,2 pinch-off testing: Q1,2 need to place enough voltage across LM334 at 68K [25k setting in 50K device] to 13K [66 setting in 50K device] pin while at the same time Q2 presents very high impedance load to U4C pin 4 as found by proving Q2 has not saturated.

NOTE — Due to this current mirror being referenced low, the signal gets inverted (higher current=lower voltage). No stage following is also inverting, so unfortunately it becomes mirrored. I haven't done that to the point of being essential to the lay operator — not worth my time right now, all things considered. For now, the operator may just have to envision the plot line as equating to DUT stress or electrical resistance instead of conductance.

NOTE — Future temperature compensation is expected to be accomplished by examining and balancing the tempo of the temperature drift. If a different tempo is found to be required for ACL balance, changing the tempos of these resistors would be the solution.

Only a single jumper to be used at a time. The jumper is used to select the modules and a third for ESP-01 adapter, though only one of the modules can be chosen from that lineup. If ESP-01 is needed to be GND for the JPA shunt but also solder-jumper on board front to surrounding copper. If it is needed for connect to Rx, pad jumpering is available connects to Rx of the other & vice versa. That is why you'll notice 1x printed on the wireless module goes to MCU Rx.

Conn_01x09_Female

6.9 STATELED

8 X JPA 2HC-05.EV.KEY

3 X JPA 2HC-05.EV.KEY

2 X JPA 2HC-05.EV.KEY

1 X JPA 2HC-05.EV.KEY

0.1 uF 10 uF

C4

U36

U35

U34

U33

U32

U31

U30

U29

U28

U27

U26

U25

U24

U23

U22

U21

U20

U19

U18

U17

U16

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U-34

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U-37

U-38

U-39

U-40

U-41

U-42

Not included — not enough board space

This function has to be done manually during assembly if a custom distribution is wanted.

If level translation on data lines becomes AN77055 and AN10441 by NXP.

Directions in link to PC functions in link to PC

1st stage guard ring

2nd stage guard ring

3rd stage guard ring

4th stage guard ring

5th stage guard ring

6th stage guard ring

7th stage guard ring

8th stage guard ring

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10th stage guard ring

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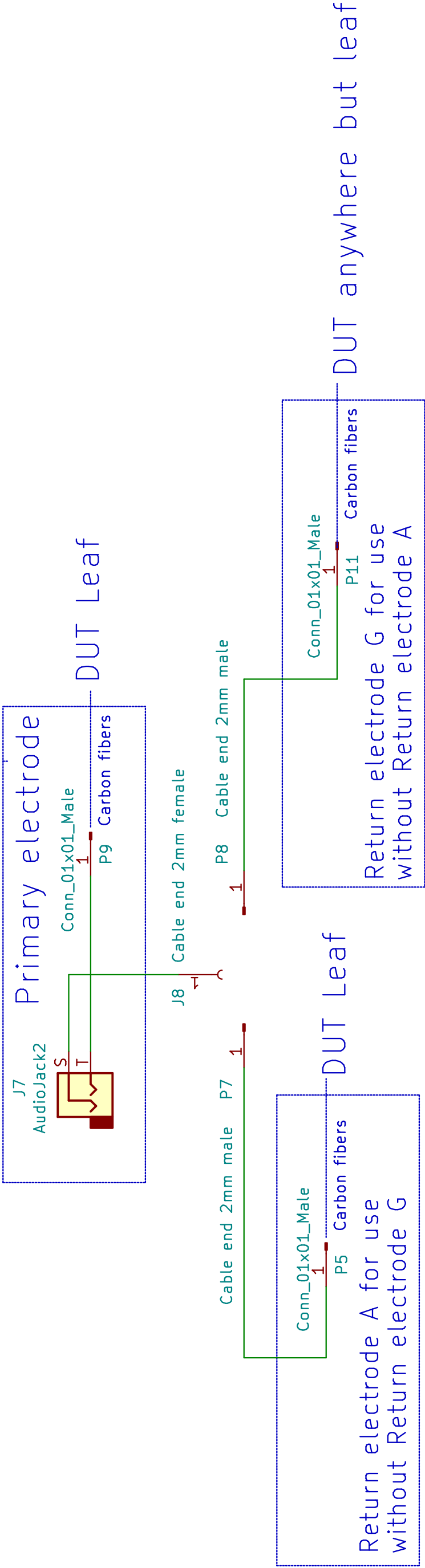
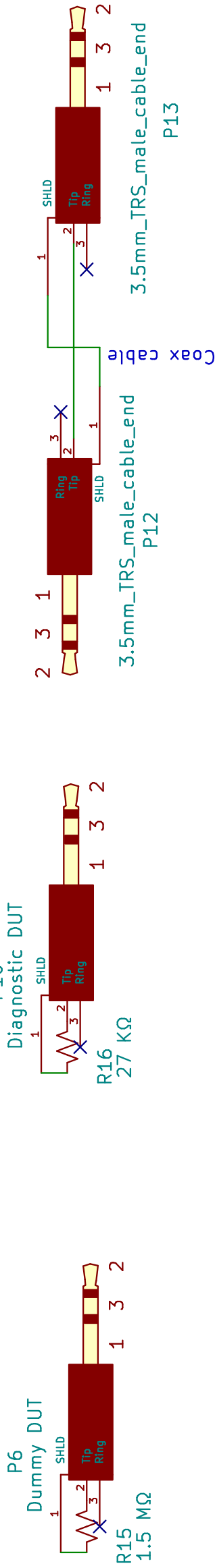
9th stage guard ring

10th stage guard ring

11th stage guard ring

12th stage guard ring

NOTE: These symbols are required to generate an accurate BOM. To use them in that way, copy them and paste them anywhere into the main page. They do not have to show within any sheet boundary. Generate the new BOM and delete the symbol copies from the main page, when finished, always leaving the original symbols in this sheet. If the copies are left in the main page when later updating the PCB, they will interfere with the generation of the PCB Gerber files because these have no residencies on the PCB.



The pins inside the electrodes are to enable gold compatible finish electrical contact in the electrode assemblies for the carbon fiber. One pin belongs in each electrode assembly.

Sheet: /Sheet5D5A7B86/
File: Coax Cable and Diagnostic DUT.sch

Title:

Size: A4 Date:

KiCad E.D.A. kicad 5.1.4-e60b26684ubuntu16.04.1

Rev:

Id: 2/2