

Options Box: 3 Vdc (Instead of 5Vdc) – Q2 unused, solder–short Q2 footprint D to S, possibly lower sensitivity result, verify ICSP1 power as desired
HX711 – Use only for extreme sensitivity; will then require C5 and C6 and the specified PCB version
A6 and A7 – These Analog inputs are found on some of the better UNO Arduino boards, but all SMD UNO –AU boards have capability for the hacker who is willing to tap onto pins 19 and 22 of the ATMEGA328P–AU. Jumpers 7 & 12 allow purely optional name switch at P2 Power/Reset header.

Triax cable from J5 – shield would be GNDD
JP1 and JP2 – UNTESTED if either or both are changed to other side with cut and solder, they can prolong lives of C1/C2.

HX–05 – UNTESTED and no code developed. Various wirings exist of this board. Ensure which jumper configuration for your style. Intended to allow an alternative to USB communications for plotting

Alternative Op Amps – Careful! I–bias linearity of U2A is very consequential R19 RF– Value might be in the G Ohms

Blue dashes indicate traces that ought to be guarded using similar voltage guard ring. Remove solder mask from the guard rings to guard against surface leakage due to contamination. In addition to the guard rings, high Ohm resistors are soldered around any Vds in the input net on the other side of the board and wire.

With triax cabling employed, GNDA would join to cable's guard conductor and GNDD to shield.

NOT VOLAGE

Security Immobilize DUT

conductor and GNDD to shield. Near–GNDA used for guard ring of J5–PadR

When used as designed (body soldered to PCB and cover used as designed), the shield is 10mm and not hermetically sealed.

Instead, hermetic sealing is suggested to provide you with the ultimate in long term sensitivity stability in scientific instrumentation class usages. This is achieved by using a being a triax connector instead of the triax connector shown.

Flex ckt'g the guarded signal path under the shielding (not yet designed) instead of ringing it would be an additional improvement if such ckt is used to prevent flexing during operation.

Hermetic sealing is achieved by: #1) Solder sealing all micro–vias of the PCB under the shield, and #2) not using the shield body, only the shield top in place with the dried desiccant in package (supplied by manufacturer with U4) stuffed with desiccant to prevent moisture absorption by U4 that could degrade U4 performance slightly over time in humid environments.

More info may be read from "Ultra–high Sensitivity Femtoamp Measurements" authored by Vicki Wong, published by Analog Devices

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NOTE: Operation currently at +3.3V is a little sticky, likely due to the following note.
NOTE FOR 3.3V OPERATION – In 3.3V operation, the output of the ADC is not as clean as in 5V operation. The highest output impedance against U4C might not allow 3.3V operation with an onboard ADC. If that is found to be the case, that JFET may be oriented across D to S with the expected result to be reduced signal formation.

MBF5460 (2N5460) NOTE: Q1 contributes nothing to functionality except to permit using the JFET as a switch. The JFET is not needed since they are co–located, distant from other guard traces, and their guard trace weaves between board sides leaving no room for another.

Blue dashes indicate traces that ought to be guarded using similar voltage guard ring. Remove solder mask from the guard rings to guard against surface leakage due to contamination. In addition to the guard rings, high Ohm resistors are soldered around any Vds in the input net on the other side of the board and wire.

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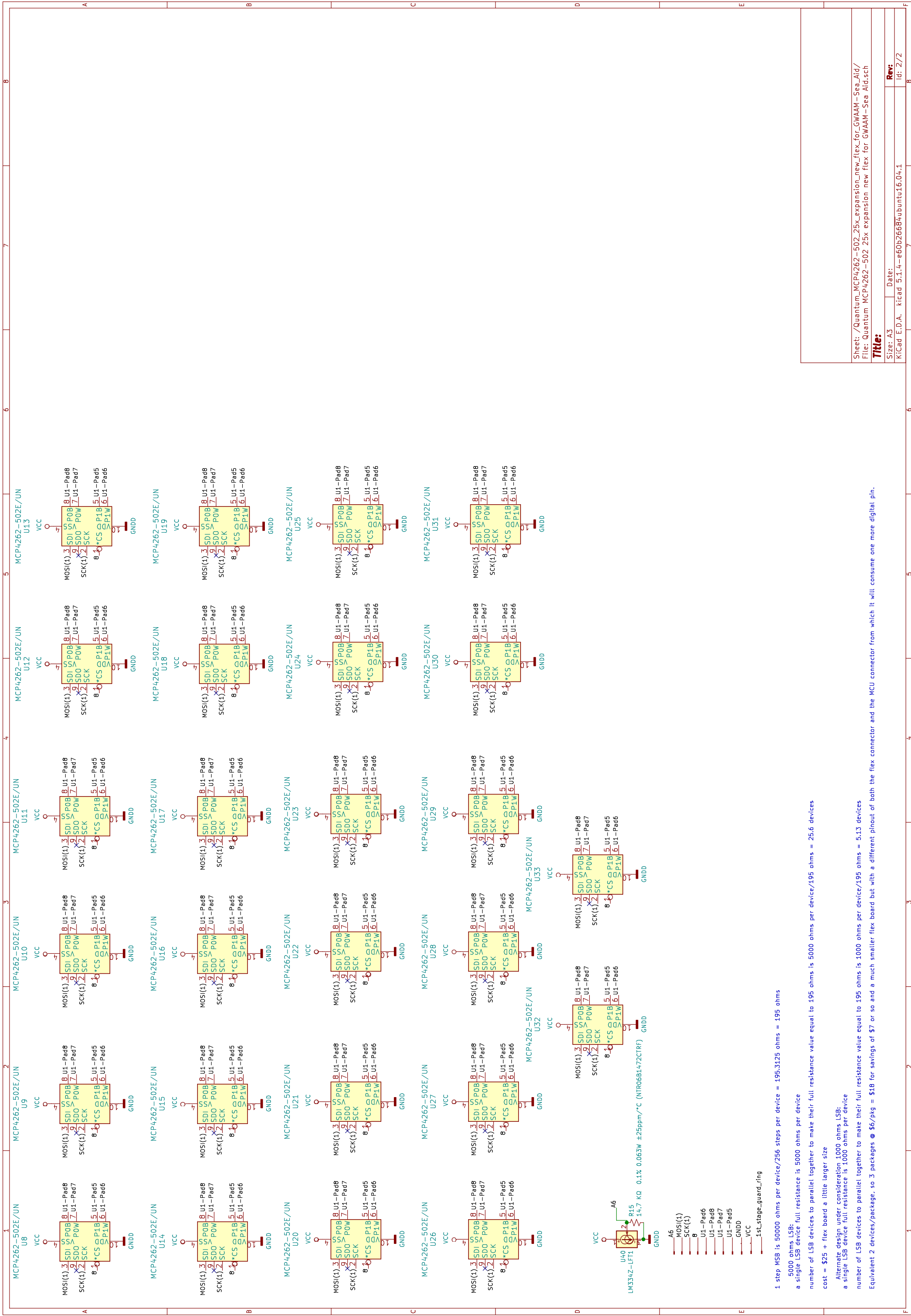
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See "Options" box also.

Only a single jumper to be used at a time. The board is designed for ESP–01 modules and a third for ESP–01 adapter, though only one of the modules can be chosen from that lineup. If ESP–01 is chosen, the JPA shunt is needed to be on the JPA shunt but also solder–jumper on board front to surrounding copper. If it is needed for connect to Rx, pad jumpering is available connects to Rx of the other & vice versa. That is why you'll notice 1x printed on the wireless module goes to MCU Rx.

IC–DB_LED/STIMLED
J5 STATE_LED
J6 Rx VCC
J7 Tx
J8 Rx VCC
J9 Tx
J10 Rx VCC
J11 Tx
J12 Rx VCC
J13 Tx
J14 Rx VCC
J15 Tx
J16 Rx VCC
J17 Tx
J18 Rx VCC
J19 Tx
J20 Rx VCC
J21 Tx
J22 Rx VCC
J23 Tx
J24 Rx VCC
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J1099 Tx
J1100 Rx VCC
J1101 Tx
J1



number of USB devices to parallel together to make their full resistance value equal to 195 ohms is 1000 ohms per device/195 ohms = 5.13 devices. Equivalent 2 devices/package, so 3 packages @ \$6/pkg = \$18 for savings of \$7 or so and a much smaller flex board but with a different pinout of

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