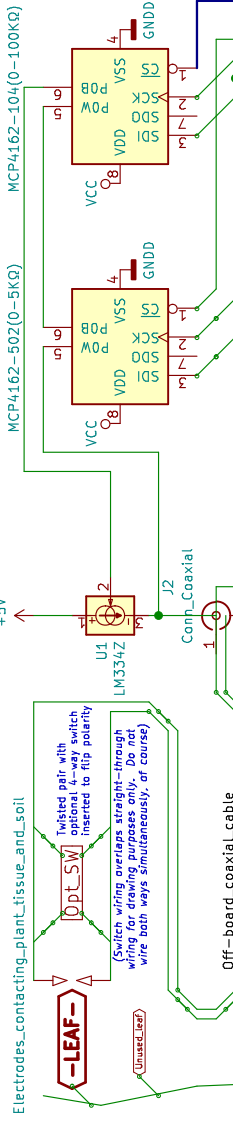
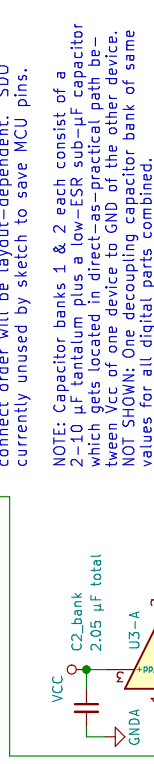


NOTE: The LM334 devices must receive 5 VDC or greater due to their own operating voltage range. Their functional range will be substantially limited if powered by the 3 or 3.3 VDC of many MCUs. Provide a dedicated positive power supply to them if 5 VDC is not otherwise available, then carefully ensure the output levels from them are prevented from exceeding the input range of the remaining circuitry. Alternative: use non-LM334 legs (AKA "bare" legs) & adjust macros – POW/B will then connect +5V to DUT or R1

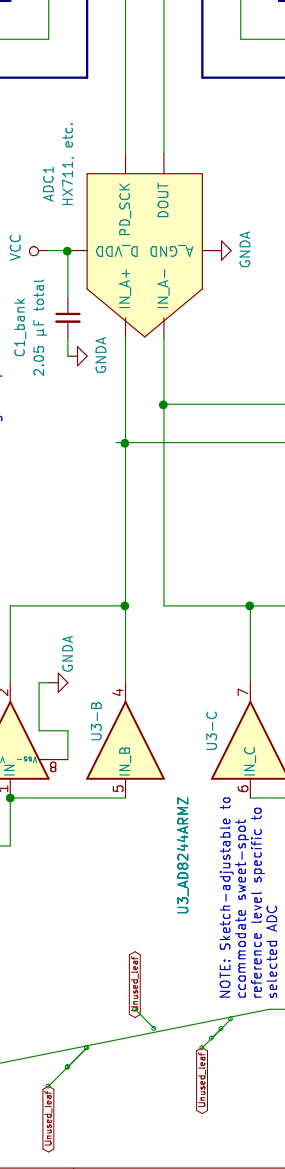


NOTE: If convenient polarity flipping is desired, GND_A a 4-way house light switch may be placed in the electrode circuit between the electrodes and the coaxial cable, assuming you hook it up correctly.



NOTE: Additional matching MSB-level DPots are allowed in this POW-POB loop. Only a single LSB-level DPot is allowed which must be of a resistance lower than or equal to MSB-level DPots. Pins 5 and 6 are interchangeable, so the actual connect order will be layout-dependent. SDO currently unused by sketch to save MCU pins.

NOTE: Capacitor banks 1 & 2 each consist of a 2-10 μF tantalum plus a low-ESR sub-μF capacitor which gets located in direct-as-practical path between Vcc of one device to GND of the other device. NOT SHOWN: One decoupling capacitor bank of same values for all digital parts combined.



U3-AD8244ARWZ

NOTE: Sketch-adjustable to accommodate sweet-spot reference level specific to selected ADC

NOTE: Unfortunately, the devices shown do not all share common data protocols except the DPots. Define the Digital Pin functions in preprocessor macros for all the disparate protocols. Thus, some flexibility remains for driving an additional device or two that may have inflexible pin assignments.

NOTE: Arduino also gets power rails and a serial connection to a computer

*NOTE FOR MCU ANALOG PINS: Low-numbered Analog input pins are reserved for all existing DPot-controlled leg outputs. Unused Analog Pins above them may be used to display additional plotter traces when so defined in preprocessor macros.

This system is shown using:
1 – LM334 bridge
1 – HX711 24-bit ADC
1 – LSB DPot/leg
1 – MSB DPot/leg

Shown not being used:
2 – 74LV138; useful with high DPot count
Non-LM334 configuration options include bridged and single leg to replace or augment LM334 option.

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Device Under Test (DUT)

*See note for the above DPot set

Title: GWAAM Training Aid – System Diagram

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