







Both options 3 and 4 may have emitter resistors added.

Option 4 (with emitter degeneration) is the internal circuit of the REF200 IC, a current mirror balanced to $\pm 0.25\%$





Other mirror configurations

Transistors 101

Eposode 12: They Do It with Mirrors

Kludges from Kevin's Cave

Sheet: /Option 2/

File: option_2_wilson_sch.kicad_sch

Title: Current Mirror Demonstrator

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