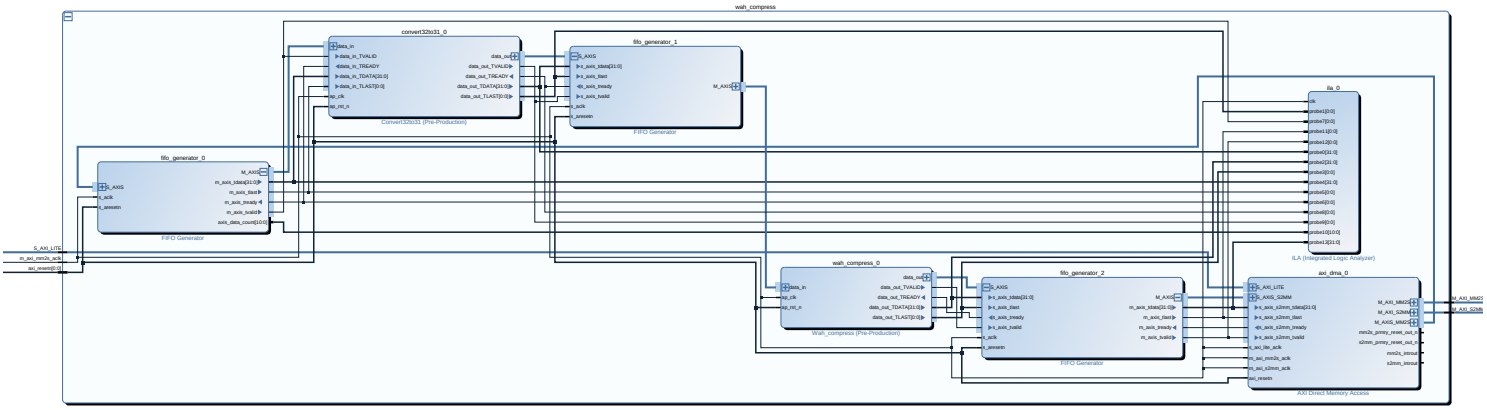
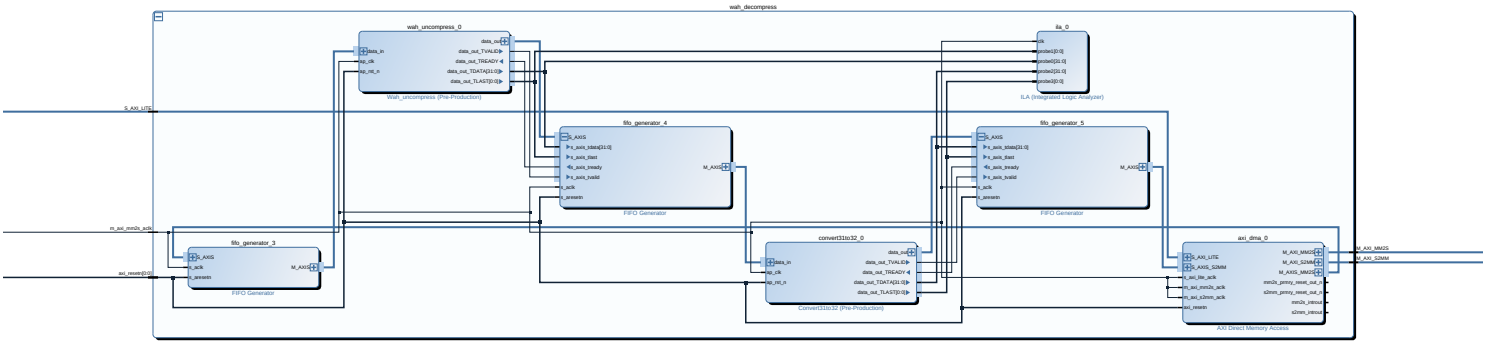

wah_compress
convert32to31_0
Convert32to31 (Pre-Production)
fifo_generator_1
FIFO Generator
fifo_generator_0
FIFO Generator
ila_0
ILA (Integrated Logic Analyzer)
wah_compress_0
Wah_compress (Pre-Production)
fifo_generator_2
FIFO Generator
axi_dma_0
AXI Direct Memory Access
S_AXI_LITE
m_axi_mm2s_aclk
axi_resetn[0:0]
M_AXI_MM2S
M_AXI_S2MM


wah_decompress
wah_uncompress_0
Wah_uncompress (Pre-Production)
ila_0
ILA (Integrated Logic Analyzer)
S_AXI_LITE
fifo_generator_4
FIFO Generator
fifo_generator_5
FIFO Generator
m_axi_mm2s_aclk
axi_resetn[0:0]
fifo_generator_3
FIFO Generator
convert31to32_0
Convert31to32 (Pre-Production)
axi_dma_0
AXI Direct Memory Access
M_AXI_MM2S
M_AXI_S2MM