		R	V1103 GP	IO V1.1	.0	
Name	Config	Description	Register	Position	Valu	IO Command
		PMIC_SLEEP_M0	0xFF388004	[0,2]	1	io -4 0xFF388004 0x00070001
	IOmux	PWM1_M0	0xFF388004	[0,2]	2	io -4 0xFF388004 0x00070002
		GPIO0_A4	0xFF388004	[0,2]	0	io -4 0xFF388004 0x00070000
		normal	0xFF388038	[8,9]	0	io -4 0xFF388038 0x03000000
	Pull	pullup	0xFF388038	[8,9]	1	io -4 0xFF388038 0x03000100
		pulldown	0xFF388038	[8,9]	2	io -4 0xFF388038 0x03000200
	I/O M 1	input	0xFF380008	[4,4]	0	io -4 0xFF380008 0x00100000
	I/O Mode	output	0xFF380008	[4,4]	1	io -4 0xFF380008 0x00100010
	0.4.4	low	0xFF380000	[4,4]	0	io -4 0xFF380000 0x00100000
	Output	high	0xFF380000	[4,4]	1	io -4 0xFF380000 0x00100010
GPIO0A4	T + C+ 1	disable	0xFF388030	[4,4]	0	io -4 0xFF388030 0x00100000
	Input Ctrl	enable	0xFF388030	[4,4]	1	io -4 0xFF388030 0x00100010
	G 1	disable	0xFF388058	[4,4]	0	io -4 0xFF388058 0x00100000
	Schmitt	enable	0xFF388058	[4,4]	1	io -4 0xFF388058 0x00100010
		level0	0xFF388018	[0,5]	1	io -4 0xFF388018 0x003F0001
		level1	0xFF388018	[0,5]	3	io -4 0xFF388018 0x003F0003
	D: 0: 1	level2	0xFF388018	[0,5]	7	io -4 0xFF388018 0x003F0007
	Drive Strength	level3	0xFF388018	[0,5]	15	io -4 0xFF388018 0x003F000F
		level4	0xFF388018	[0,5]	31	io -4 0xFF388018 0x003F001F
		level5	0xFF388018	[0,5]	63	io -4 0xFF388018 0x003F003F
	ExtPort	read gpio data in input mode	0xFF380070	[4,4]		io -4 0xFF380070
		PWM0_M0	0xFF538000	[8,10]	1	io -4 0xFF538000 0x07000100
	IOmux	AVS_ARM	0xFF538000	[8,10]	2	io -4 0xFF538000 0x07000200
		VICAP_D0_M1	0xFF538000	[8,10]	3	io -4 0xFF538000 0x07000300
		GPIO1_A2	0xFF538000	[8,10]	0	io -4 0xFF538000 0x07000000
		normal	0xFF5381C0	[4,5]	0	io -4 0xFF5381C0 0x00300000
	Pull	pullup	0xFF5381C0	[4,5]	1	io -4 0xFF5381C0 0x00300010
		pulldown	0xFF5381C0	[4,5]	2	io -4 0xFF5381C0 0x00300020
	I/O Mode	input	0xFF530008	[2,2]	0	io -4 0xFF530008 0x00040000
	1/O Mode	output	0xFF530008	[2,2]	1	io -4 0xFF530008 0x00040004
	Outmut	low	0xFF530000	[2,2]	0	io -4 0xFF530000 0x00040000
GPIO1A2	Output	high	0xFF530000	[2,2]	1	io -4 0xFF530000 0x00040004
GPIOTAZ	Input Ctrl	disable	0xFF538180	[2,2]	0	io -4 0xFF538180 0x00040000
	input Ctri	enable	0xFF538180	[2,2]	1	io -4 0xFF538180 0x00040004
	C =1:44	disable	0xFF538280	[2,2]	0	io -4 0xFF538280 0x00040000
	Schmitt	enable	0xFF538280	[2,2]	1	io -4 0xFF538280 0x00040004
		level0	0xFF538084	[0,5]	1	io -4 0xFF538084 0x003F0001
		level1	0xFF538084	[0,5]	3	io -4 0xFF538084 0x003F0003
	Duive Charact	level2	0xFF538084	[0,5]	7	io -4 0xFF538084 0x003F0007
	Drive Strength	level3	0xFF538084	[0,5]	15	io -4 0xFF538084 0x003F000F
		level4	0xFF538084	[0,5]	31	io -4 0xFF538084 0x003F001F
		level5	0xFF538084	[0,5]	63	io -4 0xFF538084 0x003F003F
	ExtPort	read gpio data in input mode	0xFF530070	[2,2]		io -4 0xFF530070

Name			R	V1103 GP	IO V1.1	.0		
Omix	Name	Config	Description	Register	Position	Valu	IO Command	
HPMCU_JTAG_TCK_M0			A7_JTAG_TCK_M1	0xFF538008	[8,10]	1	io -4 0xFF538008 0x07000100	
LPMCU_JTAG_TCK_M0			UART2_TX_M1	0xFF538008	[8,10]	2	io -4 0xFF538008 0x07000200	
Pull		IOmux	HPMCU_JTAG_TCK_M0	0xFF538008	[8,10]	3	io -4 0xFF538008 0x07000300	
Pull			LPMCU_JTAG_TCK_M0	0xFF538008	[8,10]	4	io -4 0xFF538008 0x07000400	
Pull			GPIO1_B2	0xFF538008	[8,10]	0	io -4 0xFF538008 0x07000000	
Pulldown 0xFF538104 (4,5] 2 io 4 0xFF5381C4 0x00300020			normal	0xFF5381C4	[4,5]	0	io -4 0xFF5381C4 0x00300000	
I/O Mode		Pull	pullup	0xFF5381C4		1	io -4 0xFF5381C4 0x00300010	
Order			pulldown	0xFF5381C4	[4,5]	2	io -4 0xFF5381C4 0x00300020	
Order		7/0 7/1	input	0xFF530008	[10,10]	0	io -4 0xFF530008 0x04000000	
Output		I/O Mode	output	0xFF530008	[10,10]	1	io -4 0xFF530008 0x04000400	
GPIO1B2				0xFF530000		0	io -4 0xFF530000 0x04000000	
Input Ctrl	GPIO1B2	Output	high	0xFF530000		1	io -4 0xFF530000 0x04000400	
Input Ctr)			0	io -4 0xFF538184 0x00040000	
Schmitt		Input Ctrl						
Schmitt		~	disable			0	io -4 0xFF538284 0x00040000	
Drive Strength		Schmitt	enable			1	io -4 0xFF538284 0x00040004	
Drive Strength			level0			1	io -4 0xFF538094 0x003F0001	
Drive Strength						3		
Drive Strength level3			level2					
Icvel4		Drive Strength						
ExtPort read gpio data in input mode 0xFF538094 [0,5] 63 io -4 0xFF538094 0x003F003F								
ExtPort						_		
IOmux		ExtPort						
IOmux						1		
IOmux		IOmux				2		
LPMCU_JTAG_TMS_M0								
Pull						4	io -4 0xFF538008 0x70004000	
Pull						_		
Pull			_	0xFF5381C4		_		
Dulldown OxFF5381C4 [6,7] 2 io -4 0xFF5381C4 0x00C00080 input OxFF530008 [11,11] 0 io -4 0xFF530008 0x08000000 output OxFF530008 [11,11] 1 io -4 0xFF530008 0x08000800 output OxFF530000 [11,11] 1 io -4 0xFF530000 0x08000000 output oxFF530000 [11,11] output oxFF530000 0x08000000 output oxFF530000 [11,11] output oxFF530000 0x08000000 output oxFF538184 [3,3] output output oxFF538184 output oxFF538184 output oxFF538184 output output oxFF538184 output oxFF538184 output output oxFF538184 output outp		Pull				1	io -4 0xFF5381C4 0x00C00040	
I/O Mode						2		
Output		7/0.7.5.4	^			0	io -4 0xFF530008 0x08000000	
Output low 0xFF530000 [11,11] 0 io -4 0xFF530000 0x08000000		I/O Mode		0xFF530008		1	io -4 0xFF530008 0x08000800	
Drive Strength Dutput high 0xFF538094 [8,13] 1 io -4 0xFF538094 0xF			•			0		
Drive Strength Input Ctrl disable 0xFF538184 [3,3] 0 io -4 0xFF538184 0x00080000 enable 0xFF538184 [3,3] 1 io -4 0xFF538184 0x00080008	GPIO1B3	Output	high			1		
Schmitt						0		
Schmitt disable enable 0xFF538284 [3,3] 0 io -4 0xFF538284 0x00080000 level0 0xFF538284 [3,3] 1 io -4 0xFF538284 0x00080008 level0 0xFF538094 [8,13] 1 io -4 0xFF538094 0x3F000100 level1 0xFF538094 [8,13] 3 io -4 0xFF538094 0x3F000300 level2 0xFF538094 [8,13] 7 io -4 0xFF538094 0x3F000700 level3 0xFF538094 [8,13] 15 io -4 0xFF538094 0x3F000F00 level4 0xFF538094 [8,13] 31 io -4 0xFF538094 0x3F001F00		Input Ctrl						
Drive Strength enable 0xFF538284 [3,3] 1 io -4 0xFF538284 0x00080008 level0 0xFF538094 [8,13] 1 io -4 0xFF538094 0x3F000100 level1 0xFF538094 [8,13] 3 io -4 0xFF538094 0x3F000300 level2 0xFF538094 [8,13] 7 io -4 0xFF538094 0x3F000700 level3 0xFF538094 [8,13] 15 io -4 0xFF538094 0x3F000F00 level4 0xFF538094 [8,13] 31 io -4 0xFF538094 0x3F001F00 level4 0xFF538094		~				0		
level0 0xFF538094 [8,13] 1 io -4 0xFF538094 0x3F000100 level1 0xFF538094 [8,13] 3 io -4 0xFF538094 0x3F000300 level2 0xFF538094 [8,13] 7 io -4 0xFF538094 0x3F000700 level3 0xFF538094 [8,13] 15 io -4 0xFF538094 0x3F000F00 level4 0xFF538094 [8,13] 31 io -4 0xFF538094 0x3F001F00		Schmitt						
Drive Strength								
Drive Strength								
level3								
level4 0xFF538094 [8,13] 31 io -4 0xFF538094 0x3F001F00		Drive Strength						
ExtPort read gpio data in input mode 0xFF530070 [11,11] io -4 0xFF530070		ExtPort						

		R	V1103 GP	IO V1.1	.0	
Name	Config	Description	Register	Position	Valu	IO Command
	8	LCD D7	0xFF538010	[0,2]	1	io -4 0xFF538010 0x00070001
		VICAP D2 M1	0xFF538010	[0,2]	2	io -4 0xFF538010 0x00070002
	***	PWM2 M2	0xFF538010	[0,2]	3	io -4 0xFF538010 0x00070003
	IOmux	SPI0 CS0N M0	0xFF538010	[0,2]	4	io -4 0xFF538010 0x00070004
		SDMMC1 D1 M1	0xFF538010	[0,2]	5	io -4 0xFF538010 0x00070005
		GPIO1 C0	0xFF538010	[0,2]	0	io -4 0xFF538010 0x00070000
		normal	0xFF5381C8	[0,1]	0	io -4 0xFF5381C8 0x00030000
	Pull	pullup	0xFF5381C8	[0,1]	1	io -4 0xFF5381C8 0x00030001
		pulldown	0xFF5381C8	[0,1]	2	io -4 0xFF5381C8 0x00030002
		input	0xFF53000C	[0,0]	0	io -4 0xFF53000C 0x00010000
	I/O Mode	output	0xFF53000C	[0,0]	1	io -4 0xFF53000C 0x00010001
		low	0xFF530004	[0,0]	0	io -4 0xFF530004 0x00010000
GPIO1C0	Output	high	0xFF530004	[0,0]	1	io -4 0xFF530004 0x00010001
		disable	0xFF538188	[0,0]	0	io -4 0xFF538188 0x00010000
	Input Ctrl	enable	0xFF538188	[0,0]	1	io -4 0xFF538188 0x00010001
		disable	0xFF538288	[0,0]	0	io -4 0xFF538288 0x00010000
	Schmitt	enable	0xFF538288	[0,0]	1	io -4 0xFF538288 0x00010001
		level()	0xFF5380A0	[0,5]	1	io -4 0xFF5380A0 0x003F0001
		level1	0xFF5380A0	[0,5]	3	io -4 0xFF5380A0 0x003F0003
		level2	0xFF5380A0	[0,5]	7	io -4 0xFF5380A0 0x003F0007
	Drive Strength	level3	0xFF5380A0	[0,5]	15	io -4 0xFF5380A0 0x003F000F
	ExtPort	level4	0xFF5380A0	[0,5]	31	io -4 0xFF5380A0 0x003F001F
		level5	0xFF5380A0	[0,5]	63	io -4 0xFF5380A0 0x003F003F
		read gpio data in input mode		[16,16]	0.5	io -4 0xFF530070
	Extr of	LCD D6	0xFF538010	[4,6]	1	io -4 0xFF538010 0x00700010
	IOmux	VICAP D3 M1	0xFF538010	[4,6]	2	io -4 0xFF538010 0x00700020
		PWM4 M2	0xFF538010	[4,6]	3	io -4 0xFF538010 0x00700030
		SPI0 CLK M0	0xFF538010	[4,6]	4	io -4 0xFF538010 0x00700040
		SDMMC1 D0 M1	0xFF538010	[4,6]	5	io -4 0xFF538010 0x00700050
		GPIO1 C1	0xFF538010	[4,6]	0	io -4 0xFF538010 0x00700000
		normal	0xFF5381C8	[2,3]	0	io -4 0xFF5381C8 0x000C0000
	Pull	pullup	0xFF5381C8	[2,3]	1	io -4 0xFF5381C8 0x000C0004
	1 611	pulldown	0xFF5381C8		2	io -4 0xFF5381C8 0x000C0008
		input	0xFF53000C	[1,1]	0	io -4 0xFF53000C 0x00020000
	I/O Mode	output	0xFF53000C	[1,1]	1	io -4 0xFF53000C 0x00020002
		low	0xFF530004	[1,1]	0	io -4 0xFF530004 0x00020000
GPIO1C1	Output	high	0xFF530004	[1,1]	1	io -4 0xFF530004 0x00020002
		disable	0xFF538188	[1,1]	0	io -4 0xFF538188 0x00020000
	Input Ctrl	enable	0xFF538188	[1,1]	1	io -4 0xFF538188 0x00020002
		disable	0xFF538288	[1,1]	0	io -4 0xFF538288 0x00020000
	Schmitt	enable	0xFF538288	[1,1]	1	io -4 0xFF538288 0x00020002
		level0	0xFF5380A0		1	io -4 0xFF5380A0 0x3F000100
		level1	0xFF5380A0		3	io -4 0xFF5380A0 0x3F000300
		level2	0xFF5380A0		7	io -4 0xFF5380A0 0x3F000700
	Drive Strength	level3	0xFF5380A0	[8,13]	15	io -4 0xFF5380A0 0x3F000F00
		level4	0xFF5380A0		31	io -4 0xFF5380A0 0x3F001F00
		level5	0xFF5380A0		63	io -4 0xFF5380A0 0x3F003F00
	ExtPort	read gpio data in input mode		[17,17]	0.5	io -4 0xFF530070
	LAH UIT	read Spio data in input mode	JAI 1 JJ00/0	[1/,1/]	<u> </u>	10 1 0/11 3300 / 0

		R	V1103 GP	IO V1.1	.0		
Name	Config	Description	Register	Position	Valu	IO Command	
		LCD D5	0xFF538010	[8,10]	1	io -4 0xFF538010 0x07000100	
		VICAP_D4_M1	0xFF538010	[8,10]	2	io -4 0xFF538010 0x07000200	
		PWM5_M2	0xFF538010	[8,10]	3	io -4 0xFF538010 0x07000300	
	IOmux	I2C4 SCL M1	0xFF538010	[8,10]	4	io -4 0xFF538010 0x07000400	
		SDMMC1 CLK M1	0xFF538010	[8,10]	5	io -4 0xFF538010 0x07000500	
		SPI0 MOSI M0	0xFF538010	[8,10]	6	io -4 0xFF538010 0x07000600	
		GPIO1_C2	0xFF538010	[8,10]	0	io -4 0xFF538010 0x07000000	
		normal	0xFF5381C8	[4,5]	0	io -4 0xFF5381C8 0x00300000	
	Pull	pullup	0xFF5381C8	[4,5]	1	io -4 0xFF5381C8 0x00300010	
		pulldown	0xFF5381C8	[4,5]	2	io -4 0xFF5381C8 0x00300020	
	I/O M 1	input	0xFF53000C	[2,2]	0	io -4 0xFF53000C 0x00040000	
	I/O Mode	output	0xFF53000C	[2,2]	1	io -4 0xFF53000C 0x00040004	
GPIO1C2	0.4.4	low	0xFF530004	[2,2]	0	io -4 0xFF530004 0x00040000	
	Output	high	0xFF530004	[2,2]	1	io -4 0xFF530004 0x00040004	
	I (C) 1	disable	0xFF538188	[2,2]	0	io -4 0xFF538188 0x00040000	
	Input Ctrl	enable	0xFF538188	[2,2]	1	io -4 0xFF538188 0x00040004	
	G 1	disable	0xFF538288	[2,2]	0	io -4 0xFF538288 0x00040000	
	Schmitt	enable	0xFF538288	[2,2]	1	io -4 0xFF538288 0x00040004	
		level0	0xFF5380A4	[0,5]	1	io -4 0xFF5380A4 0x003F0001	
		level1	0xFF5380A4	[0,5]	3	io -4 0xFF5380A4 0x003F0003	
	5	level2	0xFF5380A4	[0,5]	7	io -4 0xFF5380A4 0x003F0007	
	Drive Strength	level3	0xFF5380A4	[0,5]	15	io -4 0xFF5380A4 0x003F000F	
		level4	0xFF5380A4	[0,5]	31	io -4 0xFF5380A4 0x003F001F	
		level5	0xFF5380A4	[0,5]	63	io -4 0xFF5380A4 0x003F003F	
	ExtPort	read gpio data in input mode		[18,18]		io -4 0xFF530070	
		LCD D4	0xFF538010	[12,14]	1	io -4 0xFF538010 0x70001000	
	IOmux	VICAP D5 M1	0xFF538010	[12,14]	2	io -4 0xFF538010 0x70002000	
		PWM6 M2	0xFF538010	[12,14]	3	io -4 0xFF538010 0x70003000	
		I2C4 SDA M1	0xFF538010	[12,14]	4	io -4 0xFF538010 0x70004000	
		SDMMC1 CMD M1	0xFF538010	[12,14]	5	io -4 0xFF538010 0x70005000	
		SPI0 MISO M0	0xFF538010	[12,14]	6	io -4 0xFF538010 0x70006000	
		GPIO1 C3	0xFF538010	[12,14]	0	io -4 0xFF538010 0x70000000	
		normal	0xFF5381C8	[6,7]	0	io -4 0xFF5381C8 0x00C00000	
	Pull	pullup	0xFF5381C8	[6,7]	1	io -4 0xFF5381C8 0x00C00040	
		pulldown	0xFF5381C8	[6,7]	2	io -4 0xFF5381C8 0x00C00080	
	I/O M 1	input	0xFF53000C	[3,3]	0	io -4 0xFF53000C 0x00080000	
	I/O Mode	output	0xFF53000C	[3,3]	1	io -4 0xFF53000C 0x00080008	
GPIO1C3	0 4 4	low	0xFF530004	[3,3]	0	io -4 0xFF530004 0x00080000	
	Output	high	0xFF530004	[3,3]	1	io -4 0xFF530004 0x00080008	
	I (C) 1	disable	0xFF538188	[3,3]	0	io -4 0xFF538188 0x00080000	
	Input Ctrl	enable	0xFF538188	[3,3]	1	io -4 0xFF538188 0x00080008	
	G 1	disable	0xFF538288	[3,3]	0	io -4 0xFF538288 0x00080000	
	Schmitt	enable	0xFF538288	[3,3]	1	io -4 0xFF538288 0x00080008	
		level0	0xFF5380A4	[8,13]	1	io -4 0xFF5380A4 0x3F000100	
		level1	0xFF5380A4	[8,13]	3	io -4 0xFF5380A4 0x3F000300	
	Doing Grand	level2	0xFF5380A4	[8,13]	7	io -4 0xFF5380A4 0x3F000700	
	Drive Strength	level3	0xFF5380A4	[8,13]	15	io -4 0xFF5380A4 0x3F000F00	
		level4	0xFF5380A4	[8,13]	31	io -4 0xFF5380A4 0x3F001F00	
		level5	0xFF5380A4	[8,13]	63	io -4 0xFF5380A4 0x3F003F00	
	ExtPort	read gpio data in input mode		[19,19]		io -4 0xFF530070	

		R	V1103 GP	IO V1.1	.0		
Name	Config	Description	Register	Position	Valu	IO Command	
		LCD_D3	0xFF538014	[0,2]	1	io -4 0xFF538014 0x00070001	
		VICAP_D6_M1	0xFF538014	[0,2]	2	io -4 0xFF538014 0x00070002	
	10	PWM8_M1	0xFF538014	[0,2]	3	io -4 0xFF538014 0x00070003	
	IOmux	UART4_RX_M1	0xFF538014	[0,2]	4	io -4 0xFF538014 0x00070004	
		SDMMC1_D3_M1	0xFF538014	[0,2]	5	io -4 0xFF538014 0x00070005	
		GPIO1_C4	0xFF538014	[0,2]	0	io -4 0xFF538014 0x00070000	
		normal	0xFF5381C8	[8,9]	0	io -4 0xFF5381C8 0x03000000	
	Pull	pullup	0xFF5381C8	[8,9]	1	io -4 0xFF5381C8 0x03000100	
		pulldown	0xFF5381C8	[8,9]	2	io -4 0xFF5381C8 0x03000200	
	I/O M - 4 -	input	0xFF53000C	[4,4]	0	io -4 0xFF53000C 0x00100000	
	I/O Mode	output	0xFF53000C	[4,4]	1	io -4 0xFF53000C 0x00100010	
CDIO1C4	Ontor	low	0xFF530004	[4,4]	0	io -4 0xFF530004 0x00100000	
GPIO1C4	Output	high	0xFF530004	[4,4]	1	io -4 0xFF530004 0x00100010	
	Invest Ctal	disable	0xFF538188	[4,4]	0	io -4 0xFF538188 0x00100000	
	Input Ctrl	enable	0xFF538188	[4,4]	1	io -4 0xFF538188 0x00100010	
	G 1	disable	0xFF538288	[4,4]	0	io -4 0xFF538288 0x00100000	
	Schmitt	enable	0xFF538288	[4,4]	1	io -4 0xFF538288 0x00100010	
		level0	0xFF5380A8	[0,5]	1	io -4 0xFF5380A8 0x003F0001	
		level1	0xFF5380A8	[0,5]	3	io -4 0xFF5380A8 0x003F0003	
	D: 0. 1	level2	0xFF5380A8	[0,5]	7	io -4 0xFF5380A8 0x003F0007	
	Drive Strength ExtPort	level3	0xFF5380A8	[0,5]	15	io -4 0xFF5380A8 0x003F000F	
		level4	0xFF5380A8	[0,5]	31	io -4 0xFF5380A8 0x003F001F	
		level5	0xFF5380A8	[0,5]	63	io -4 0xFF5380A8 0x003F003F	
		read gpio data in input mode	0xFF530070	[20,20]		io -4 0xFF530070	
		LCD D2	0xFF538014	[4,6]	1	io -4 0xFF538014 0x00700010	
	10	VICAP_D7_M1	0xFF538014	[4,6]	2	io -4 0xFF538014 0x00700020	
		PWM9_M1	0xFF538014	[4,6]	3	io -4 0xFF538014 0x00700030	
	IOmux	UART4_TX_M1	0xFF538014	[4,6]	4	io -4 0xFF538014 0x00700040	
		SDMMC1 D2 M1	0xFF538014	[4,6]	5	io -4 0xFF538014 0x00700050	
		GPIO1 C5	0xFF538014	[4,6]	0	io -4 0xFF538014 0x00700000	
		normal	0xFF5381C8		0	io -4 0xFF5381C8 0x0C000000	
	Pull	pullup	0xFF5381C8		1	io -4 0xFF5381C8 0x0C000400	
		2 2	0xFF5381C8		2	io -4 0xFF5381C8 0x0C000800	
	7/0.7.5.4	input	0xFF53000C	[5,5]	0	io -4 0xFF53000C 0x00200000	
	I/O Mode	output	0xFF53000C	[5,5]	1	io -4 0xFF53000C 0x00200020	
CDY O 1 CF	0	low	0xFF530004	[5,5]	0	io -4 0xFF530004 0x00200000	
GPIO1C5	Output	high	0xFF530004	[5,5]	1	io -4 0xFF530004 0x00200020	
	T G. 1	disable	0xFF538188	[5,5]	0	io -4 0xFF538188 0x00200000	
	Input Ctrl	enable	0xFF538188	[5,5]	1	io -4 0xFF538188 0x00200020	
	~ .	disable	0xFF538288	[5,5]	0	io -4 0xFF538288 0x00200000	
	Schmitt	enable	0xFF538288	[5,5]	1	io -4 0xFF538288 0x00200020	
		level0	0xFF5380A8	[8,13]	1	io -4 0xFF5380A8 0x3F000100	
		level1	0xFF5380A8		3	io -4 0xFF5380A8 0x3F000300	
		level2	0xFF5380A8	[8,13]	7	io -4 0xFF5380A8 0x3F000700	
	Drive Strength	level3	0xFF5380A8	[8,13]	15	io -4 0xFF5380A8 0x3F000F00	
		level4	0xFF5380A8		31	io -4 0xFF5380A8 0x3F001F00	
		level5	0xFF5380A8		63	io -4 0xFF5380A8 0x3F003F00	
	ExtPort	read gpio data in input mode		[21,21]	0.5	io -4 0xFF530070	
	LAH OIT	Tead Spio data in input mode	UNI 1 330070	[21,21]		10 1 0/11 2 2 0 0 / 0	

		R	V1103 GP	IO V1.1	.0		
Name	Config	Description	Register	Position	Valu	IO Command	
		LCD D1	0xFF538014	[8,10]	1	io -4 0xFF538014 0x07000100	
		VICAP D8 M1	0xFF538014	[8,10]	2	io -4 0xFF538014 0x07000200	
	IOmux	PWM10 M1	0xFF538014	[8,10]	3	io -4 0xFF538014 0x07000300	
		UART4 RTSN M1	0xFF538014	[8,10]	4	io -4 0xFF538014 0x07000400	
		GPIO1 C6	0xFF538014	[8,10]	0	io -4 0xFF538014 0x07000000	
		normal	0xFF5381C8	[12,13]	0	io -4 0xFF5381C8 0x30000000	
	Pull	pullup	0xFF5381C8	[12,13]	1	io -4 0xFF5381C8 0x30001000	
		pulldown	0xFF5381C8	[12,13]	2	io -4 0xFF5381C8 0x30002000	
	1/0.1/1	input	0xFF53000C	[6,6]	0	io -4 0xFF53000C 0x00400000	
	I/O Mode	output	0xFF53000C	[6,6]	1	io -4 0xFF53000C 0x00400040	
		low	0xFF530004	[6,6]	0	io -4 0xFF530004 0x00400000	
GPIO1C6	Output	high	0xFF530004	[6,6]	1	io -4 0xFF530004 0x00400040	
		disable	0xFF538188	[6,6]	0	io -4 0xFF538188 0x00400000	
	Input Ctrl	enable	0xFF538188	[6,6]	1	io -4 0xFF538188 0x00400040	
	~	disable	0xFF538288	[6,6]	0	io -4 0xFF538288 0x00400000	
	Schmitt	enable	0xFF538288	[6,6]	1	io -4 0xFF538288 0x00400040	
		level0	0xFF5380AC	[0,5]	1	io -4 0xFF5380AC 0x003F0001	
		level1	0xFF5380AC	[0,5]	3	io -4 0xFF5380AC 0x003F0003	
		level2	0xFF5380AC	[0,5]	7	io -4 0xFF5380AC 0x003F0007	
	Drive Strength	level3	0xFF5380AC	[0,5]	15	io -4 0xFF5380AC 0x003F000F	
		level4	0xFF5380AC	[0,5]	31	io -4 0xFF5380AC 0x003F001F	
		level5	0xFF5380AC	[0,5]	63	io -4 0xFF5380AC 0x003F003F	
	ExtPort	read gpio data in input mode		[22,22]		io -4 0xFF530070	
		LCD D0	0xFF538014	[12,14]	1	io -4 0xFF538014 0x70001000	
	IOmux	VICAP D9 M1	0xFF538014	[12,14]	2	io -4 0xFF538014 0x70002000	
		PWM11 IR M1	0xFF538014	[12,14]	3	io -4 0xFF538014 0x70003000	
		UART4 CTSN M1	0xFF538014	[12,14]	4	io -4 0xFF538014 0x70004000	
		GPIO1 C7	0xFF538014	[12,14]	0	io -4 0xFF538014 0x70000000	
		normal	0xFF5381C8		0	io -4 0xFF5381C8 0xC0000000	
	Pull	pullup	0xFF5381C8		1	io -4 0xFF5381C8 0xC0004000	
		pulldown	0xFF5381C8		2	io -4 0xFF5381C8 0xC0008000	
	7/0.37	input	0xFF53000C	[7,7]	0	io -4 0xFF53000C 0x00800000	
	I/O Mode	output	0xFF53000C	[7,7]	1	io -4 0xFF53000C 0x00800080	
		low	0xFF530004	[7,7]	0	io -4 0xFF530004 0x00800000	
GPIO1C7	Output	high	0xFF530004	[7,7]	1	io -4 0xFF530004 0x00800080	
	T G . 1	disable	0xFF538188	[7,7]	0	io -4 0xFF538188 0x00800000	
	Input Ctrl	enable	0xFF538188	[7,7]	1	io -4 0xFF538188 0x00800080	
	G 1	disable	0xFF538288	[7,7]	0	io -4 0xFF538288 0x00800000	
	Schmitt	enable	0xFF538288	[7,7]	1	io -4 0xFF538288 0x00800080	
		level0	0xFF5380AC	[8,13]	1	io -4 0xFF5380AC 0x3F000100	
		level1	0xFF5380AC	[8,13]	3	io -4 0xFF5380AC 0x3F000300	
	Dia G	level2	0xFF5380AC	[8,13]	7	io -4 0xFF5380AC 0x3F000700	
	Drive Strength	level3	0xFF5380AC	[8,13]	15	io -4 0xFF5380AC 0x3F000F00	
		level4	0xFF5380AC	[8,13]	31	io -4 0xFF5380AC 0x3F001F00	
		level5	0xFF5380AC	[8,13]	63	io -4 0xFF5380AC 0x3F003F00	
1	ExtPort	read gpio data in input mode		[23,23]		io -4 0xFF530070	

		R	V1103 GP	IO V1.1	.0	
Name	Config	Description	Register	Position	Valu	IO Command
		LCD DEN	0xFF538018	[0,2]	1	io -4 0xFF538018 0x00070001
		VICAP_CLKIN_M1	0xFF538018	[0,2]	2	io -4 0xFF538018 0x00070002
	10	PWM3_IR_M2	0xFF538018	[0,2]	3	io -4 0xFF538018 0x00070003
	IOmux	UART5_RTSN_M1	0xFF538018	[0,2]	4	io -4 0xFF538018 0x00070004
		UART3_TX_M1	0xFF538018	[0,2]	5	io -4 0xFF538018 0x00070005
		GPIO1_D0	0xFF538018	[0,2]	0	io -4 0xFF538018 0x00070000
		normal	0xFF5381CC	[0,1]	0	io -4 0xFF5381CC 0x00030000
	Pull	pullup	0xFF5381CC	[0,1]	1	io -4 0xFF5381CC 0x00030001
		pulldown	0xFF5381CC	[0,1]	2	io -4 0xFF5381CC 0x00030002
	I/O M 1	input	0xFF53000C	[8,8]	0	io -4 0xFF53000C 0x01000000
	I/O Mode	output	0xFF53000C	[8,8]	1	io -4 0xFF53000C 0x01000100
CDIO1D0	0.4.4	low	0xFF530004	[8,8]	0	io -4 0xFF530004 0x01000000
GPIO1D0	Output	high	0xFF530004	[8,8]	1	io -4 0xFF530004 0x01000100
	Invest Ctal	disable	0xFF53818C	[0,0]	0	io -4 0xFF53818C 0x00010000
	Input Ctrl	enable	0xFF53818C	[0,0]	1	io -4 0xFF53818C 0x00010001
	C -1	disable	0xFF53828C	[0,0]	0	io -4 0xFF53828C 0x00010000
	Schmitt	enable	0xFF53828C	[0,0]	1	io -4 0xFF53828C 0x00010001
		level0	0xFF5380B0	[0,5]	1	io -4 0xFF5380B0 0x003F0001
		level1	0xFF5380B0	[0,5]	3	io -4 0xFF5380B0 0x003F0003
	D.:	level2	0xFF5380B0	[0,5]	7	io -4 0xFF5380B0 0x003F0007
	Drive Strength	level3	0xFF5380B0	[0,5]	15	io -4 0xFF5380B0 0x003F000F
		level4	0xFF5380B0	[0,5]	31	io -4 0xFF5380B0 0x003F001F
		level5	0xFF5380B0	[0,5]	63	io -4 0xFF5380B0 0x003F003F
	ExtPort	read gpio data in input mode	0xFF530070	[24,24]		io -4 0xFF530070
		LCD_HSYNC	0xFF538018	[4,6]	1	io -4 0xFF538018 0x00700010
	IOmux	VICAP_HSYNC_M1	0xFF538018	[4,6]	2	io -4 0xFF538018 0x00700020
		PWM10_M2	0xFF538018	[4,6]	3	io -4 0xFF538018 0x00700030
		UART5_CTSN_M1	0xFF538018	[4,6]	4	io -4 0xFF538018 0x00700040
		UART3_RX_M1	0xFF538018	[4,6]	5	io -4 0xFF538018 0x00700050
		GPIO1_D1	0xFF538018	[4,6]	0	io -4 0xFF538018 0x00700000
		normal	0xFF5381CC	[2,3]	0	io -4 0xFF5381CC 0x000C0000
	Pull	pullup	0xFF5381CC	[2,3]	1	io -4 0xFF5381CC 0x000C0004
		pulldown	0xFF5381CC	[2,3]	2	io -4 0xFF5381CC 0x000C0008
	I/O Mode	input	0xFF53000C	[9,9]	0	io -4 0xFF53000C 0x02000000
	1/O Wiode	output	0xFF53000C	[9,9]	1	io -4 0xFF53000C 0x02000200
GPIO1D1	Output	low	0xFF530004	[9,9]	0	io -4 0xFF530004 0x02000000
GLIOIDI	Output	high	0xFF530004	[9,9]	1	io -4 0xFF530004 0x02000200
	Input Ctrl	disable	0xFF53818C	[1,1]	0	io -4 0xFF53818C 0x00020000
	Input Cur	enable	0xFF53818C	[1,1]	1	io -4 0xFF53818C 0x00020002
	Schmitt	disable	0xFF53828C	[1,1]	0	io -4 0xFF53828C 0x00020000
	Schilltt	enable	0xFF53828C	[1,1]	1	io -4 0xFF53828C 0x00020002
		level0	0xFF5380B0	[8,13]	1	io -4 0xFF5380B0 0x3F000100
		level1	0xFF5380B0	[8,13]	3	io -4 0xFF5380B0 0x3F000300
	Drive Strength	level2	0xFF5380B0	[8,13]	7	io -4 0xFF5380B0 0x3F000700
	Drive Suengin	level3	0xFF5380B0	[8,13]	15	io -4 0xFF5380B0 0x3F000F00
		level4	0xFF5380B0	[8,13]	31	io -4 0xFF5380B0 0x3F001F00
		level5	0xFF5380B0	[8,13]	63	io -4 0xFF5380B0 0x3F003F00
	ExtPort	read gpio data in input mode	0xFF530070	[25,25]		io -4 0xFF530070

		R	V1103 GP	IO V1.1	.0	
Name	Config	Description	Register	Position	Valu	IO Command
		LCD_VSYNC	0xFF538018	[8,10]	1	io -4 0xFF538018 0x07000100
		VICAP_VSYNC_M1	0xFF538018	[8,10]	2	io -4 0xFF538018 0x07000200
		I2C3_SDA_M1	0xFF538018	[8,10]	3	io -4 0xFF538018 0x07000300
	10	UART5_RX_M1	0xFF538018	[8,10]	4	io -4 0xFF538018 0x07000400
	IOmux	SPI0 CS1N M0	0xFF538018	[8,10]	5	io -4 0xFF538018 0x07000500
		PWM0 M1	0xFF538018	[8,10]	6	io -4 0xFF538018 0x07000600
		DSMAUDIO_P	0xFF538018	[8,10]	7	io -4 0xFF538018 0x07000700
		GPIO1_D2	0xFF538018	[8,10]	0	io -4 0xFF538018 0x07000000
		normal	0xFF5381CC	[4,5]	0	io -4 0xFF5381CC 0x00300000
	Pull	pullup	0xFF5381CC	[4,5]	1	io -4 0xFF5381CC 0x00300010
		pulldown	0xFF5381CC		2	io -4 0xFF5381CC 0x00300020
	7/0.7.5.4	input	0xFF53000C		0	io -4 0xFF53000C 0x04000000
	I/O Mode	output	0xFF53000C		1	io -4 0xFF53000C 0x04000400
GPIO1D2	_	low	0xFF530004		0	io -4 0xFF530004 0x04000000
	Output	high	0xFF530004	[10,10]	1	io -4 0xFF530004 0x04000400
		disable	0xFF53818C	[2,2]	0	io -4 0xFF53818C 0x00040000
	Input Ctrl	enable	0xFF53818C	[2,2]	1	io -4 0xFF53818C 0x00040004
		disable	0xFF53828C	[2,2]	0	io -4 0xFF53828C 0x00040000
	Schmitt	enable	0xFF53828C	[2,2]	1	io -4 0xFF53828C 0x00040004
		level()	0xFF5380B4	[0,5]	1	io -4 0xFF5380B4 0x003F0001
		level1	0xFF5380B4	[0,5]	3	io -4 0xFF5380B4 0x003F0003
		level2	0xFF5380B4	[0,5]	7	io -4 0xFF5380B4 0x003F0007
	Drive Strength	level3	0xFF5380B4	[0,5]	15	io -4 0xFF5380B4 0x003F000F
		level4	0xFF5380B4	[0,5]	31	io -4 0xFF5380B4 0x003F001F
		level5	0xFF5380B4	[0,5]	63	io -4 0xFF5380B4 0x003F003F
	ExtPort	read gpio data in input mode		[26,26]	0.5	io -4 0xFF530070
	Extr oft	LCD CLK	0xFF538018	[12,14]	1	io -4 0xFF538018 0x70001000
	IOmux	VICAP CLKOUT M1	0xFF538018	[12,14]	2	io -4 0xFF538018 0x70002000
		I2C3 SCL M1	0xFF538018	[12,14]	3	io -4 0xFF538018 0x70003000
		UART5 TX M1	0xFF538018	[12,14]	4	io -4 0xFF538018 0x70004000
	Tomax	PWM11 IR M2	0xFF538018		5	io -4 0xFF538018 0x70005000
		DSMAUDIO N	0xFF538018		7	io -4 0xFF538018 0x70007000
		GPIO1 D3	0xFF538018		0	io -4 0xFF538018 0x70000000
		normal	0xFF5381CC		0	io -4 0xFF5381CC 0x00C00000
	Pull	pullup	0xFF5381CC	[6,7]	1	io -4 0xFF5381CC 0x00C00040
	1 411	pulldown	0xFF5381CC		2	io -4 0xFF5381CC 0x00C00080
		input	0xFF53000C		0	io -4 0xFF53000C 0x08000000
	I/O Mode	output	0xFF53000C		1	io -4 0xFF53000C 0x08000800
GPIO1D3		low	0xFF530004	[11,11]	0	io -4 0xFF530004 0x08000000
GIIGIDS	Output	high	0xFF530004	[11,11]	1	io -4 0xFF530004 0x08000800
		disable	0xFF53818C	[3,3]	0	io -4 0xFF53818C 0x00080000
	Input Ctrl	enable	0xFF53818C	[3,3]	1	io -4 0xFF53818C 0x00080008
		disable	0xFF53828C	[3,3]	0	io -4 0xFF53828C 0x00080000
	Schmitt	enable	0xFF53828C	[3,3]	1	io -4 0xFF53828C 0x00080008
		level0	0xFF5380B4	[8,13]	1	io -4 0xFF5380B4 0x3F000100
		level1	0xFF5380B4	[8,13]	3	io -4 0xFF5380B4 0x3F000300
		level2	0xFF5380B4	[8,13]	7	io -4 0xFF5380B4 0x3F000700
	Drive Strength	level3	0xFF5380B4	[8,13]	15	io -4 0xFF5380B4 0x3F000F00
		level4	0xFF5380B4	[8,13]	31	io -4 0xFF5380B4 0x3F000F00
		level5	0xFF5380B4	[8,13]	63	io -4 0xFF5380B4 0x3F001F00
	ExtPort	read gpio data in input mode		[27,27]	03	io -4 0xFF530070
<u> </u>	L'ALF OF L	read gpro data in input mode	071.1.3300.10	[4/,4/]		10 -4 0A1TJJ00/0

		R	V1103 GP	IO V1.1	.0		
Name	Config	Description	Register	Position	Valu	IO Command	
	10	SDMMC0_DET	0xFF558040	[4,6]	1	io -4 0xFF558040 0x00700010	
	IOmux	GPIO3_A1	0xFF558040	[4,6]	0	io -4 0xFF558040 0x00700000	
		normal	0xFF5581E0	[2,3]	0	io -4 0xFF5581E0 0x000C0000	
	Pull	pullup	0xFF5581E0	[2,3]	1	io -4 0xFF5581E0 0x000C0004	
		pulldown	0xFF5581E0	[2,3]	2	io -4 0xFF5581E0 0x000C0008	
	I/O Mode	input	0xFF550008	[1,1]	0	io -4 0xFF550008 0x00020000	
	I/O Mode	output	0xFF550008	[1,1]	1	io -4 0xFF550008 0x00020002	
	Ontout	low	0xFF550000	[1,1]	0	io -4 0xFF550000 0x00020000	
	Output	high	0xFF550000	[1,1]	1	io -4 0xFF550000 0x00020002	
CDIO2 A 1	Invest Ctal	disable	0xFF5581A0	[1,1]	0	io -4 0xFF5581A0 0x00020000	
GPIO3A1	Input Ctrl	enable	0xFF5581A0	[1,1]	1	io -4 0xFF5581A0 0x00020002	
	C -1	disable	0xFF5582A0	[1,1]	0	io -4 0xFF5582A0 0x00020000	
	Schmitt	enable	0xFF5582A0	[1,1]	1	io -4 0xFF5582A0 0x00020002	
		level0	0xFF558100	[8,13]	1	io -4 0xFF558100 0x3F000100	
		level1	0xFF558100	[8,13]	3	io -4 0xFF558100 0x3F000300	
	D.:	level2	0xFF558100	[8,13]	7	io -4 0xFF558100 0x3F000700	
	Drive Strength	level3	0xFF558100	[8,13]	15	io -4 0xFF558100 0x3F000F00	
		level4	0xFF558100	[8,13]	31	io -4 0xFF558100 0x3F001F00	
		level5	0xFF558100	[8,13]	63	io -4 0xFF558100 0x3F003F00	
	ExtPort	read gpio data in input mode	0xFF550070	[1,1]		io -4 0xFF550070	
		SDMMC0_D1	0xFF558040	[8,10]	1	io -4 0xFF558040 0x07000100	
		UART2_TX_M0	0xFF558040	[8,10]	2	io -4 0xFF558040 0x07000200	
	IOmux	TEST_CLK0_OUT	0xFF558040	[8,10]	3	io -4 0xFF558040 0x07000300	
		PWM9_M0	0xFF558040	[8,10]	4	io -4 0xFF558040 0x07000400	
		GPIO3_A2	0xFF558040	[8,10]	0	io -4 0xFF558040 0x07000000	
		normal	0xFF5581E0	[4,5]	0	io -4 0xFF5581E0 0x00300000	
	Pull	pullup	0xFF5581E0	[4,5]	1	io -4 0xFF5581E0 0x00300010	
		pulldown	0xFF5581E0	[4,5]	2	io -4 0xFF5581E0 0x00300020	
	I/O M - 4 -	input	0xFF550008	[2,2]	0	io -4 0xFF550008 0x00040000	
	I/O Mode	output	0xFF550008	[2,2]	1	io -4 0xFF550008 0x00040004	
	Ontout	low	0xFF550000	[2,2]	0	io -4 0xFF550000 0x00040000	
GPIO3A2	Output	high	0xFF550000	[2,2]	1	io -4 0xFF550000 0x00040004	
	Innut Ctul	disable	0xFF5581A0	[2,2]	0	io -4 0xFF5581A0 0x00040000	
	Input Ctrl	enable	0xFF5581A0	[2,2]	1	io -4 0xFF5581A0 0x00040004	
	Schmitt	disable	0xFF5582A0	[2,2]	0	io -4 0xFF5582A0 0x00040000	
	Schmitt	enable	0xFF5582A0	[2,2]	1	io -4 0xFF5582A0 0x00040004	
		level0	0xFF558104	[0,5]	1	io -4 0xFF558104 0x003F0001	
		level1	0xFF558104	[0,5]	3	io -4 0xFF558104 0x003F0003	
	Duive Cture 41	level2	0xFF558104	[0,5]	7	io -4 0xFF558104 0x003F0007	
	Drive Strength	level3	0xFF558104	[0,5]	15	io -4 0xFF558104 0x003F000F	
		level4	0xFF558104	[0,5]	31	io -4 0xFF558104 0x003F001F	
		level5	0xFF558104	[0,5]	63	io -4 0xFF558104 0x003F003F	
	ExtPort	read gpio data in input mode	0xFF550070	[2,2]		io -4 0xFF550070	

		R	V1103 GP	IO V1.1	.0	
Name	Config	Description	Register	Position	Valu	IO Command
	6	SDMMC0 D0	0xFF558040	[12,14]	1	io -4 0xFF558040 0x70001000
		UART2 RX M0	0xFF558040	[12,14]	2	io -4 0xFF558040 0x70002000
	IOmux	TEST CLK1 OUT	0xFF558040	[12,14]	3	io -4 0xFF558040 0x70003000
		PWM8 M0	0xFF558040	[12,14]	4	io -4 0xFF558040 0x70004000
		GPIO3 A3	0xFF558040		0	io -4 0xFF558040 0x70000000
		normal	0xFF5581E0	[6,7]	0	io -4 0xFF5581E0 0x00C00000
	Pull	pullup	0xFF5581E0	[6,7]	1	io -4 0xFF5581E0 0x00C00040
		pulldown	0xFF5581E0	[6,7]	2	io -4 0xFF5581E0 0x00C00080
	7/0.7.5.4	input	0xFF550008	[3,3]	0	io -4 0xFF550008 0x00080000
	I/O Mode	output	0xFF550008	[3,3]	1	io -4 0xFF550008 0x00080008
		low	0xFF550000	[3,3]	0	io -4 0xFF550000 0x00080000
GPIO3A3	Output	high	0xFF550000	[3,3]	1	io -4 0xFF550000 0x00080008
		disable	0xFF5581A0	[3,3]	0	io -4 0xFF5581A0 0x00080000
	Input Ctrl	enable	0xFF5581A0	[3,3]	1	io -4 0xFF5581A0 0x00080008
	a 1 ·	disable	0xFF5582A0		0	io -4 0xFF5582A0 0x00080000
	Schmitt	enable	0xFF5582A0	[3,3]	1	io -4 0xFF5582A0 0x00080008
		level0	0xFF558104	[8,13]	1	io -4 0xFF558104 0x3F000100
		level1	0xFF558104	[8,13]	3	io -4 0xFF558104 0x3F000300
		level?	0xFF558104	[8,13]	7	io -4 0xFF558104 0x3F000700
	Drive Strength	level3	0xFF558104	[8,13]	15	io -4 0xFF558104 0x3F000F00
		level4	0xFF558104	[8,13]	31	io -4 0xFF558104 0x3F001F00
		level5	0xFF558104	[8,13]	63	io -4 0xFF558104 0x3F003F00
	ExtPort	read gpio data in input mode		[3,3]		io -4 0xFF550070
		SDMMC0 CLK	0xFF558044	[0,2]	1	io -4 0xFF558044 0x00070001
	IOmux	UART5 RTSN M0	0xFF558044	[0,2]	2	io -4 0xFF558044 0x00070002
		I2C0 SCL M2	0xFF558044	[0,2]	3	io -4 0xFF558044 0x00070003
		LPMCU JTAG TCK M1	0xFF558044	[0,2]	4	io -4 0xFF558044 0x00070004
		PWM10 M0	0xFF558044	[0,2]	5	io -4 0xFF558044 0x00070005
		GPIO3 A4	0xFF558044	[0,2]	0	io -4 0xFF558044 0x00070000
		normal	0xFF5581E0	[8,9]	0	io -4 0xFF5581E0 0x03000000
	Pull	pullup	0xFF5581E0	[8,9]	1	io -4 0xFF5581E0 0x03000100
		pulldown	0xFF5581E0		2	io -4 0xFF5581E0 0x03000200
	7/0.34	input	0xFF550008	[4,4]	0	io -4 0xFF550008 0x00100000
	I/O Mode	output	0xFF550008	[4,4]	1	io -4 0xFF550008 0x00100010
CDIO 2 . 4	0	low	0xFF550000	[4,4]	0	io -4 0xFF550000 0x00100000
GPIO3A4	Output	high	0xFF550000	[4,4]	1	io -4 0xFF550000 0x00100010
		disable	0xFF5581A0	[4,4]	0	io -4 0xFF5581A0 0x00100000
	Input Ctrl	enable	0xFF5581A0	[4,4]	1	io -4 0xFF5581A0 0x00100010
	a 1	disable	0xFF5582A0	[4,4]	0	io -4 0xFF5582A0 0x00100000
	Schmitt	enable	0xFF5582A0	[4,4]	1	io -4 0xFF5582A0 0x00100010
		level0	0xFF558108	[0,5]	1	io -4 0xFF558108 0x003F0001
		level1	0xFF558108	[0,5]	3	io -4 0xFF558108 0x003F0003
		level2	0xFF558108	[0,5]	7	io -4 0xFF558108 0x003F0007
	Drive Strength	level3	0xFF558108	[0,5]	15	io -4 0xFF558108 0x003F000F
		level4	0xFF558108	[0,5]	31	io -4 0xFF558108 0x003F001F
		level5	0xFF558108	[0,5]	63	io -4 0xFF558108 0x003F003F
	ExtPort	read gpio data in input mode		[4,4]		io -4 0xFF550070

		R	V1103 GP	IO V1.1	.0		
Name	Config	Description	Register	Position	Valu	IO Command	
		SDMMC0 CMD	0xFF558044	[4,6]	1	io -4 0xFF558044 0x00700010	
		UART5 CTSN M0	0xFF558044	[4,6]	2	io -4 0xFF558044 0x00700020	
	10	I2C0 SDA M2	0xFF558044	[4,6]	3	io -4 0xFF558044 0x00700030	
	IOmux	LPMCU JTAG TMS M1	0xFF558044	[4,6]	4	io -4 0xFF558044 0x00700040	
		PWM11 IR M0	0xFF558044	[4,6]	5	io -4 0xFF558044 0x00700050	
		GPIO3 A5	0xFF558044	[4,6]	0	io -4 0xFF558044 0x00700000	
		normal	0xFF5581E0	[10,11]	0	io -4 0xFF5581E0 0x0C000000	
	Pull	pullup	0xFF5581E0	[10,11]	1	io -4 0xFF5581E0 0x0C000400	
		pulldown	0xFF5581E0	[10,11]	2	io -4 0xFF5581E0 0x0C000800	
	1/0 1/1	input	0xFF550008	[5,5]	0	io -4 0xFF550008 0x00200000	
	I/O Mode	output	0xFF550008	[5,5]	1	io -4 0xFF550008 0x00200020	
CDIO2 4.5	0 1 1	low	0xFF550000	[5,5]	0	io -4 0xFF550000 0x00200000	
GPIO3A5	Output	high	0xFF550000	[5,5]	1	io -4 0xFF550000 0x00200020	
	Invest Ctal	disable	0xFF5581A0	[5,5]	0	io -4 0xFF5581A0 0x00200000	
	Input Ctrl	enable	0xFF5581A0	[5,5]	1	io -4 0xFF5581A0 0x00200020	
	G 1	disable	0xFF5582A0	[5,5]	0	io -4 0xFF5582A0 0x00200000	
	Schmitt	enable	0xFF5582A0	[5,5]	1	io -4 0xFF5582A0 0x00200020	
		level0	0xFF558108	[8,13]	1	io -4 0xFF558108 0x3F000100	
		level1	0xFF558108	[8,13]	3	io -4 0xFF558108 0x3F000300	
	D.:	level2	0xFF558108	[8,13]	7	io -4 0xFF558108 0x3F000700	
	Drive Strength	level3	0xFF558108	[8,13]	15	io -4 0xFF558108 0x3F000F00	
		level4	0xFF558108	[8,13]	31	io -4 0xFF558108 0x3F001F00	
		level5	0xFF558108	[8,13]	63	io -4 0xFF558108 0x3F003F00	
	ExtPort	read gpio data in input mode	0xFF550070	[5,5]		io -4 0xFF550070	
		SDMMC0_D3	0xFF558044	[8,10]	1	io -4 0xFF558044 0x07000100	
	IOmux	UART5_TX_M0	0xFF558044	[8,10]	2	io -4 0xFF558044 0x07000200	
		A7_JTAG_TMS_M0	0xFF558044	[8,10]	3	io -4 0xFF558044 0x07000300	
		HPMCU_JTAG_TMS_M1	0xFF558044	[8,10]	4	io -4 0xFF558044 0x07000400	
		GPIO3_A6	0xFF558044	[8,10]	0	io -4 0xFF558044 0x07000000	
		normal	0xFF5581E0	[12,13]	0	io -4 0xFF5581E0 0x30000000	
	Pull	pullup	0xFF5581E0	[12,13]	1	io -4 0xFF5581E0 0x30001000	
		pulldown	0xFF5581E0	[12,13]	2	io -4 0xFF5581E0 0x30002000	
	I/O Mode	input	0xFF550008	[6,6]	0	io -4 0xFF550008 0x00400000	
	I/O Mode	output	0xFF550008	[6,6]	1	io -4 0xFF550008 0x00400040	
	Output	low	0xFF550000	[6,6]	0	io -4 0xFF550000 0x00400000	
GPIO3A6	Output	high	0xFF550000	[6,6]	1	io -4 0xFF550000 0x00400040	
	Input Ctrl	disable	0xFF5581A0	[6,6]	0	io -4 0xFF5581A0 0x00400000	
	Input Cur	enable	0xFF5581A0	[6,6]	1	io -4 0xFF5581A0 0x00400040	
	Schmitt	disable	0xFF5582A0	[6,6]	0	io -4 0xFF5582A0 0x00400000	
	Schilltt	enable	0xFF5582A0	[6,6]	1	io -4 0xFF5582A0 0x00400040	
		level0	0xFF55810C	[0,5]	1	io -4 0xFF55810C 0x003F0001	
		level1	0xFF55810C	[0,5]	3	io -4 0xFF55810C 0x003F0003	
	Drive Strength	level2	0xFF55810C	[0,5]	7	io -4 0xFF55810C 0x003F0007	
	Direc Suchgui	level3	0xFF55810C	[0,5]	15	io -4 0xFF55810C 0x003F000F	
		level4	0xFF55810C	[0,5]	31	io -4 0xFF55810C 0x003F001F	
		level5	0xFF55810C	[0,5]	63	io -4 0xFF55810C 0x003F003F	
	ExtPort	read gpio data in input mode	0xFF550070	[6,6]		io -4 0xFF550070	

Pull			R	V1103 GP	IO V1.1	.0		
IOmix	Name	Config	Description	Register	Position	Valu	IO Command	
A7_TIAG_TCK_MO			SDMMC0_D2	0xFF558044	[12,14]	1	io -4 0xFF558044 0x70001000	
HPMCU FTAG TCK MI 0xFF558044 12,141 4 10 - 4 0xFF558044 0x70004000			UART5_RX_M0	0xFF558044	[12,14]	2	io -4 0xFF558044 0x70002000	
Pull		IOmux	A7_JTAG_TCK_M0	0xFF558044	[12,14]	3	io -4 0xFF558044 0x70003000	
Pull			HPMCU_JTAG_TCK_M1	0xFF558044	[12,14]	4	io -4 0xFF558044 0x70004000	
Poll			GPIO3_A7	0xFF558044	[12,14]	0	io -4 0xFF558044 0x70000000	
Pull			normal	0xFF5581E0	[14,15]	0	io -4 0xFF5581E0 0xC0000000	
LO Mode		Pull	pullup	0xFF5581E0	[14,15]	1	io -4 0xFF5581E0 0xC0004000	
April			pulldown	0xFF5581E0	[14,15]	2	io -4 0xFF5581E0 0xC0008000	
Output		I/O Mada	input	0xFF550008	[7,7]	0	io -4 0xFF550008 0x00800000	
Part		I/O Mode	output	0xFF550008	[7,7]	1	io -4 0xFF550008 0x00800080	
Part		Outmut	low	0xFF550000	[7,7]	0	io -4 0xFF550000 0x00800000	
Input Ctrl	GPIO3A7	Output	high	0xFF550000	[7,7]	1	io -4 0xFF550000 0x00800080	
Schmitt		Immed Ctul	disable	0xFF5581A0	[7,7]	0	io -4 0xFF5581A0 0x00800000	
Schmitt		input Ctri	enable	0xFF5581A0	[7,7]	1	io -4 0xFF5581A0 0x00800080	
Care		Sahmitt	disable	0xFF5582A0	[7,7]	0	io -4 0xFF5582A0 0x00800000	
Drive Strength		Schillt	enable	0xFF5582A0	[7,7]	1	io -4 0xFF5582A0 0x00800080	
Drive Strength			level0	0xFF55810C	[8,13]	1	io -4 0xFF55810C 0x3F000100	
Drive Strength			level1	0xFF55810C	[8,13]	3	io -4 0xFF55810C 0x3F000300	
Level4		Duizza Stuamath	level2	0xFF55810C	[8,13]	7	io -4 0xFF55810C 0x3F000700	
Revel5		Drive Strength	level3	0xFF55810C	[8,13]	15	io -4 0xFF55810C 0x3F000F00	
ExtPort read gpio data in input mode 0xFF550070 [7,7]			level4	0xFF55810C	[8,13]	31	io -4 0xFF55810C 0x3F001F00	
Facility Company Com			level5	0xFF55810C	[8,13]	63	io -4 0xFF55810C 0x3F003F00	
IOmux		ExtPort	read gpio data in input mode	0xFF550070	[7,7]		io -4 0xFF550070	
Full		IOmux	VICAP_D0_M0	0xFF558048	[0,2]	1	io -4 0xFF558048 0x00070001	
Figure F			MIPI_LVDS_D3N	0xFF558048	[0,2]	2	io -4 0xFF558048 0x00070002	
Pull			GP103_B0	0xFF558048	[0, 2]	0	io -4 0xFF3B4804 0x40000000 io -4 0xFF3E8000 0x0000007D io -4 0xFF3E8080 0x0000005F	input only
L/O Mode		Pull					TO TOMPOLOGIC GROUDOUT	
Input Ctrl	GPIO3B0		input	0xFF550008	[8,8]	0	io -4 0xFF550008 0x01000000	
Schmitt Drive Strength ExtPort read gpio data in input mode 0xFF550070 [8,8] io -4 0xFF550070		Output						
Drive Strength ExtPort read gpio data in input mode 0xFF550070 [8,8] io -4 0xFF550070								
ExtPort read gpio data in input mode 0xFF550070 [8,8] io -4 0xFF550070								
Output Input Ctrl Schmitt Drive Strength Output Input Strength IOmux IOmux VICAP_D1_M0 OxfF558048 [4,6] 1 io -4 0xfF558048 0x00700010 o -4 0xfF558048 0x00700020 o -4 0xfF558048 0x00700000 io -4 0xfF38804 0x00700000 io -4 0xfF38804 0x00700000 io -4 0xfF38808 0x00000005F io -4 0xfF38808 0x00000005F io -4 0xfF38808 0x000000001 o -4 0xfF38808 0x0000000001 o -4 0xfF38808 0x00000000000000000000000000000000								
IOmux		ExtPort	7.					
IOmux								
Oxff558048			MIPI_LVDS_D3P	0xFF558048	[4,6]	2		
I/O Mode		IOmux	GPI03_B1	0xFF558048	[4, 6]	0	io -4 0xFF3B4804 0x40000000 io -4 0xFF3E8000 0x0000007D io -4 0xFF3E8080 0x0000005F	input only
I/O Mode	CDIO2D1	Pull						
Input Ctrl Schmitt Drive Strength	GPIO3B1	I/O Mode	input	0xFF550008	[9,9]	0	io -4 0xFF550008 0x02000000	
Schmitt Drive Strength		Output						
Schmitt Drive Strength		Input Ctrl						
Drive Strength								
		ExtPort		0xFF550070	[9,9]		io -4 0xFF550070	

		R	V1103 GP	IO V1.1	.0		
Name	Config	Description	Register	Position	Valu	IO Command	
	3	VICAP D2 M0	0xFF558048	[8,10]	1	io -4 0xFF558048 0x07000100	
		MIPI LVDS CK1N	0xFF558048	[8,10]	2	io -4 0xFF558048 0x07000200	
				[/]		io -4 0xFF558048 0x07000000	
	IOmux					io -4 0xFF3B4804 0x40000000	
		GPI03_B2	0xFF558048	[8, 10]	0	io -4 0xFF3E8000 0x0000007D	input only
						io -4 0xFF3E8080 0x0000005F	
						io -4 0xFF3E884C 0x00000001	
GPIO3B2	Pull						
G1103B2	I/O Mode	input	0xFF550008	[10,10]	0	io -4 0xFF550008 0x04000000	
	Output						
	Input Ctrl						
	Schmitt						
	Drive Strength						
		read gpio data in input mode	0xFF550070	[10,10]		io -4 0xFF550070	
	Extr of	VICAP D3 M0	0xFF558048	[12,14]	1	io -4 0xFF558048 0x70001000	
		MIPI LVDS CK1P	0xFF558048	[12,14]	2	io -4 0xFF558048 0x70002000	
		IMITE V BO_CITI	0.11 1 2 2 3 3 1 3	[12,11]	_	io -4 0xFF558048 0x70000000	
	IOmux					io -4 0xFF3B4804 0x40000000	
		GPIO3_B3	0xFF558048	[12, 14]	0	io -4 0xFF3E8000 0x0000007D	input only
		_				io -4 0xFF3E8080 0x0000005F	
						io -4 0xFF3E884C 0x00000001	
GPIO3B3	Pull		0. 55550000	F14 143	0	. 40 77550000 0 00000000	
	I/O Mode	input	0xFF550008	[11,11]	0	io -4 0xFF550008 0x08000000	
	Output						
	Input Ctrl						
	Schmitt						
	Drive Strength						
	ExtPort	read gpio data in input mode	0xFF550070	[11,11]		io -4 0xFF550070	
		VICAP_D4_M0	0xFF55804C	[0,2]	1	io -4 0xFF55804C 0x00070001	
		MIPI_LVDS_D2N	0xFF55804C	[0,2]	2	io -4 0xFF55804C 0x00070002	
						io -4 0xFF55804C 0x00070000	
	IOmux					io -4 0xFF3B4804 0x40000000	
		GPI03_B4	0xFF55804C	[0, 2]	0	io -4 0xFF3E8000 0x0000007D	input only
						io -4 0xFF3E8080 0x0000005F	
	D 11					io -4 0xFF3E884C 0x00000001	
GPIO3B4	Pull	*	0-EE550000	F10 103		. 40-FE550000 0 10000000	
	I/O Mode	input	0xFF550008	[12,12]	0	io -4 0xFF550008 0x10000000	
	Output						
	Input Ctrl						
	Schmitt						
	Drive Strength						
		read gpio data in input mode	0vFE550070	[12,12]		io -4 0xFF550070	
	EXIPOR	read gpro data ili iliput inode	0XIT-3300/0	[14,14]	<u> </u>	10 -4 UAI F 3 3 UU / U	

		R	V1103 GP	IO V1.1	.0		
Name	Config	Description	Register	Position	Valu	IO Command	
	3	VICAP D5 M0	0xFF55804C	[4,6]	1	io -4 0xFF55804C 0x00700010	
		MIPI LVDS D2P	0xFF55804C	[4,6]	2	io -4 0xFF55804C 0x00700020	
				E / 3		io -4 0xFF55804C 0x00700000	
	IOmux					io -4 0xFF3B4804 0x40000000	
		GPI03_B5	0xFF55804C	[4, 6]	0	io -4 0xFF3E8000 0x0000007D	input only
						io -4 0xFF3E8080 0x0000005F	
						io -4 0xFF3E884C 0x00000001	
GPIO3B5	Pull						
GHOSES	I/O Mode	input	0xFF550008	[13,13]	0	io -4 0xFF550008 0x20000000	
	Outout						
	Output						
	Input Ctrl						
	Schmitt						
	Drive Strength	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	0 PE550050	F10 103		. 40 DD550050	
	ExtPort	read gpio data in input mode		[13,13]		io -4 0xFF550070	
		VICAP_D6_M0	0xFF55804C	[8,10]	1	io -4 0xFF55804C 0x07000100	
		MIPI_LVDS_D1N	0xFF55804C	[8,10]	2	io -4 0xFF55804C 0x07000200	
	IOmuv					io -4 0xFF55804C 0x07000000	
	IOmux	CDIO2 DC	0	[0 10]	0	io -4 0xFF3B4804 0x40000000	:
		GPI03_B6	0xFF55804C	[8, 10]	0	io -4 0xFF3E8000 0x0000007D io -4 0xFF3E8080 0x0000005F	input only
						io -4 0xFF3E844C 0x00000001	
	Pull					10 4 0311 320440 0300000001	
GPIO3B6	I/O Mode	input	0xFF550008	[14,14]	0	io -4 0xFF550008 0x40000000	
	I/O Wiode						
	Output						
	Input Ctrl						
	Schmitt						
	Drive Strength						
		read gpio data in input mode	0xFF550070	[14,14]		io -4 0xFF550070	
		VICAP D7 M0	0xFF55804C	[12,14]	1	io -4 0xFF55804C 0x70001000	
		MIPI LVDS D1P	0xFF55804C		2	io -4 0xFF55804C 0x70002000	
				. /]		io -4 0xFF55804C 0x70000000	
	IOmux					io -4 0xFF3B4804 0x40000000	
		GPI03_B7	0xFF55804C	[12, 14]	0	io -4 0xFF3E8000 0x0000007D	input only
						io -4 0xFF3E8080 0x0000005F	
						io -4 0xFF3E844C 0x00000001	
GPIO3B7	Pull				_		
GI IO3B/	I/O Mode	input	0xFF550008	[15,15]	0	io -4 0xFF550008 0x80000000	
	Outout						
	Output						
	Input Ctrl						
	Schmitt						
	Drive Strength						
	ExtPort	read gpio data in input mode	0xFF550070	[15,15]		io -4 0xFF550070	

Name			R	V1103 GP	IO V1.1	.0		
Formation	Name	Config	Description	Register	Position	Valu	IO Command	
FORMAX		3						
Formax						2	io -4 0xFF558050 0x00070002	
GPI03_C0					E / 3		io -4 0xFF558050 0x00070000	
GPI03CO		IOmux						
Pull			GPIO3_CO	0xFF558050	[0, 2]	0	io -4 0xFF3E8000 0x0000007D	input only
Pull								
Formation							io -4 0xFF3E844C 0x00000001	
I/O Mode	GPIO3C0	Pull						
Input Ctrl Schmitt Drive Strength ExtPort read gpio data in input mode 0xFF550070 [16,16] io -4 0xFF550070	0110000	I/O Mode	input	0xFF55000C	[0,0]	0	io -4 0xFF55000C 0x00010000	
Schmitt Drive Strength ExtPort read gpio data in input mode 0xFF550070 [16,16]		Output						
Schmitt Drive Strength ExtPort read gpio data in input mode 0xFF550070 [16,16]		Input Ctrl						
Drive Strength ExtPort read gpio data in input mode 0xFF558050 [16,16] io -4 0xFF558050 0x00700010								
ExtPort								
Full			read gpio data in input mode	0xFF550070	[16,16]		io -4 0xFF550070	
IOmux						1		
IOmux			MIPI LVDS CK0P	0xFF558050		2	io -4 0xFF558050 0x00700020	
GPI03_C1							io -4 0xFF558050 0x00700000	
Pull		IOmux					io -4 0xFF3B4804 0x40000000	
Pull			GPIO3_C1	0xFF558050	[4, 6]	0		input only
Pull								
I/O Mode							io -4 0xFF3E844C 0x00000001	
Output Input Ctrl Schmitt Drive Strength ExtPort read gpio data in input mode 0xFF550070 [17,17] io -4 0xFF558050 0x07000100 MIPI_LVDS_DON 0xFF558050 [8,10] 1 io -4 0xFF558050 0x07000100 MIPI_LVDS_DON 0xFF558050 [8,10] 2 io -4 0xFF558050 0x07000200 io -4 0xFF358050 0x07000200 io -4 0xFF358050 0x070000000 io -4 0xFF384804 0x400000000 io -4 0xFF388080 0x0000007D io -4 0xFF388080 0x00000007D io -4 0xFF388080 0x0000000000000000000000000000000	GPIO3C1	Pull	:	0EE55000C	F1 17	0	:- 4.0EE55000C 000020000	
Input Ctrl Schmitt Drive Strength ExtPort read gpio data in input mode 0xFF550070 [17,17] io -4 0xFF550070		I/O Mode	input	0XFF33000C	[1,1]	U	10 -4 0XFF33000C 0X00020000	
Input Ctrl Schmitt Drive Strength ExtPort read gpio data in input mode 0xFF550070 [17,17] io -4 0xFF550070		_						
Schmitt Drive Strength Drive Strength ExtPort read gpio data in input mode 0xFF550070 [17,17] io -4 0xFF550070 read gpio data in input mode 0xFF550070 [17,17] io -4 0xFF558050 0x07000100 read 0xFF558050 [8,10] 1 io -4 0xFF558050 0x07000100 read 0xFF558050 [8,10] 2 io -4 0xFF558050 0x07000200 read 0xFF558050 0x07000200 read 0xFF558050 0x07000000 read 0xFF558050 0x070000000 read 0xFF558050 0x070000000 read 0xFF558050 0x070000000 read 0xFF558050 0x070000000 read 0xFF558080 0x0000005F read 0xFF558080 0x0000005F read 0xFF558080 0x00000005F read 0xFF558080 0x000000000 read 0xFF558080 0x0000000000 read 0xFF558080 0x000000000 read 0xFF558080 0x0000000000000000000000000000000		Output						
Drive Strength		Input Ctrl						
ExtPort read gpio data in input mode 0xFF550070 [17,17] io -4 0xFF550070		Schmitt						
Full								
IOmux		ExtPort		0xFF550070	[17,17]		io -4 0xFF550070	
IOmux			VICAP_CLKIN_M0		[8,10]	1	io -4 0xFF558050 0x07000100	
Full			MIPI_LVDS_D0N	0xFF558050	[8,10]	2		
GPI03_C2		**						
GPIO3C2 Pull I/O Mode Input Ctrl Schmitt Drive Strength io -4 0xFF3E8080 0x00000005F io -4 0xFF3E844C 0x00000001 0xFF55000C [2,2] 0 io -4 0xFF55000C 0x00040000		IOmux			F 7			
GPIO3C2 Pull			GPI03_C2	0xFF558050	[8, 10]	0		input only
Pull								
I/O Mode input 0xFF55000C [2,2] 0 io -4 0xFF55000C 0x00040000		Du11					10 -4 UXFF3E844C UXUUUUUUU1	
Output Input Ctrl Schmitt Drive Strength	GPIO3C2		input	0vFF55000C	[2 2]	0	io -4 0xFE55000C 0x00040000	
Input Ctrl Schmitt Drive Strength		I/O Mode	mput	0411330000	[2,2]	U	10 -4 0A11 33000C 0A00040000	
Schmitt Drive Strength		Output						
Schmitt Drive Strength		Input Ctrl						
Drive Strength		•						
T 15AG 90 HUAU 900 WAIA HERBURHOUGI WATT 2 2007 (UT 11A 1ATT T 1 1 H 1 € UXTT 2 2007 (UT 1 1 H 1 1 H 1 € UXTT 2		-	read gnio data in innut mode	0xFF550070	[18,18]		io -4 0xFF550070	

		R	V1103 GP	IO V1.1	.0		
Name	Config	Description	Register	Position	Valu	IO Command	
		VICAP_HSYNC_M0	0xFF558050	[12,14]	1	io -4 0xFF558050 0x70001000	
		MIPI LVDS D0P	0xFF558050	[12,14]	2	io -4 0xFF558050 0x70002000	
	IOmux	GPI03_C3	0xFF558050	[12, 14]	0	io -4 0xFF558050 0x70000000 io -4 0xFF3B4804 0x40000000 io -4 0xFF3E8000 0x0000007D io -4 0xFF3E8080 0x0000005F io -4 0xFF3E844C 0x00000001	input only
CDIO2C2	Pull						
GPIO3C3	I/O Mode	input	0xFF55000C	[3,3]	0	io -4 0xFF55000C 0x00080000	
	Output						
	Input Ctrl						
	Schmitt						
	Drive Strength						
	ExtPort	read gpio data in input mode		[19,19]		io -4 0xFF550070	
	IOmux	VICAP_CLKOUT_M0	0xFF558054	[0,2]	1	io -4 0xFF558054 0x00070001	
		MIPI_REFCLK_OUT0	0xFF558054	[0,2]	2	io -4 0xFF558054 0x00070002	
		GPIO3_C4	0xFF558054	[0,2]	0	io -4 0xFF558054 0x00070000	
	Pull	normal	0xFF5581E8	[8,9]	0	io -4 0xFF5581E8 0x03000000	
		pullup	0xFF5581E8	[8,9]	1	io -4 0xFF5581E8 0x03000100	
		pulldown	0xFF5581E8	[8,9]	2	io -4 0xFF5581E8 0x03000200	
	I/O Mode	input	0xFF55000C	[4,4]	0	io -4 0xFF55000C 0x00100000	
	1/O Wode	output	0xFF55000C	[4,4]	1	io -4 0xFF55000C 0x00100010	
	Output	low	0xFF550004	[4,4]	0	io -4 0xFF550004 0x00100000	
	Output	high	0xFF550004	[4,4]	1	io -4 0xFF550004 0x00100010	
GPIO3C4	Input Ctrl	disable	0xFF5581A8	[4,4]	0	io -4 0xFF5581A8 0x00100000	
	Input Cu1	enable	0xFF5581A8	[4,4]	1	io -4 0xFF5581A8 0x00100010	
	Schmitt	disable	0xFF5582A8	[4,4]	0	io -4 0xFF5582A8 0x00100000	
	Schillitt	enable	0xFF5582A8	[4,4]	1	io -4 0xFF5582A8 0x00100010	
		level0	0xFF558128	[0,5]	1	io -4 0xFF558128 0x003F0001	
		level1	0xFF558128	[0,5]	3	io -4 0xFF558128 0x003F0003	
	Duizza Cturan atla	level2	0xFF558128	[0,5]	7	io -4 0xFF558128 0x003F0007	
	Drive Strength	level3	0xFF558128	[0,5]	15	io -4 0xFF558128 0x003F000F	
		level4	0xFF558128	[0,5]	31	io -4 0xFF558128 0x003F001F	
		level5	0xFF558128	[0,5]	63	io -4 0xFF558128 0x003F003F	
	ExtPort	read gpio data in input mode	0xFF550070	[20,20]		io -4 0xFF550070	

	RV1103 GPIO V1.1.0									
Name	Config	Description	Register	Position	Valu	IO Command				
		VICAP_VSYNC_M0	0xFF558054	[4,6]	1	io -4 0xFF558054 0x00700010				
	IOmux	GPIO3_C5	0xFF558054	[4,6]	0	io -4 0xFF558054 0x00700000				
		normal	0xFF5581E8	[10,11]	0	io -4 0xFF5581E8 0x0C000000				
	Pull	pullup	0xFF5581E8	[10,11]	1	io -4 0xFF5581E8 0x0C000400				
		pulldown	0xFF5581E8	[10,11]	2	io -4 0xFF5581E8 0x0C000800				
	I/O M 1	input	0xFF55000C	[5,5]	0	io -4 0xFF55000C 0x00200000				
	I/O Mode	output	0xFF55000C	[5,5]	1	io -4 0xFF55000C 0x00200020				
	0.4.4	low	0xFF550004	[5,5]	0	io -4 0xFF550004 0x00200000				
	Output	high	0xFF550004	[5,5]	1	io -4 0xFF550004 0x00200020				
CDIO2C5	T C. 1	disable	0xFF5581A8	[5,5]	0	io -4 0xFF5581A8 0x00200000				
GPIO3C5	Input Ctrl	enable	0xFF5581A8	[5,5]	1	io -4 0xFF5581A8 0x00200020				
	G 1	disable	0xFF5582A8	[5,5]	0	io -4 0xFF5582A8 0x00200000				
	Schmitt	enable	0xFF5582A8	[5,5]	1	io -4 0xFF5582A8 0x00200020				
		level0	0xFF558128	[8,13]	1	io -4 0xFF558128 0x3F000100				
		level1	0xFF558128	[8,13]	3	io -4 0xFF558128 0x3F000300				
	D : G : 1	level2	0xFF558128	[8,13]	7	io -4 0xFF558128 0x3F000700				
	Drive Strength	level3	0xFF558128	[8,13]	15	io -4 0xFF558128 0x3F000F00				
		level4	0xFF558128	[8,13]	31	io -4 0xFF558128 0x3F001F00				
		level5	0xFF558128	[8,13]	63	io -4 0xFF558128 0x3F003F00				
	ExtPort	read gpio data in input mode	0xFF550070	[21,21]		io -4 0xFF550070				
	IOmux	VICAP_D10	0xFF558054	[8,10]	1	io -4 0xFF558054 0x07000100				
		PWM7_IR_M2	0xFF558054	[8,10]	2	io -4 0xFF558054 0x07000200				
		MIPI_REFCLK_OUT1	0xFF558054	[8,10]	3	io -4 0xFF558054 0x07000300				
		GPIO3_C6	0xFF558054	[8,10]	0	io -4 0xFF558054 0x07000000				
		normal	0xFF5581E8	[12,13]	0	io -4 0xFF5581E8 0x30000000				
	Pull	pullup	0xFF5581E8	[12,13]	1	io -4 0xFF5581E8 0x30001000				
		pulldown	0xFF5581E8	[12,13]	2	io -4 0xFF5581E8 0x30002000				
	I/O Mode	input	0xFF55000C	[6,6]	0	io -4 0xFF55000C 0x00400000				
	I/O Mode	output	0xFF55000C	[6,6]	1	io -4 0xFF55000C 0x00400040				
	Outmut	low	0xFF550004	[6,6]	0	io -4 0xFF550004 0x00400000				
CDIO2CC	Output	high	0xFF550004	[6,6]	1	io -4 0xFF550004 0x00400040				
GPIO3C6	Laurent Ctal	disable	0xFF5581A8	[6,6]	0	io -4 0xFF5581A8 0x00400000				
	Input Ctrl	enable	0xFF5581A8	[6,6]	1	io -4 0xFF5581A8 0x00400040				
	G .1	disable	0xFF5582A8	[6,6]	0	io -4 0xFF5582A8 0x00400000				
	Schmitt	enable	0xFF5582A8	[6,6]	1	io -4 0xFF5582A8 0x00400040				
		level0	0xFF55812C	[0,5]	1	io -4 0xFF55812C 0x003F0001				
		level1	0xFF55812C	[0,5]	3	io -4 0xFF55812C 0x003F0003				
	Duixo Stuan atl	level2	0xFF55812C	[0,5]	7	io -4 0xFF55812C 0x003F0007				
	Drive Strength	level3	0xFF55812C	[0,5]	15	io -4 0xFF55812C 0x003F000F				
		level4	0xFF55812C	[0,5]	31	io -4 0xFF55812C 0x003F001F				
		level5	0xFF55812C	[0,5]	63	io -4 0xFF55812C 0x003F003F				
	ExtPort	read gpio data in input mode	0xFF550070	[22,22]		io -4 0xFF550070				

		R	V1103 GP	IO V1.1	.0_	
Name	Config	Description	Register	Position	Valu	IO Command
		VICAP_D11	0xFF558054	[12,14]	1	io -4 0xFF558054 0x70001000
	10	UART5_TX_M2	0xFF558054	[12,14]	2	io -4 0xFF558054 0x70002000
	IOmux	I2C4 SCL M2	0xFF558054	[12,14]	3	io -4 0xFF558054 0x70003000
		GPIO3 C7	0xFF558054	[12,14]	0	io -4 0xFF558054 0x70000000
		normal	0xFF5581E8	[14,15]	0	io -4 0xFF5581E8 0xC0000000
	Pull	pullup	0xFF5581E8	[14,15]	1	io -4 0xFF5581E8 0xC0004000
		pulldown	0xFF5581E8	[14,15]	2	io -4 0xFF5581E8 0xC0008000
	7/0.75	input	0xFF55000C	[7,7]	0	io -4 0xFF55000C 0x00800000
	I/O Mode	output	0xFF55000C	[7,7]	1	io -4 0xFF55000C 0x00800080
		low	0xFF550004	[7,7]	0	io -4 0xFF550004 0x00800000
~~~~	Output	high	0xFF550004	[7,7]	1	io -4 0xFF550004 0x00800080
GPIO3C7		disable	0xFF5581A8	[7,7]	0	io -4 0xFF5581A8 0x00800000
	Input Ctrl	enable	0xFF5581A8	[7,7]	1	io -4 0xFF5581A8 0x00800080
	~	disable	0xFF5582A8	[7,7]	0	io -4 0xFF5582A8 0x00800000
	Schmitt	enable	0xFF5582A8	[7,7]	1	io -4 0xFF5582A8 0x00800080
		level0	0xFF55812C	[8,13]	1	io -4 0xFF55812C 0x3F000100
		level1	0xFF55812C	[8,13]	3	io -4 0xFF55812C 0x3F000300
	Drive Strength	level2	0xFF55812C	[8,13]	7	io -4 0xFF55812C 0x3F000700
		level3	0xFF55812C	[8,13]	15	io -4 0xFF55812C 0x3F000F00
		level4	0xFF55812C	[8,13]	31	io -4 0xFF55812C 0x3F001F00
		level5	0xFF55812C	[8,13]	63	io -4 0xFF55812C 0x3F003F00
	ExtPort	read gpio data in input mode		[23,23]		io -4 0xFF550070
		VICAP D12	0xFF558058	[0,2]	1	io -4 0xFF558058 0x00070001
	IOmux	UART5 RX M2	0xFF558058	[0,2]	2	io -4 0xFF558058 0x00070002
		I2C4 SDA M2	0xFF558058	[0,2]	3	io -4 0xFF558058 0x00070003
		GPIO3 D0	0xFF558058	[0,2]	0	io -4 0xFF558058 0x00070000
		normal	0xFF5581EC	[0,1]	0	io -4 0xFF5581EC 0x00030000
	Pull	pullup	0xFF5581EC	[0,1]	1	io -4 0xFF5581EC 0x00030001
		pulldown	0xFF5581EC	[0,1]	2	io -4 0xFF5581EC 0x00030002
		input	0xFF55000C	[8,8]	0	io -4 0xFF55000C 0x01000000
	I/O Mode	output	0xFF55000C	[8,8]	1	io -4 0xFF55000C 0x01000100
		low	0xFF550004	[8,8]	0	io -4 0xFF550004 0x01000000
	Output	high	0xFF550004	[8,8]	1	io -4 0xFF550004 0x01000100
GPIO3D0		disable	0xFF5581AC	[0,0]	0	io -4 0xFF5581AC 0x00010000
	Input Ctrl	enable	0xFF5581AC	[0,0]	1	io -4 0xFF5581AC 0x00010001
		disable	0xFF5582AC	[0,0]	0	io -4 0xFF5582AC 0x00010000
	Schmitt	enable	0xFF5582AC		1	io -4 0xFF5582AC 0x00010001
		level0	0xFF558130	[0,5]	1	io -4 0xFF558130 0x003F0001
		level1	0xFF558130	[0,5]	3	io -4 0xFF558130 0x003F0003
		level2	0xFF558130	[0,5]	7	io -4 0xFF558130 0x003F0007
	Drive Strength	level3	0xFF558130	[0,5]	15	io -4 0xFF558130 0x003F000F
		level4	0xFF558130	[0,5]	31	io -4 0xFF558130 0x003F001F
		level5	0xFF558130	[0,5]	63	io -4 0xFF558130 0x003F003F
	ExtPort	read gpio data in input mode		[24,24]	0.5	io -4 0xFF550070
	LAH UIT	read gpro data in input inode	OALT 3300/0	[47,44]		10 -4 UALL 2200/U

	RV1103 GPIO V1.1.0									
Name	Config	Description	Register	Position	Valu	IO Command				
		EMMC_D2	0xFF568000	[8,10]	1	io -4 0xFF568000 0x07000100				
	IOmux	FSPI_D2	0xFF568000	[8,10]	2	io -4 0xFF568000 0x07000200				
		GPIO4_A2	0xFF568000	[8,10]	0	io -4 0xFF568000 0x07000000				
		normal	0xFF568070	[4,5]	0	io -4 0xFF568070 0x00300000				
	Pull	pullup	0xFF568070	[4,5]	1	io -4 0xFF568070 0x00300010				
		pulldown	0xFF568070	[4,5]	2	io -4 0xFF568070 0x00300020				
	I/O M 1	input	0xFF560008	[2,2]	0	io -4 0xFF560008 0x00040000				
	I/O Mode	output	0xFF560008	[2,2]	1	io -4 0xFF560008 0x00040004				
	0 4 4	low	0xFF560000	[2,2]	0	io -4 0xFF560000 0x00040000				
	Output	high	0xFF560000	[2,2]	1	io -4 0xFF560000 0x00040004				
GPIO4A2	I (C) 1	disable	0xFF568060	[2,2]	0	io -4 0xFF568060 0x00040000				
	Input Ctrl	enable	0xFF568060	[2,2]	1	io -4 0xFF568060 0x00040004				
	G 1	disable	0xFF5680A0	[2,2]	0	io -4 0xFF5680A0 0x00040000				
	Schmitt	enable	0xFF5680A0	[2,2]	1	io -4 0xFF5680A0 0x00040004				
		level0	0xFF568024	[0,5]	1	io -4 0xFF568024 0x003F0001				
		level1	0xFF568024	[0,5]	3	io -4 0xFF568024 0x003F0003				
	Drive Strength	level2	0xFF568024	[0,5]	7	io -4 0xFF568024 0x003F0007				
		level3	0xFF568024	[0,5]	15	io -4 0xFF568024 0x003F000F				
		level4	0xFF568024	[0,5]	31	io -4 0xFF568024 0x003F001F				
		level5	0xFF568024	[0,5]	63	io -4 0xFF568024 0x003F003F				
	ExtPort	read gpio data in input mode	0xFF560070	[2,2]		io -4 0xFF560070				
	IOmux	EMMC_D1	0xFF568000	[12,14]	1	io -4 0xFF568000 0x70001000				
		FSPI_D1	0xFF568000	[12,14]	2	io -4 0xFF568000 0x70002000				
		GPIO4_A3	0xFF568000	[12,14]	0	io -4 0xFF568000 0x70000000				
		normal	0xFF568070	[6,7]	0	io -4 0xFF568070 0x00C00000				
	Pull	pullup	0xFF568070	[6,7]	1	io -4 0xFF568070 0x00C00040				
		pulldown	0xFF568070	[6,7]	2	io -4 0xFF568070 0x00C00080				
	I/O Mode	input	0xFF560008	[3,3]	0	io -4 0xFF560008 0x00080000				
	I/O Mode	output	0xFF560008	[3,3]	1	io -4 0xFF560008 0x00080008				
	Outmut	low	0xFF560000	[3,3]	0	io -4 0xFF560000 0x00080000				
	Output	high	0xFF560000	[3,3]	1	io -4 0xFF560000 0x00080008				
GPIO4A3	Innut Ctul	disable	0xFF568060	[3,3]	0	io -4 0xFF568060 0x00080000				
	Input Ctrl	enable	0xFF568060	[3,3]	1	io -4 0xFF568060 0x00080008				
	G .1	disable	0xFF5680A0		0	io -4 0xFF5680A0 0x00080000				
	Schmitt	enable	0xFF5680A0	[3,3]	1	io -4 0xFF5680A0 0x00080008				
		level0	0xFF568024	[8,13]	1	io -4 0xFF568024 0x3F000100				
		level1	0xFF568024	[8,13]	3	io -4 0xFF568024 0x3F000300				
	Duixes Stude -41-	level2	0xFF568024	[8,13]	7	io -4 0xFF568024 0x3F000700				
	Drive Strength	level3	0xFF568024	[8,13]	15	io -4 0xFF568024 0x3F000F00				
		level4	0xFF568024	[8,13]	31	io -4 0xFF568024 0x3F001F00				
		level5	0xFF568024	[8,13]	63	io -4 0xFF568024 0x3F003F00				
	ExtPort	read gpio data in input mode	0xFF560070	[3,3]		io -4 0xFF560070				

		R	V1103 GP	IO V1.1	.0		
Name	Config	Description	Register	Position	Valu	IO Command	
	6	EMMC D0	0xFF568004	[0,2]	1	io -4 0xFF568004 0x00070001	
	IOmux	FSPI D0	0xFF568004	[0,2]	2	io -4 0xFF568004 0x00070002	
		GPIO4 A4	0xFF568004	[0,2]	0	io -4 0xFF568004 0x00070000	
		normal	0xFF568070	[8,9]	0	io -4 0xFF568070 0x03000000	
	Pull	pullup	0xFF568070	[8,9]	1	io -4 0xFF568070 0x03000100	
		pulldown	0xFF568070	[8,9]	2	io -4 0xFF568070 0x03000200	
	I/O Mode	input	0xFF560008	[4,4]	0	io -4 0xFF560008 0x00100000	
	I/O Mode	output	0xFF560008	[4,4]	1	io -4 0xFF560008 0x00100010	
	Output	low	0xFF560000	[4,4]	0	io -4 0xFF560000 0x00100000	
	Output	high	0xFF560000	[4,4]	1	io -4 0xFF560000 0x00100010	
GPIO4A4	Input Ctrl	disable	0xFF568060	[4,4]	0	io -4 0xFF568060 0x00100000	
	Input Cu1	enable	0xFF568060	[4,4]	1	io -4 0xFF568060 0x00100010	
	Schmitt	disable	0xFF5680A0	[4,4]	0	io -4 0xFF5680A0 0x00100000	
	Schillitt	enable	0xFF5680A0	[4,4]	1	io -4 0xFF5680A0 0x00100010	
		level0	0xFF568028	[0,5]	1	io -4 0xFF568028 0x003F0001	
		level1	0xFF568028	[0,5]	3	io -4 0xFF568028 0x003F0003	
	Drive Strength	level2	0xFF568028	[0,5]	7	io -4 0xFF568028 0x003F0007	
	Dive Strength	level3	0xFF568028	[0,5]	15	io -4 0xFF568028 0x003F000F	
		level4	0xFF568028	[0,5]	31	io -4 0xFF568028 0x003F001F	
		level5	0xFF568028	[0,5]	63	io -4 0xFF568028 0x003F003F	
	ExtPort	read gpio data in input mode	0xFF560070	[4,4]		io -4 0xFF560070	
	IOmux	EMMC_D3	0xFF568004	[8,10]	1	io -4 0xFF568004 0x07000100	
		FSPI_D3	0xFF568004	[8,10]	2	io -4 0xFF568004 0x07000200	
		GPIO4_A6	0xFF568004	[8,10]	0	io -4 0xFF568004 0x07000000	
		normal	0xFF568070	[12,13]	0	io -4 0xFF568070 0x30000000	
	Pull	pullup	0xFF568070	[12,13]	1	io -4 0xFF568070 0x30001000	
		pulldown	0xFF568070	[12,13]	2	io -4 0xFF568070 0x30002000	
	I/O Mode	input	0xFF560008	[6,6]	0	io -4 0xFF560008 0x00400000	
	1/O Wiode	output	0xFF560008	[6,6]	1	io -4 0xFF560008 0x00400040	
	Output	low	0xFF560000	[6,6]	0	io -4 0xFF560000 0x00400000	
	-	high	0xFF560000	[6,6]	1	io -4 0xFF560000 0x00400040	
GPIO4A6	Input Ctrl	disable	0xFF568060	[6,6]	0	io -4 0xFF568060 0x00400000	
	Input Cu1	enable	0xFF568060	[6,6]	1	io -4 0xFF568060 0x00400040	
	Schmitt	disable	0xFF5680A0	[6,6]	0	io -4 0xFF5680A0 0x00400000	
	Schillt	enable	0xFF5680A0	[6,6]	1	io -4 0xFF5680A0 0x00400040	
		level0	0xFF56802C	[0,5]	1	io -4 0xFF56802C 0x003F0001	
		level1	0xFF56802C	[0,5]	3	io -4 0xFF56802C 0x003F0003	
	Drive Strength	level2	0xFF56802C	[0,5]	7	io -4 0xFF56802C 0x003F0007	
		level3	0xFF56802C	[0,5]	15	io -4 0xFF56802C 0x003F000F	
		level4	0xFF56802C	[0,5]	31	io -4 0xFF56802C 0x003F001F	
		level5	0xFF56802C	[0,5]	63	io -4 0xFF56802C 0x003F003F	
	ExtPort	read gpio data in input mode		[6,6]		io -4 0xFF560070	

Name			R	V1103 GP	IO V1.1	.0	
HOME    FSPI CSON   0xFF568008   0.2    2   io 4 0xFF568008 0x00070002	Name	Config	Description	Register	Position	Valu	IO Command
GPIO4 B0			EMMC_CMD	0xFF568008	[0,2]	1	io -4 0xFF568008 0x00070001
Pull		IOmux	FSPI_CS0N	0xFF568008	[0,2]	2	io -4 0xFF568008 0x00070002
Pull			GPIO4_B0	0xFF568008	[0,2]	0	io -4 0xFF568008 0x00070000
Pulldown   OxFF568074   O.1]   2   io 4 OxFF568074 0x00030002			normal	0xFF568074	[0,1]	0	io -4 0xFF568074 0x00030000
I/O Mode		Pull	pullup	0xFF568074	[0,1]	1	io -4 0xFF568074 0x00030001
A			pulldown	0xFF568074	[0,1]	2	io -4 0xFF568074 0x00030002
Drive Strength   Driv		1/0.1/1	input	0xFF560008	[8,8]	0	io -4 0xFF560008 0x01000000
Output		I/O Mode	output	0xFF560008		1	io -4 0xFF560008 0x01000100
Output			· ·	0xFF560000		0	io -4 0xFF560000 0x01000000
Input Ctrl		Output	high			1	
Input Ctrl	GPIO4B0			0xFF568064		0	io -4 0xFF568064 0x00010000
Schmitt		Input Ctrl				l	
Schmitt			disable			0	
Drive Strength		Schmitt				1	
Drive Strength			level0			1	
Drive Strength						3	
Drive Strength   level3			level2				io -4 0xFF568030 0x003F0007
Ievel4		Drive Strength					
Revel5			level4				
ExtPort   read gpio data in input mode   0xFF560070   [8,8]   io -4 0xFF560070							
IOmux		ExtPort	read gpio data in input mode				io -4 0xFF560070
IOmux			7.			1	
Pull			_			2	
Pull			GPIO4 B1			0	
Pull			_			0	
Dulldown   DxFF568074   [2,3]   2   io -4 0xFF568074 0x000C0008   input   0xFF560008   [9,9]   0   io -4 0xFF560008 0x02000000   io -4 0xFF560008 0x02000000   io -4 0xFF560008 0x02000200   io -4 0xFF560008 0x02000200   io -4 0xFF560000 0x02000000   io -4 0xFF568064 0x00020000   io -4 0xFF568030 0x3F000100   io -4 0xFF568030 0x3F000100   io -4 0xFF568030 0x3F000300   io -4 0xFF568030 0x3F0003		Pull	pullup			1	io -4 0xFF568074 0x000C0004
I/O Mode			<u> </u>			2	
Output		-/	*	0xFF560008		0	io -4 0xFF560008 0x02000000
Output low 0xFF560000 [9,9] 0 io -4 0xFF560000 0x02000000 high 0xFF560000 [9,9] 1 io -4 0xFF560000 0x02000200 low ligh 0xFF560000 [9,9] 1 io -4 0xFF560000 0x02000200 low		I/O Mode	_	0xFF560008		1	io -4 0xFF560008 0x02000200
GPIO4B1 Input Ctrl Input Ctrl Contact Contact Input Ctrl Input Ctrl Input Ctrl  Contact Contact Input Ctrl Input Ctrl  Contact Contact Input Ctrl Input Ct				0xFF560000		0	io -4 0xFF560000 0x02000000
Input Ctrl   disable   0xFF568064   [1,1]   0   io -4 0xFF568064 0x00020000		Output	high	0xFF560000		1	
Schmitt	GPIO4B1					0	io -4 0xFF568064 0x00020000
Schmitt         disable enable         0xFF5680A4         [1,1]         0         io -4 0xFF5680A4 0x00020000           enable enable         0xFF5680A4         [1,1]         1         io -4 0xFF5680A4 0x00020002           level0         0xFF568030         [8,13]         1         io -4 0xFF568030 0x3F000100           level1         0xFF568030         [8,13]         3         io -4 0xFF568030 0x3F000300           level2         0xFF568030         [8,13]         7         io -4 0xFF568030 0x3F000700           level3         0xFF568030         [8,13]         15         io -4 0xFF568030 0x3F000F00           level4         0xFF568030         [8,13]         31         io -4 0xFF568030 0x3F001F00		Input Ctrl	enable	0xFF568064		1	
Schmitt			disable			0	
level0		Schmitt				1	
Drive Strength						1	
Drive Strength level2							
Drive Strength   level3   0xFF568030 [8,13]   15   io -4 0xFF568030 0x3F000F00   level4   0xFF568030 [8,13]   31   io -4 0xFF568030 0x3F001F00		D	level?				
level4 0xFF568030 [8,13] 31 io -4 0xFF568030 0x3F001F00		Drive Strength				15	
UNICUOICAU UCUOOCATAXU			level5	0xFF568030	[8,13]	63	io -4 0xFF568030 0x3F003F00
ExtPort read gpio data in input mode 0xFF560070 [9,9] io -4 0xFF560070		ExtPort					

		R	V1103 GP	IO V1.1	.0		
Name	Config	Description	Register	Position	Valu	IO Command	
		ADC_IN0	0xFF568010	[0,2]	1	io -4 0xFF568010 0x00070001	
	IOmux	CDIO4 CO	0. EEG(0010	[0.0]	0	io -4 0xFF568010 0x00070000	
		GPIO4_C0	0xFF568010	[0,2]	0	io -4 0xFF5680C0 0x00040004	input only
		normal	0xFF5680C0	[13,14]	0	io -4 0xFF5680C0 0x60000000	
	Pull	pullup	0xFF5680C0	[13,14]	3	io -4 0xFF5680C0 0x60006000	
		pulldown	0xFF5680C0	[13,14]	1	io -4 0xFF5680C0 0x60002000	
	I/O Mode	input	0xFF56000C	[0,0]	0	io -4 0xFF56000C 0x00010000	
	I/O Mode						
GPIO4C0	Output						
0110400	Output						
	Input Ctrl	disable	0xFF5680C0	[2,2]	0	io -4 0xFF5680C0 0x00040000	
		enable	0xFF5680C0	[2,2]	1	io -4 0xFF5680C0 0x00040004	
	Schmitt						
		level0	0xFF5680C0	[10,11]	0	io -4 0xFF5680C0 0x0C000000	
	Drive Strength	level1	0xFF5680C0	[10,11]	1	io -4 0xFF5680C0 0x0C000400	
		level2	0xFF5680C0	[10,11]	2	io -4 0xFF5680C0 0x0C000800	
		level3	0xFF5680C0	[10,11]	3	io -4 0xFF5680C0 0x0C000C00	
	ExtPort	read gpio data in input mode	0xFF560070	[16,16]		io -4 0xFF560070	
	10	ADC_IN1	0xFF568010	[4,6]	1	io -4 0xFF568010 0x00700010	
		PWM1_M1	0xFF568010	[4,6]	2	io -4 0xFF568010 0x00700020	
	IOmux	CDIO4 C1	0-EE569010	[4,6]	0	io -4 0xFF568010 0x00700000	
		GPIO4_C1	0xFF568010		0	io -4 0xFF5680C0 0x00080008	input only
		normal	0xFF5680C0	[13,14]	0	io -4 0xFF5680C0 0x60000000	
	Pull	pullup	0xFF5680C0	[13,14]	3	io -4 0xFF5680C0 0x60006000	
		pulldown	0xFF5680C0	[13,14]	1	io -4 0xFF5680C0 0x60002000	
	I/O Mode	input	0xFF56000C	[1,1]	0	io -4 0xFF56000C 0x00020000	
	I/O Mode						
GPIO4C1	Outmut						
	Output						
	Innut Ctul	disable	0xFF5680C0	[3,3]	0	io -4 0xFF5680C0 0x00080000	
	Input Ctrl	enable	0xFF5680C0	[3,3]	1	io -4 0xFF5680C0 0x00080008	
	Schmitt						
		level0	0xFF5680C0	[10,11]	0	io -4 0xFF5680C0 0x0C000000	
	Drive Strong 41	level1	0xFF5680C0	[10,11]	1	io -4 0xFF5680C0 0x0C000400	
	Drive Strength	level2	0xFF5680C0	[10,11]	2	io -4 0xFF5680C0 0x0C000800	
		level3	0xFF5680C0	[10,11]	3	io -4 0xFF5680C0 0x0C000C00	
	ExtPort	read gpio data in input mode	0xFF560070	[17,17]		io -4 0xFF560070	