2023 Digital IC Design Homework 5

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| NAME | 吳紀寬 | | | |
| Student ID | N26112128 | | | |
| **Simulation Result** | | | | |
| Functional simulation | | Completed | Gate-level simulation | Completed |
|  | | |  | |
| **Evaluation Results** | | | | |
| test1.png | | **27.76** | test2.png | **26.83** |
| test3.png | | **30.73** | test4.png | **22.73** |
| test5.png | | **23.61** | test6.png | **26.47** |
| **Description of your design** | | | | |
| **Algorithm:**    **Finite State Machine**    **Readdata: Load data into RGB memory.**  **Readcolor: load necessary RGB pixel into buffer.**  **Compute: Get result by above algorithm and write result into RGB memory.**  **Finish: Get next pixel address.**  **Terminate: terminate program.** | | | | |

*Scoring = average PSNR of the six test images*

**\* PSNR of all interpolation results should meet at least the baseline.**