Cycle	Instruction Retired
1	
2	
3	
4	
5	lbi r0, 0
6	lbi r5, 43
7	lbi r6, 43
8	lbi r7, 43
9	ld r1, r0, 0
10	st r5, r1, 0
11	ld r1, r0, 2
12	st r6, r1, 1
13	ld r1, r0, 4
14	st r7, r1, 1
15	HALT
16	
17	
18	

Reason
There is a depency between ld in cycle 9 and st in cycle11, Rd of load is the same as Rs of ST. There is a Mem to Ex

forwarding that allows read data from memory to be used