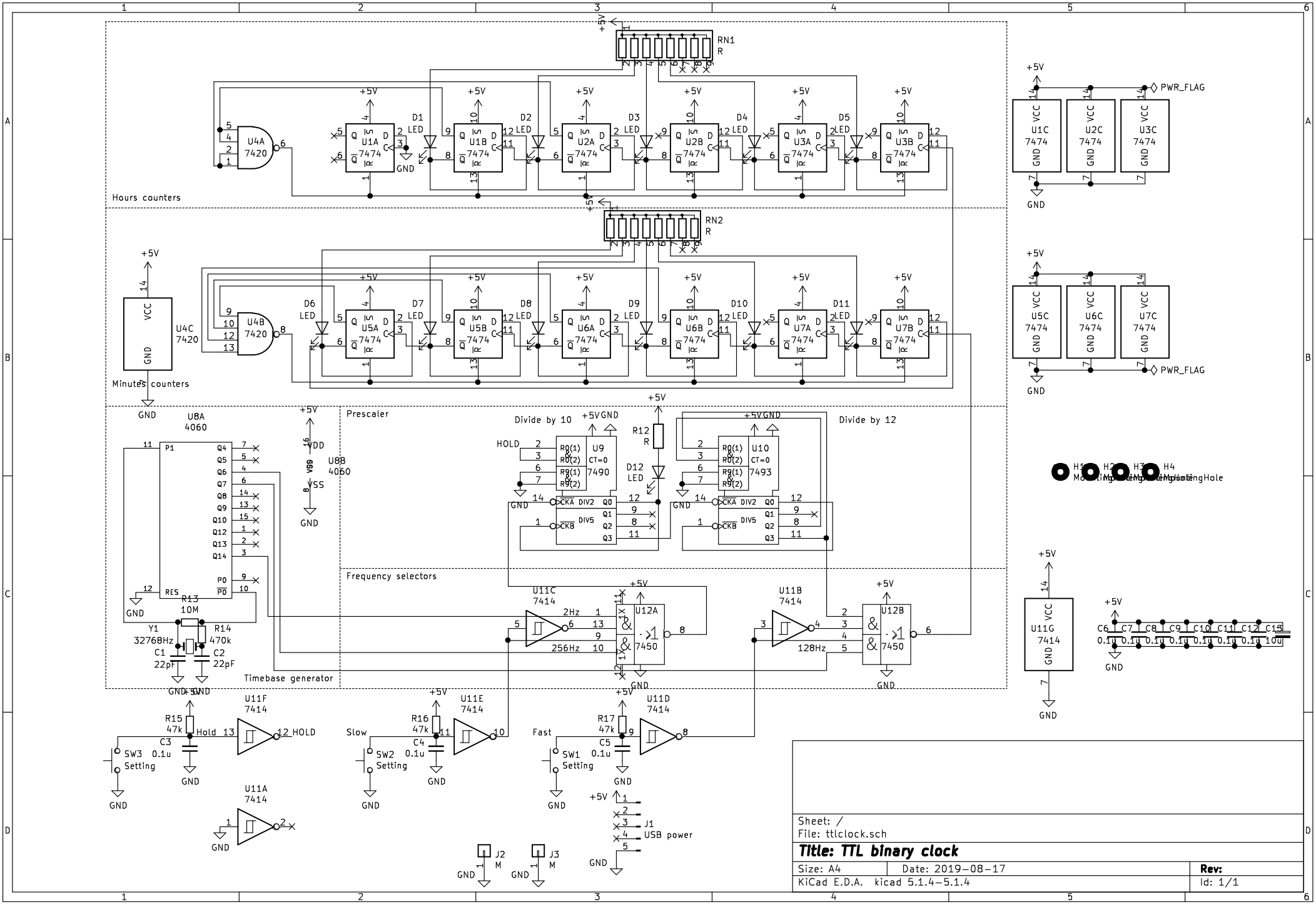
Hours counters
Minutes counters
Prescaler
Divide by 10
Divide by 12
Frequency selectors
Timebase generator
RN1
RN2
U4A 7420
U4B 7420
U4C 7420
U1A 7474
U1B 7474
U2A 7474
U2B 7474
U3A 7474
U3B 7474
U5A 7474
U5B 7474
U6A 7474
U6B 7474
U7A 7474
U7B 7474
U1C 7474
U2C 7474
U3C 7474
U5C 7474
U6C 7474
U7C 7474
U8A 4060
U8B 4060
U9 7490
U10 7493
U11C 7414
U12A 7450
U11B 7414
U12B 7450
U11G 7414
U11F 7414
U11E 7414
U11D 7414
U11A 7414
Y1 32768Hz
R13 10M
R14 470k
R15 47k
R16 47k
R17 47k
R12 R
SW3 Setting
SW2 Setting
SW1 Setting
J1 USB power
J2 M
J3 M
PWR_FLAG
Sheet: /
File: ttlclock.sch
Title: TTL binary clock
Size: A4
Date: 2019-08-17
Rev:
KiCad E.D.A. kicad 5.1.4-5.1.4
Id: 1/1