

6.12.8 Synchronization Functions

The OpenCL C programming language implements the following synchronization function.

Function	Description
void barrier (cl_mem_fence_flags <i>flags</i>)	<p>All work-items in a work-group executing the kernel on a processor must execute this function before any are allowed to continue execution beyond the barrier. This function must be encountered by all work-items in a work-group executing the kernel.</p> <p>If barrier is inside a conditional statement, then all work-items must enter the conditional if any work-item enters the conditional statement and executes the barrier.</p> <p>If barrier is inside a loop, all work-items must execute the barrier for each iteration of the loop before any are allowed to continue execution beyond the barrier.</p> <p>The barrier function also queues a memory fence (reads and writes) to ensure correct ordering of memory operations to local or global memory.</p> <p>The <i>flags</i> argument specifies the memory address space and can be set to a combination of the following literal values.</p> <p>CLK_LOCAL_MEM_FENCE - The barrier function will either flush any variables stored in local memory or queue a memory fence to ensure correct ordering of memory operations to local memory.</p> <p>CLK_GLOBAL_MEM_FENCE – The barrier function will queue a memory fence to ensure correct ordering of memory operations to global memory. This can be useful when work-items, for example, write to buffer or image objects and then want to read the updated data.</p>

Table 6.16 *Built-in Synchronization Functions*

6.12.9 Explicit Memory Fence Functions

The OpenCL C programming language implements the following explicit memory fence functions to provide ordering between memory operations of a work-item.

Function	Description
void mem_fence (cl_mem_fence_flags <i>flags</i>)	<p>Orders loads and stores of a work-item executing a kernel. This means that loads and stores preceding the mem_fence will be committed to memory before any loads and stores following the mem_fence.</p> <p>The <i>flags</i> argument specifies the memory address space and can be set to a combination of the following literal values:</p> <p>CLK_LOCAL_MEM_FENCE CLK_GLOBAL_MEM_FENCE.</p>
void read_mem_fence (cl_mem_fence_flags <i>flags</i>)	<p>Read memory barrier that orders only loads.</p> <p>The <i>flags</i> argument specifies the memory address space and can be set to to a combination of the following literal values:</p> <p>CLK_LOCAL_MEM_FENCE CLK_GLOBAL_MEM_FENCE.</p>
void write_mem_fence (cl_mem_fence_flags <i>flags</i>)	<p>Write memory barrier that orders only stores.</p> <p>The <i>flags</i> argument specifies the memory address space and can be set to to a combination of the following literal values:</p> <p>CLK_LOCAL_MEM_FENCE CLK_GLOBAL_MEM_FENCE.</p>

Table 6.17 *Built-in Explicit Memory Fence Functions*