Electronic Circuit 2

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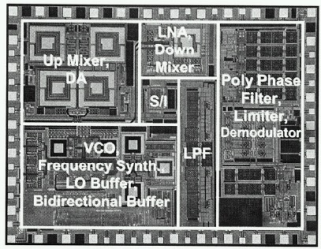
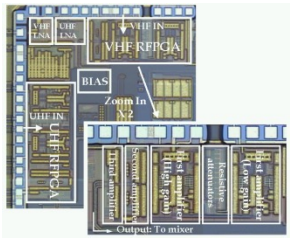
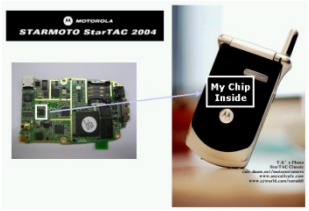
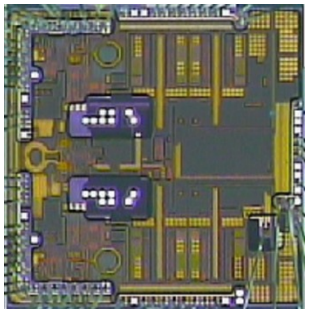
Professor, Yonsei Univ.

Professor Introduction









- B.S: Electrical Engineering, Yonsei 2000
- M.S: Dept. of EE, KAIST, 2002,
  CMOS RF/Analog Circuit Design
- Ph. D: Dept. of EE, KAIST, 2005
  CMOS RF/Analog Circuit Design

- Integrant Technologies (now Analog Device): 2002-2005
  RF chip for CDMA
  CMOS receiver for T-DMB, S-DBM, DVB-H
- Qualcomm : 2006-2007
  CMOS receiver for Mobile Broadcasting (DVB-H, Media FLO)
- Yonsei : 2007~
  will focus on wireless SOC (System on a Chip) research

Course Introduction

- Lecturer: Tae Wook Kim,
  - taewook.kim@yonsei.ac.kr Tel. 5770, office C620
- Text Book:
  - Fundamentals of Microelectronics, Razavi
  - Lecture note
- Pre-requisite : Electronic Circuit I
  - TA: Woojin Choi, (7879)
  - If you miss the class more than 1/3, you will automatically get F (1/3 이상 결석시: F)
- Attitude is important (수업 태도) :
  - No smart phone, notebook etc. 노트북 (스마트폰) 사용금지 (칠판 사용 강의, 강의 노트 배부)
  - 시험시 스마트폰 사용금지
  - No food (음식물 섭취 금지)
  - No recording
  - Don't move (강의 중 이동 금지)
  - Do not disturb others 다른 사람 방해하는 사람, 다른 짓 하는 사람, 자는 사람
  - 학점 올려달라는 사람

Course Introduction

- Academic dishonesty: see syllabus
- Grading policy:
  - Mid term (25%) Final (25%) Project (10%) Quiz (20%) Attendance(10%) Homework (10%)
- There will be a quiz after finishing each chapter
- Extra Credit can be given for extra assignments
- Designated Seat 지정 좌석제 (좌석 추첨)
- Office hour: Thur. 6,7
- Re-taker penalty: one grade down (A0 -> A-)
- Project: Amplifier Design
  - Pspice Simulation
  - 5 People 1 team (Contribution)
  - Report
- 전자 출결

Questions

- Why do you want to be an EE engineer?
- Why do you take this class?
- What do you think you are going to learn in this class?
- Rate your level of knowledge on microelectronic circuits (1-10)
- Anything to suggest for this class