| | |
|---|---|
| Analysis & Synthesis Status | Successful - Sun Jul 03 12:29:18 2016 |
| Quartus II Version | 7.2 Build 151 09/26/2007 SJ Full Version |
| Revision Name | crossbar_2m2s |
| Top-level Entity Name | crossbar_2m2s |
| Family | Stratix II |
| Logic utilization | N/A |
| Combinational ALUTs | 212 |
| Dedicated logic registers | 2 |
| Total registers | 2 |
| Total pins | 397 |
| Total virtual pins | 0 |
| Total block memory bits | 0 |
| DSP block 9-bit elements | 0 |
| Total PLLs | 0 |
| Total DLLs | 0 |