

Flow Status	Successful - Sun Jul 03 12:29:46 2016
Quartus II Version	7.2 Build 151 09/26/2007 SJ Full Version
Revision Name	crossbar_2m2s
Top-level Entity Name	crossbar_2m2s
Family	Stratix II
Met timing requirements	Yes
Logic utilization	< 1 %
Combinational ALUTs	212 / 27,104 ( < 1 % )
Dedicated logic registers	2 / 27,104 ( < 1 % )
Total registers	2
Total pins	397 / 501 ( 79 % )
Total virtual pins	0
Total block memory bits	0 / 1,369,728 ( 0 % )
DSP block 9-bit elements	0 / 128 ( 0 % )
Total PLLs	0 / 6 ( 0 % )
Total DLLs	0 / 2 ( 0 % )
Device	EP2S30F672C3
Timing Models	Final