Date: July 01, 2016 Project: crossbar\_2m2s

Analysis & Synthesis Status Successful - Fri Jul 01 02:36:46 2016

Quartus II Version 7.2 Build 151 09/26/2007 SJ Full Version

Revision Name crossbar\_2m2s

Top-level Entity Name crossbar\_2m2s

Family Stratix II

Logic utilization N/A

Combinational ALUTs 216

Dedicated logic registers 0

Total registers 0

Total pins 396

Total virtual pins 0

Total block memory bits 0

DSP block 9-bit elements 0

Total PLLs 0

Total DLLs 0