Date: June 30, 2016 Project: crossbar_2m2s

Analysis & Synthesis Status Successful - Thu Jun 30 20:01:18 2016

Quartus II Version 7.2 Build 151 09/26/2007 SJ Full Version

Revision Name crossbar_2m2s

Top-level Entity Name crossbar_2m2s

Family Stratix II

Logic utilization N/A

Combinational ALUTs 256

Dedicated logic registers 0

Total registers 0

Total pins 396

Total virtual pins 0

Total block memory bits 0

DSP block 9-bit elements 0

Total PLLs 0

Total DLLs 0