

Surname :	First Name :
N° Student Card :	Date : 27 / 11 / 2025
Section :	Group :
Module : Architecture of Computer 1 (Midterm) Academic Year : 2025 / 2026	
CODE :	CODE : Reserved for administration
MARK :  <b>/ 20</b>	<b>Exercise 1: (04 points)</b> Select all correct answers (multiple correct answers possible). +0.5 point for each correct answer. 1. The Simplified expression of $(A + B)(\bar{A} + B)(A + \bar{A})$ is: a) A <span style="float: right;">c) B</span> b) A+B <span style="float: right;">d) 0</span> 2. The symmetric function of 3 variables is 1 only if the number of 1s is even. The minimal SOP form is: a) AB + AC + BC <span style="float: right;">c) A + B + C</span> b) A⊕B⊕C <span style="float: right;">d) ABC</span> <span style="color: green; float: right;">"None is correcte"</span> 3. In IEEE 754 single precision, the hexadecimal number C1200000 corresponds to : a) -10.0 <span style="float: right;">c) -9.5</span> b) -9.0 <span style="float: right;">d) -8.5</span> 4. The biased exponent for the value 1.0 in IEEE 754 single precision is: a) 127 <span style="float: right;">c) 0</span> b) 128 <span style="float: right;">d) 1</span> 5. In The sum of two 8-bit binary numbers 01101101 + 11011011 is: a) 01001000 (overflow) <span style="float: right;">c) 10100110 (overflow)</span> b) 01001000 (no overflow) <span style="float: right;">d) 10100110 (no overflow)</span> 6. Which single logic gate can implement any Boolean function? a) AND <span style="float: right;">c) NAND</span> b) XOR <span style="float: right;">d) OR</span> 7. Given the Boolean expression $F = A + AB$ , what is the dual of this expression? a) $F_d = A \cdot (A + B)$ <span style="float: right;">c) <math>F_d = A + (A \cdot B)</math></span> b) $F_d = A \cdot (A \cdot B)$ <span style="float: right;">d) <math>F_d = A \cdot (A + B) + B</math></span> 8. Which of the following statements about Karnaugh maps (K-maps) is TRUE? a) K-maps can only be used for functions with 2 or 3 variables. <span style="float: right;">c) K-maps can directly implement XOR functions without grouping.</span> b) Adjacent cells in a K-map differ by exactly one variable. <span style="float: right;">d) The number of rows in a K-map is always equal to the number of variables.</span>
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**Exercise 2: (2 points)**

- How many positive integers can be represented in binary using one byte?

The total number of positive integers that can be represented in binary using one byte is  $2^8 - 1 = 255$  (1pts)

- How many bits are necessary to represent in binary the natural numbers less than or equal to n?

Let k be the number of bits needed to represent a natural number  $\leq n$ .

We have  $2^k - 1 \geq n \Rightarrow 2^k \geq n + 1 \Rightarrow k \geq \lceil \log_2(n+1) \rceil$  (1pts)

**Exercise 3: (03 points)**

Consider the following two numbers encoded according to the IEEE 754 standard (32 bits) and represented in hexadecimal: 3EE00000 and 3D800000.

- Compute their sum and give the result in IEEE 754 format and in decimal form.

$$3EE00000 = 00111110110000000000000000000000 = (+1, 110*2^{-2})_2$$

$$3D800000 = 00111101100000000000000000000000 = (+1, 0*2^{-4})_2$$

$$\begin{aligned} 3EE00000 + 3D800000 &= (+1, 110*2^{-2})_2 + (+1, 0*2^{-4})_2 = (+111, 0*2^{-4})_2 + (+1, 0*2^{-4})_2 = (+1000, 0*2^{-4})_2 = (+1, 0*2^{-1})_2 \\ &\quad (+1, 0*2^{-1})_2 = 00111110000000000000000000000000 = \text{3F000000} \end{aligned}$$

$$(+1, 0*2^{-1})_2 = (0.1)_2 = (0.5)_{10} \quad (0.5\text{pts})$$

- Same question for the numbers: C8800000 and C8000000.

$$C8800000 = 11001000100000000000000000000000 = (-1.0*2^{+18})_2 = (-10.0*2^{+17})_2$$

$$C8000000 = 11001000000000000000000000000000 = (-1.0*2^{+17})_2$$

$$C8800000 + C8000000 = (-10.0*2^{+17})_2 + (-1.0*2^{+17})_2 = (-11.0*2^{+17})_2 = (-1, 1*2^{+18})_2$$

$$(-1, 1*2^{+18})_2 = 11001000100000000000000000000000 = \text{C8C00000} \quad (1\text{pts})$$

$$(-1, 1*2^{+18})_2 = (-1, 5*2^{+18})_{10} = (-393216)_{10} \quad (0.5\text{pts})$$

#### Exercise 4: (06 points)

In this exercise, numbers are stored in 1-byte words, i.e., 8 bits.

- Give the two's complement representation of the following signed integers: -13 and -127.

$$(-13)_{10} = (10001101)_{SM} = (11110010)_{1^c} = (1111\ 0011)_{2^c} \text{ (0.5pts)}$$

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$$(-127)_{10} = (11111111)_{SM} = (10000000)_{1^c} = (10000001)_{2^c} \text{ (0.5pts)}$$

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- Compute the opposite of the following signed integers represented in 1 byte: 10011101 and 00110011.

$$(10011101)_{2^s} = (10011100)_{1^s} = (11100011)_{SM} = (-99)_{10} \text{ its opposite is } (+99)_{10} = (01100011)_{SM} \text{ (0.5pts)}$$

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$$(00110011)_{SM} = (+51)_{10} \text{ its opposite is } (-51)_{10} = (10110011)_{SM} = (11001100)_{1^c} = (11001101)_{SM} \text{ (0.5pts)}$$

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- Give the decimal representation of the following signed integers (in two's complement): 11001101 and 00001101.

$$(11001101)_{2^s} = (11001100)_{1^s} = (10110011)_{SM} = (-51)_{10} \text{ (0.5pts)}$$

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$$(00001101)_{2^s} = (00001101)_{SM} = (+13)_{10} \text{ (0.5pts)}$$

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- Convert to binary, then compute on 8 bits the following operation:  $(-13) + 13$ ,  $23 - 46$ , and  $127 + 2$ .

$$(+13)_{10} = (00001101)_{SM}, \quad (-13)_{10} = (10001101)_{SM} = (11110010)_{1^c} = (11110011)_{2^c}$$

$$(+13)_{10} + (-13)_{10} = (00001101)_{SM} + (11110011)_{2^c} = (11110000)_{2^c} = (00000000)_{SM} \text{ (1pts)}$$

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$$(+23)_{10} = (00010111)_{SM}, \quad (-46)_{10} = (10101110)_{SM} = (11010001)_{1^c} = (11010010)_{2^c}$$

$$(+23)_{10} + (-46)_{10} = (00010111)_{SM} + (11010010)_{2^c} = (11101001)_{2^c} = (11101000)_{1^c} = (10010111)_{SM} = (-23)_{SM} \text{ (1pts)}$$

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$$(+127)_{10} = (01111111)_{SM}, \quad (+2)_{10} = (00000010)_{SM}$$

$$(+127)_{10} + (+2)_{10} = (01111111)_{SM} + (00000010)_{SM} = (10000001)_{SM} \text{ with overflow. Invalid result (1pts)}$$

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## **Exercise 5 : (05 points)**

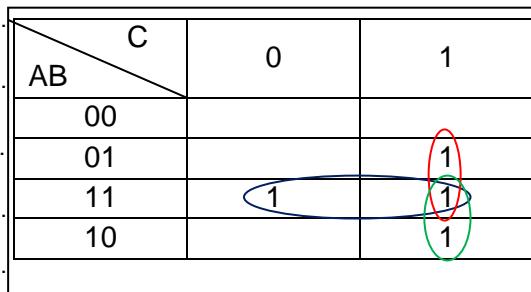
Let X, Y, Z be input variables, and let F be defined as:

$$F(A, B, C) = \begin{cases} 1 & \text{if the majority of variables is 1} \\ 0 & \text{sinon} \end{cases}$$

1. Construct the truth table for  $F(A, B, C)$ . (1pts)

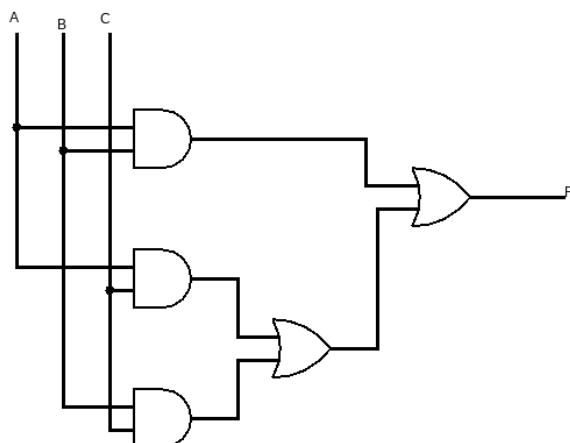
A	B	C	F
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1

2. Simplify the function F using a Karnaugh map.

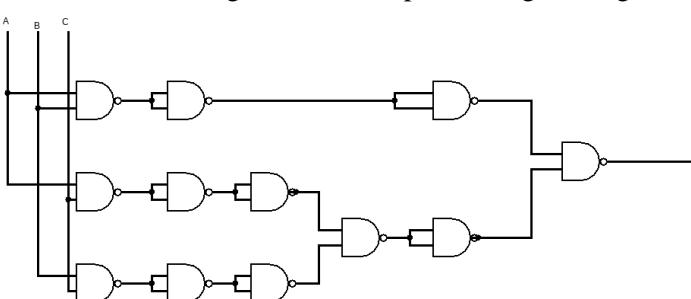


... (0.5pts)

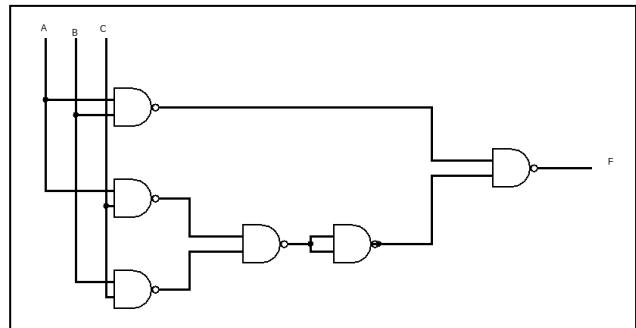
3. Draw the logical circuit implementing F (1pts)



4. Draw the logical circuit implementing F using NAND gates



(1pts)



(1pts)

