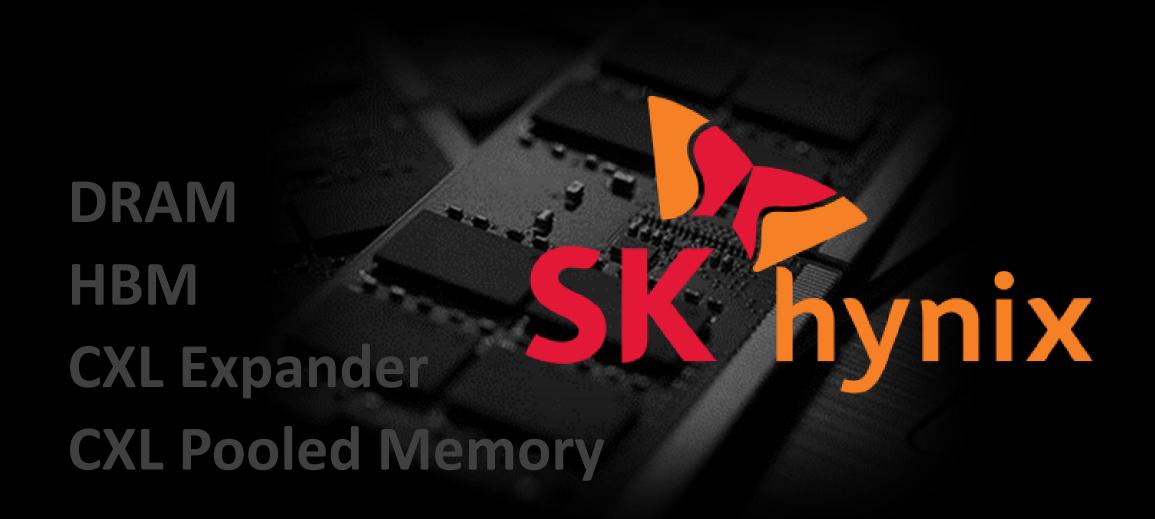
LUF (Lazy Unmap Flush)

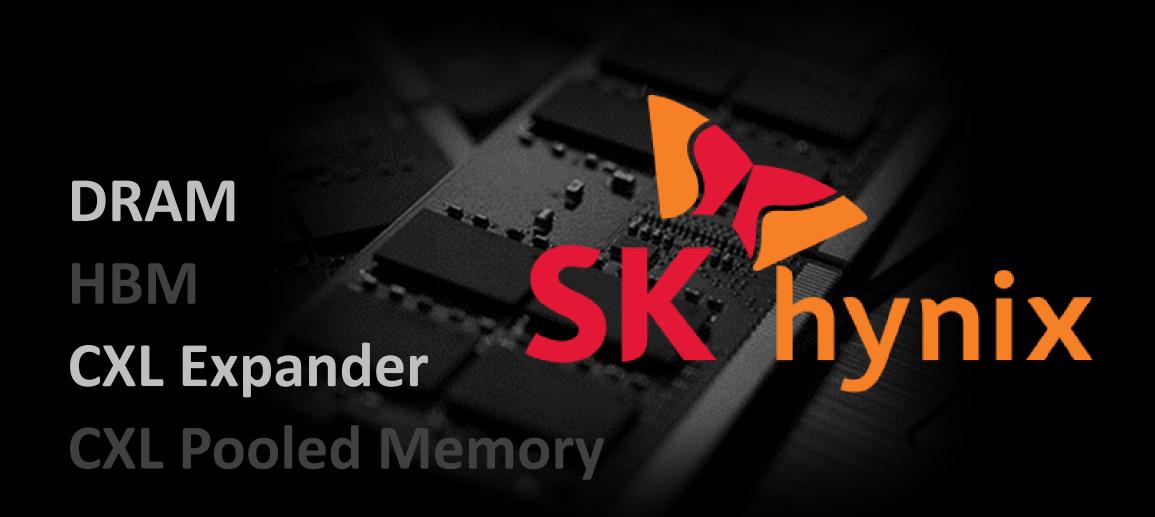
Skip TLB Shootdown on Unmapping

SK hynix Byungchul Park

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MACHINE

Intel(R) Xeon(R) Gold 6430

1 socket 64 cores hyper threaded on (x2)

DRAM 42GB + CXL Expander 96GB

swap off, numa balancing tiering on, demotion enabled

MEASUREMENT (LLM inference)

llama.cpp/main –m \$(70G_model1) –p "who are you?" –s 1 –t 15 –n 20 & llama.cpp/main –m \$(70G_model2) –p "who are you?" –s 1 –t 15 –n 20 &

llama.cpp/main -m \$(70G_model3) -p "who are you?" -s 1 -t 15 -n 20 &



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MACHINE

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Heavy page reclaim

Heavy page migration e.g. demotion

llama.cpp/main –m \$(70G_model1) –p "who are you?" –s 1 –t 15 –n 20

llama.cpp/main –m \$(70G_model2) –p "who are you?" –s 1 –t 15 –n 20 8

llama.cpp/main -m \$(70G_model3) -p "who are you?" -s 1 -t 15 -n 20 &



While the workload of llama.cpp runs for 4000 secs... \$ grep TLB /proc/interrupts

TLB:

80911532 93691786 100296251 111062810 109769109 109862429 108968588 119175230 115779676 118377498 119325266 120300143 124514185 116697222 121068466 118031913 122660681 117494403 121819907 116960596 120936335 117217061 118630217 122322724 119595577 111693298 119232201 120030377 115334687 113179982 118808254 116353592 140987367 137095516 131724276 139742240 136501150 130428761 127585535 132483981 133430250 133756207 131786710 126365824 129812539 133850040 131742690 125142213 128572830 132234350 131945922 128417707 133355434 129972846 126331823 134050849 133991626 121129038 124637283 132830916 126875507 122322440 125776487 124340278 TLB shootdowns



TLB (Translation Lookaside Buffer)

TLB is a CPU cache for mapping.

What if mappings have changed?

CPUs with the mappings should invalidate their TLBs,

So that the CPUs can run with the updated ones.

A method to invalidate TLBs involved.

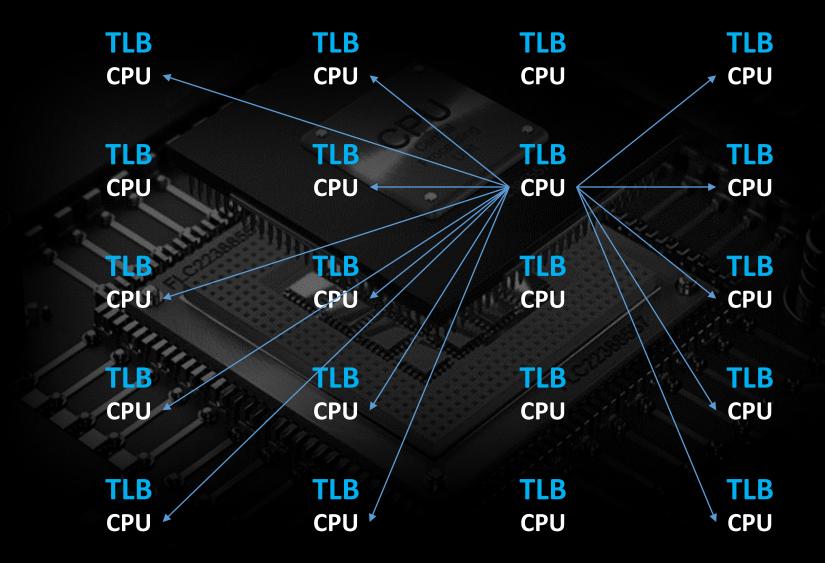
Request TLB flush to CPUs.

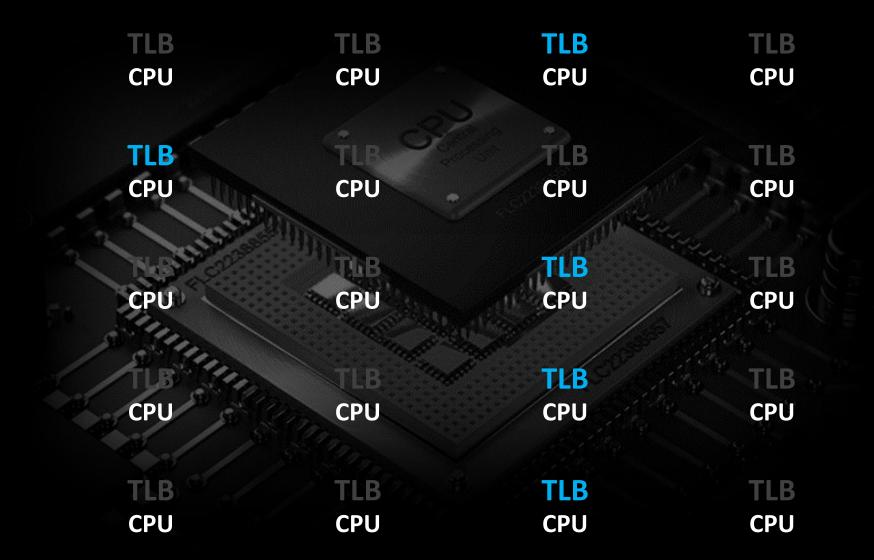
Pay the cost of IPI(Inter Processor Interrupt).

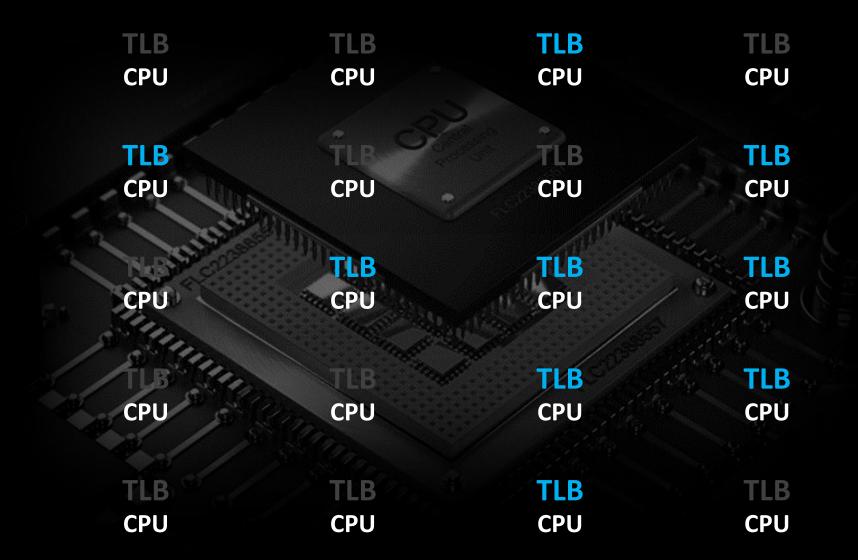
Pay the cost of TLB miss.

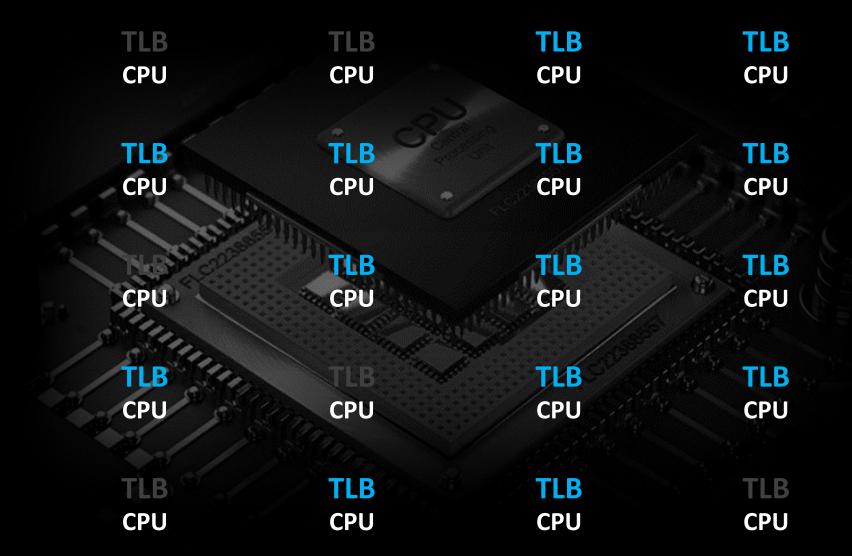
TLB	TLB	TLB
CPU	CPU	CPU
60		
TLB	TLB	TLB
CPU	CPU	CPU
TLB	TLB	TLB
CPU	CPU	CPU
TLB	TLB	TLB
CPU	CPU	CPU
TLB	TLB	TLB
		CPU
	TLB CPU TLB CPU	TLB CPU TLB CPU TLB CPU TLB CPU TLB CPU TLB TLB CPU TLB TLB TLB TLB TLB TLB

TLB	TLB	TLB	TLB
CPU	CPU	CPU	CPU
	5		
TLB	TLB	TLB	TLB
CPU	CPU	CPU	CPU
TLB	TLB	TLB	TLB
CPU	CPU	CPU	CPU
Stan			
TLB	TLB	TLB	TLB
CPU	CPU	CPU	CPU
			9, 9
TID	TID	TLD	TLD
TLB	TLB	TLB	TLB
CPU	CPU	CPU	CPU









TLB	TLB	TLB
CPU	CPU	CPU
60		
TLB	TLB	TLB
CPU	CPU	CPU
TLB	TLB	TLB
CPU	CPU	CPU
TLB	TLB	TLB
CPU	CPU	CPU
TLB	TLB	TLB
		CPU
	TLB CPU TLB CPU	TLB CPU TLB CPU TLB CPU TLB CPU TLB CPU TLB TLB CPU TLB TLB TLB TLB TLB TLB

ME

Is it required to perform TLB shootdown when unmapping virtual space?

CHAT GPT

Yes, performing a TLB shootdown is typically required when unmapping virtual space to ensure that all stale TLB entries are invalidated. This prevents the CPU from using old, invalid mappings and maintains consistency across multiple processors. The operating system's role is to manage this process, ensuring correctness and synchronization across the system.

While the workload of llama.cpp runs for 4000 secs... \$ grep TLB /proc/interrupts

TLB:

80911532 93691786 100296251 111062810 109769109 109862429 108968588 119175230 115779676 118377498 119325266 120300143 124514185 116697222 121068466 118031913 122660681 117494403 121819907 116960596 120936335 117217061 118630217 122322724 119595577 111693298 119232201 120030377 115334687 113179982 118808254 116353592 140987367 137095516 131724276 139742240 136501150 130428761 127585535 132483981 133430250 133756207 131786710 126365824 129812539 133850040 131742690 125142213 128572830 132234350 131945922 128417707 133355434 129972846 126331823 134050849 133991626 121129038 124637283 132830916 126875507 122322440 125776487 124340278 TLB shootdowns



LUF's Goal

A method to invalidate TLBs involved.

Request TLB flush to CPUs.

Pay the cost of IPI(Inter Processor Interrupt).

Pay the cost of TLB miss.

A method to invalidate TLBs involved.

Request TLB flush to CPUs.

Pay the cost of IPI(Inter Processor Interrupt).

Pay the cost of TLB miss.

A method to invalidate TLBs involved.

Request TLB flush to CPUs.

Pay t\$kipost of IPI(Inter Processor Interrupt).

Pay tkeepost of TLB rhits.

LUF's Interest

Unmap Cases

A user explicitly unmaps using e.g. munmap().

Page cache cleans folios e.g. folio_mkclean().

Page reclaim unmaps to free pages.

Page migration unmaps to move pages.

And so on...

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Page reclaim unmaps to free pages.

Page migration unmaps to move pages.

And so on...

Page Migration Steps

Unmap mappings of the source page.

TLB shootdown to invalidate TLBs involved.

Allocate a new page.

Copy the source page to the new page.

Free the source page.

Page Migration Steps

Unmap mappings of the source page.

TLB shootdown to invalidate TLBs involved.

Allocate a new page.

Copy the source page to the new page.

Free the source page.

Page Migration Steps

Unmap mappings of the source page.

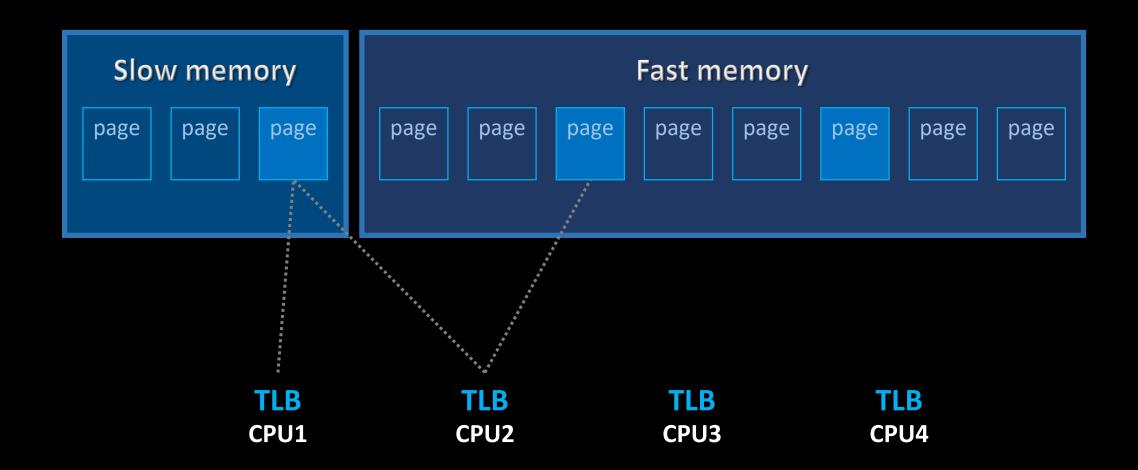
TLB shootdown Skip ! validate TLBs involved.

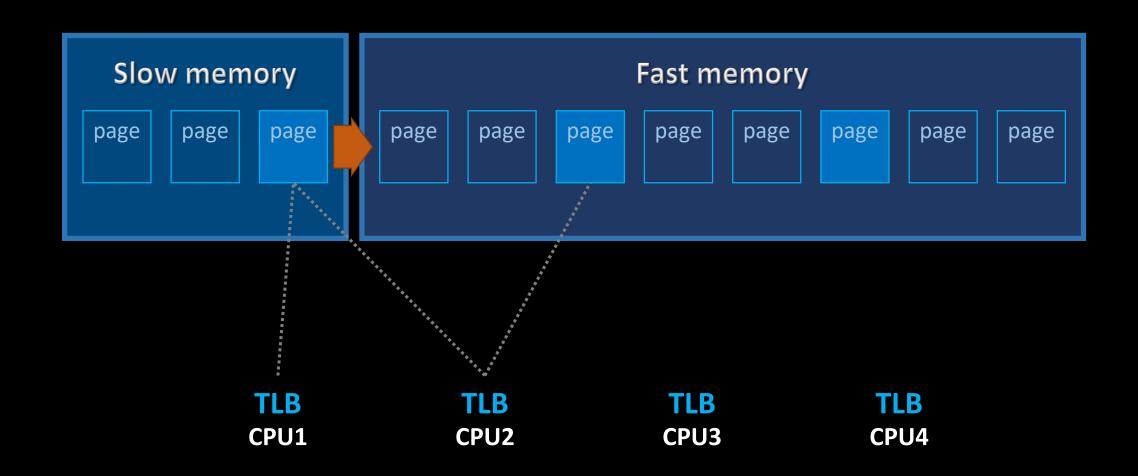
Allocate a new page.

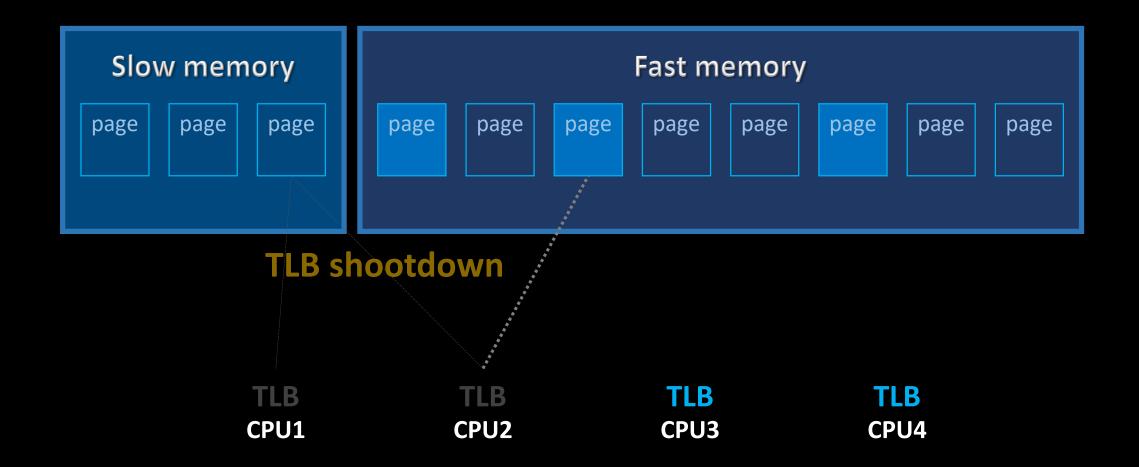
Copy the source page to the new page.

Free the source page.

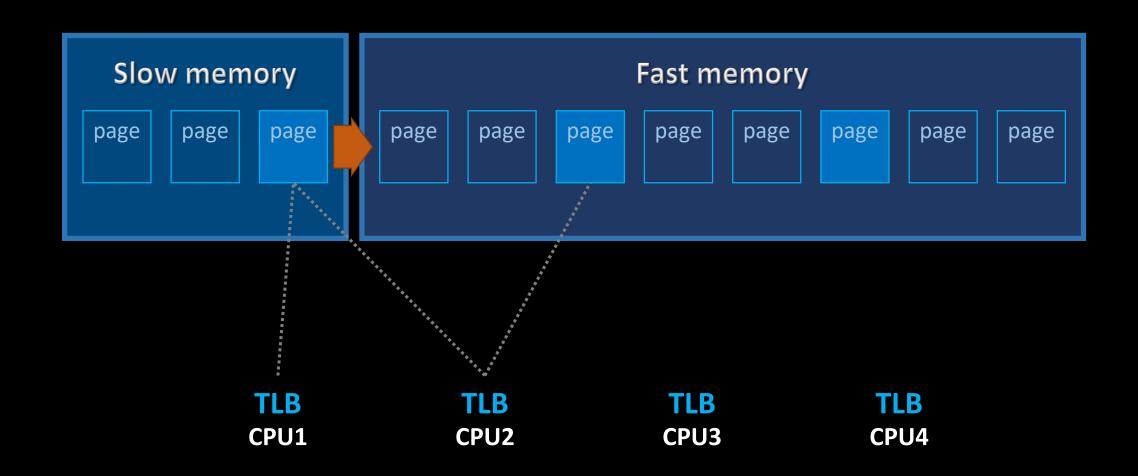
How?

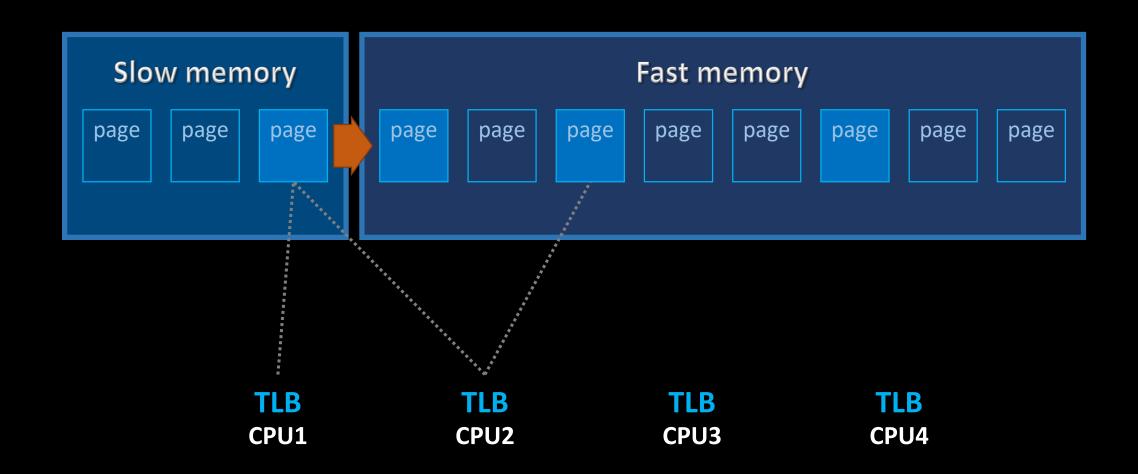


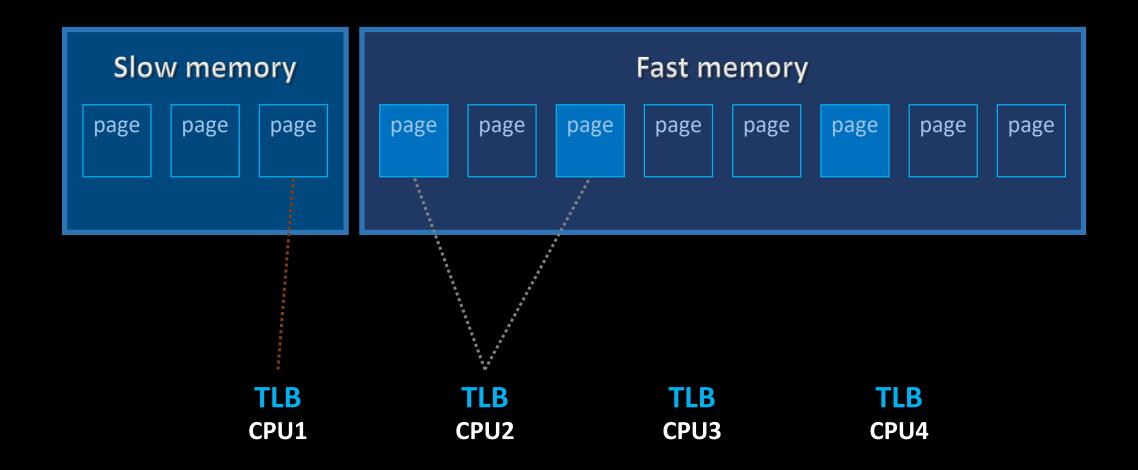


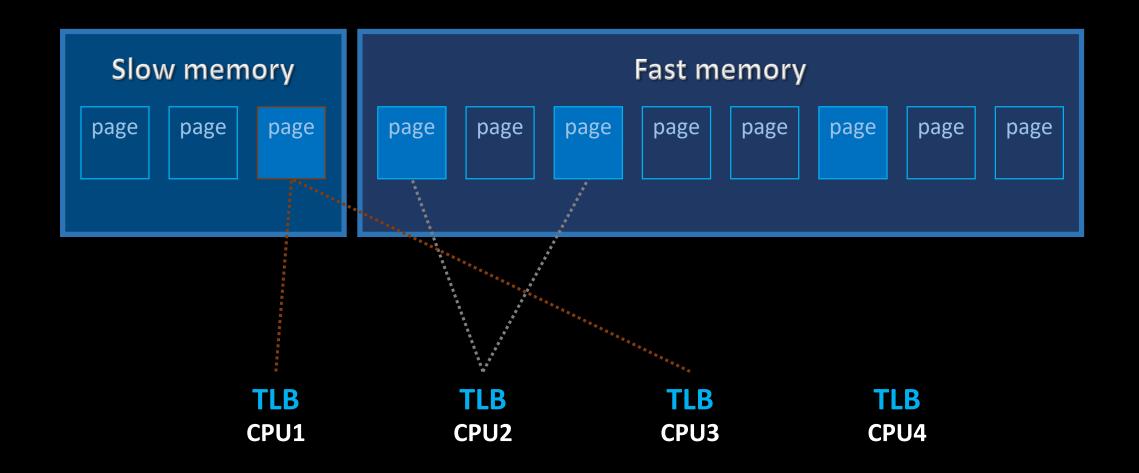


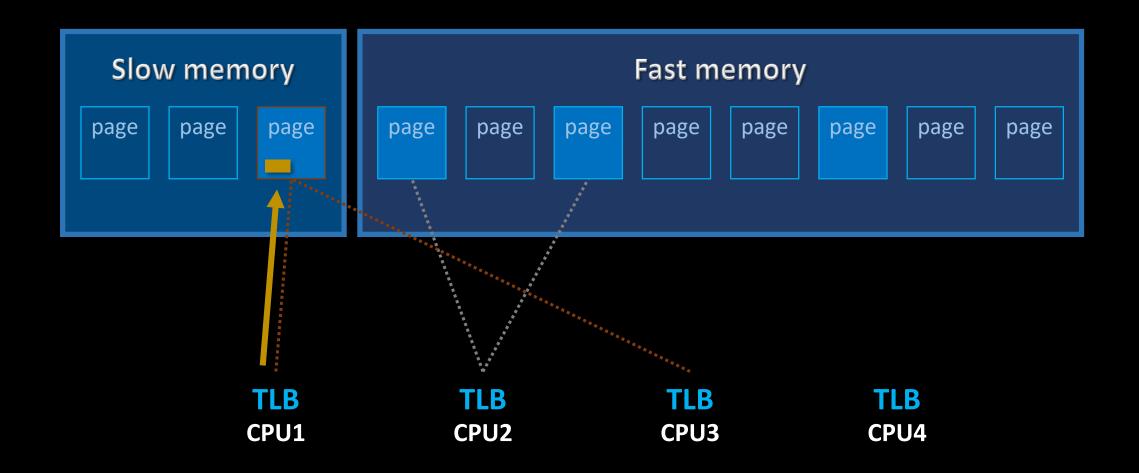
What if w/o TLB shootdown?

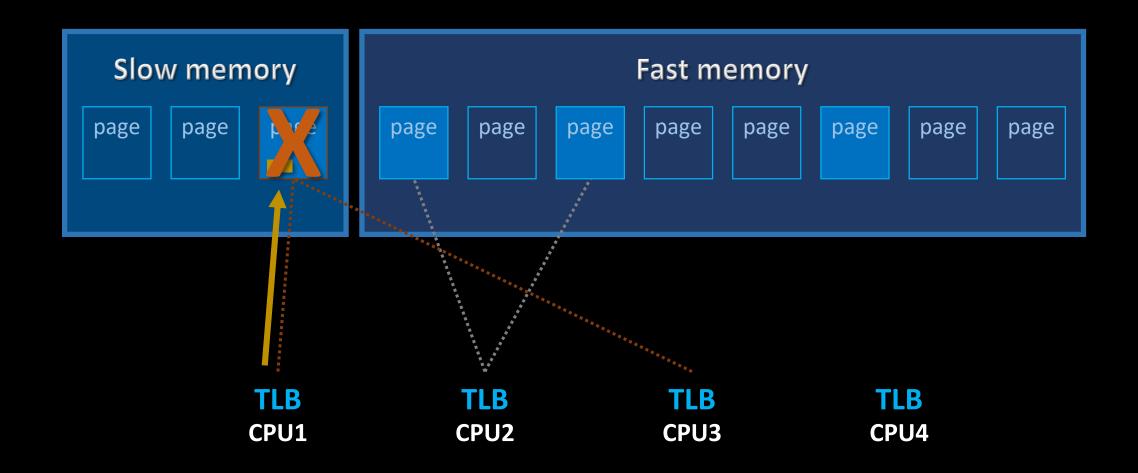




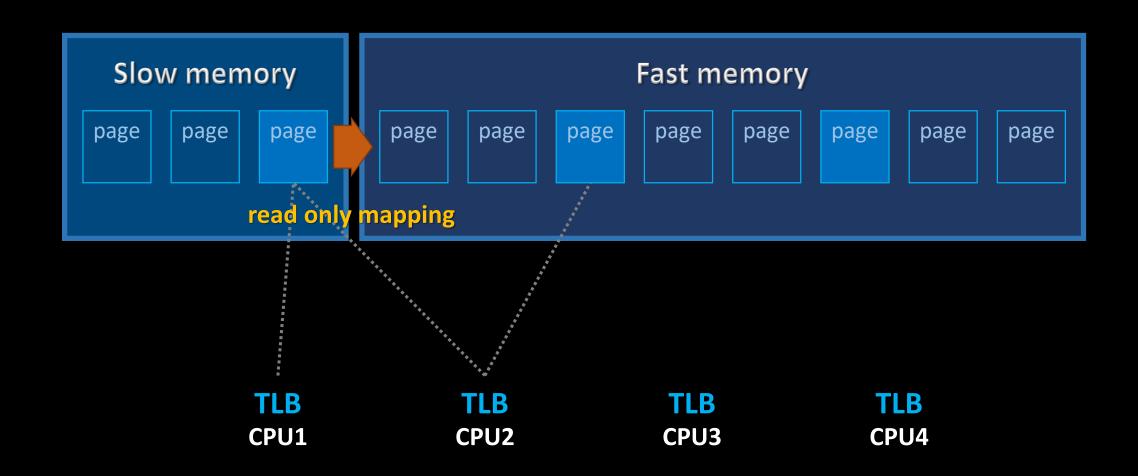


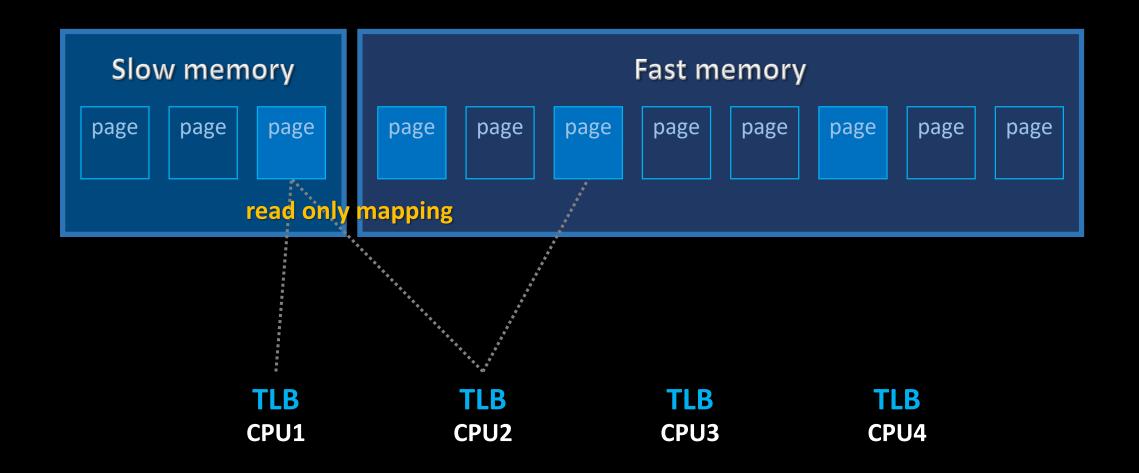


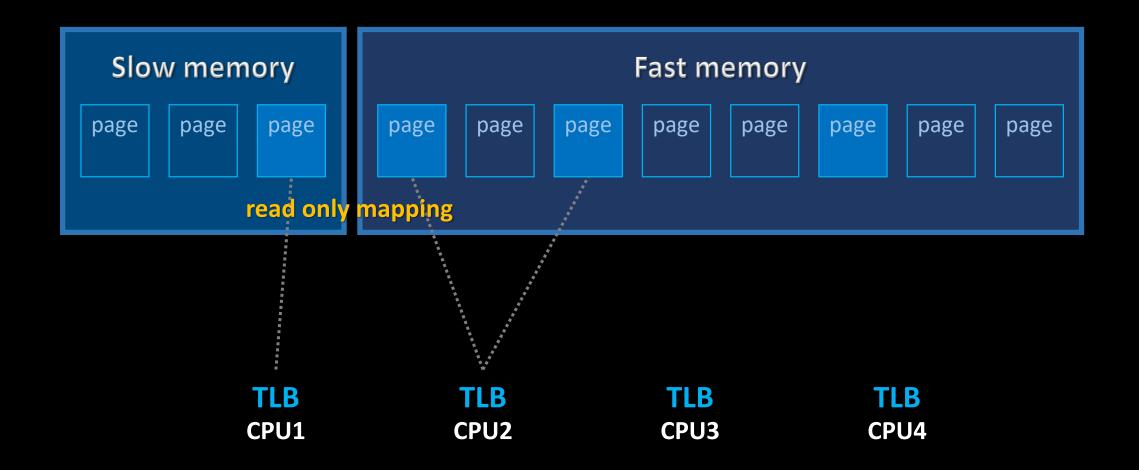


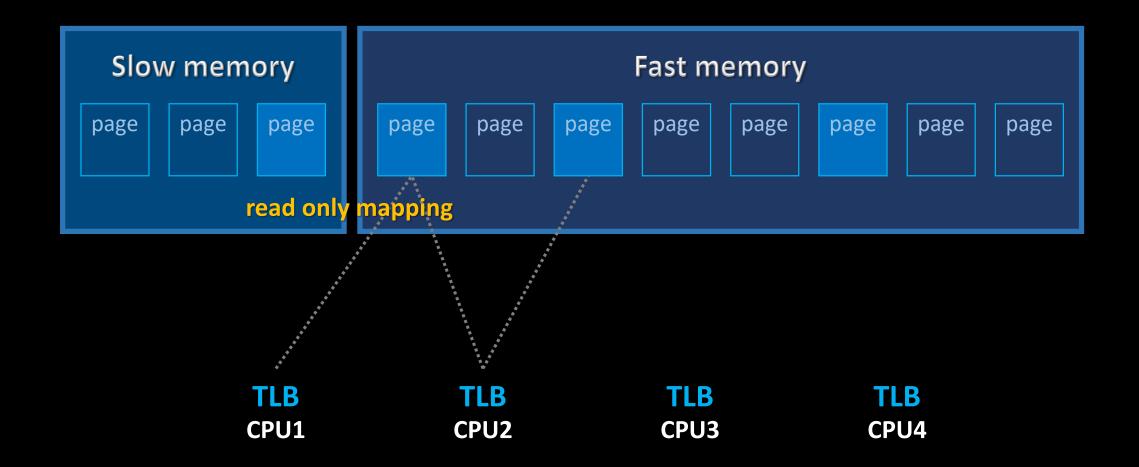


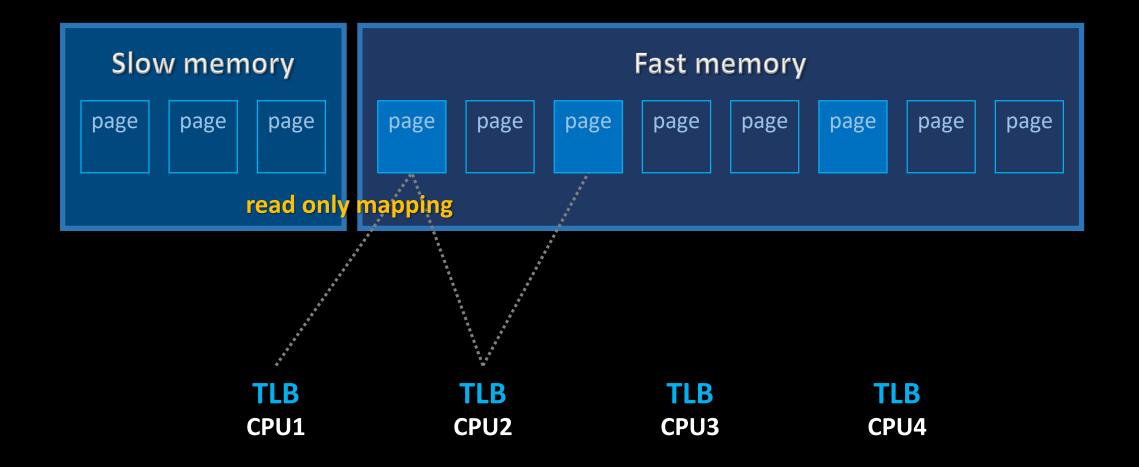
What if it's read only mapping?











Safe, even w/o TLB shootdown!

So...

For read only mappings,

Keep read copies even after unmapping,

So that some CPUs can run with stale TLB entries.

Hopefully skip whole TLB shootdown.

Or partially TLB shootdown when it's inevitable.

For read only mappings,

Keep read copies even after unmapping,

So that some HOWILUFstWOrkSes.

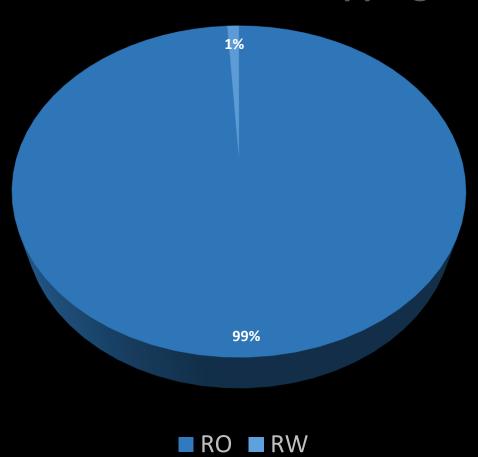
Hopefully skip whole TLB shootdown.

Or partially TLB shootdown when it's inevitable.

But...

Worthy only with RO mappings

RO: RW Ratio
Collected on Unmapping



Dive Into LUF

Approaches

Considerations

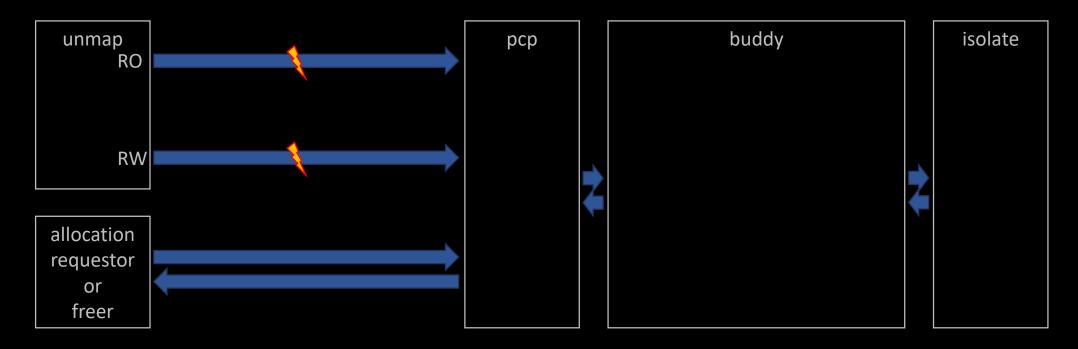
Measurement

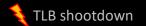
1 Keep read copies before freeing

2 Keep read copies residing allocator

Approaches

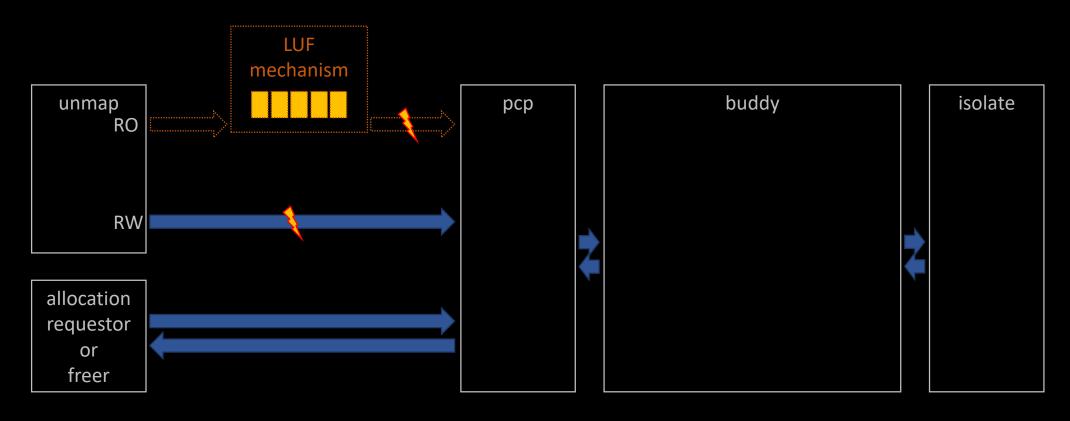
Considerations

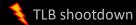




Approaches

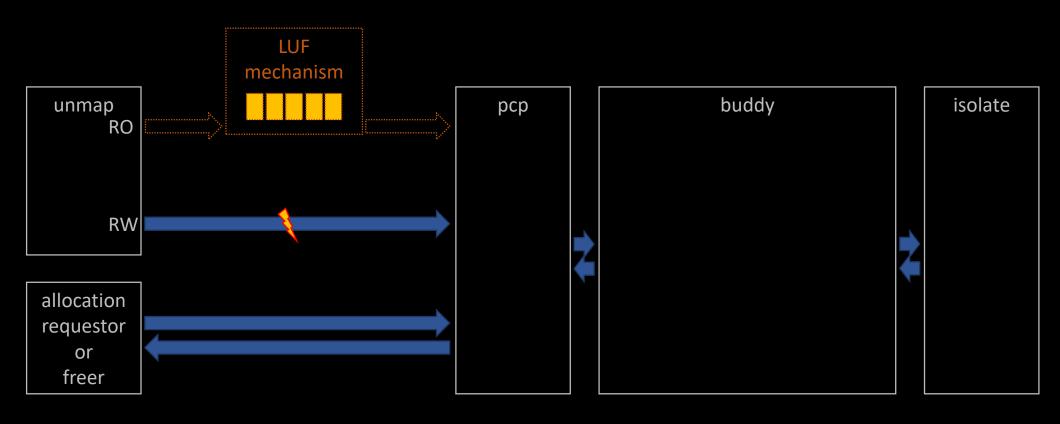
Considerations

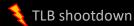




Approaches

Considerations





Approaches

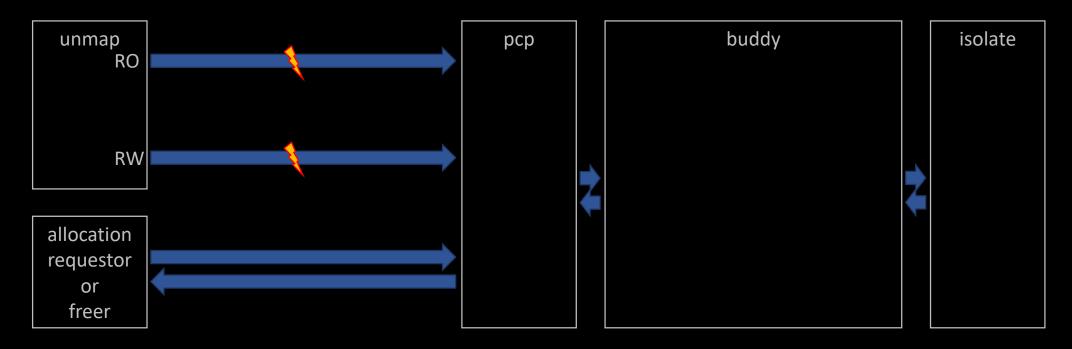
Considerations

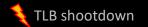


Keep Read Copies Residing Allocator

Approaches

Considerations

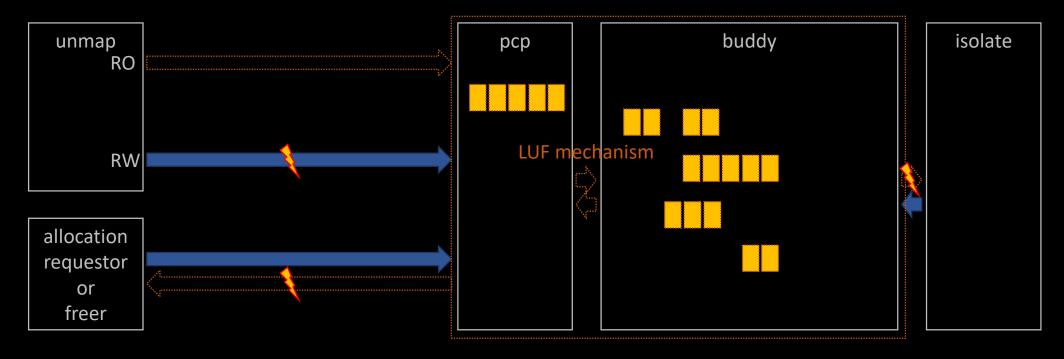


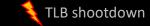


Keep Read Copies Residing Allocator

Approaches

Considerations



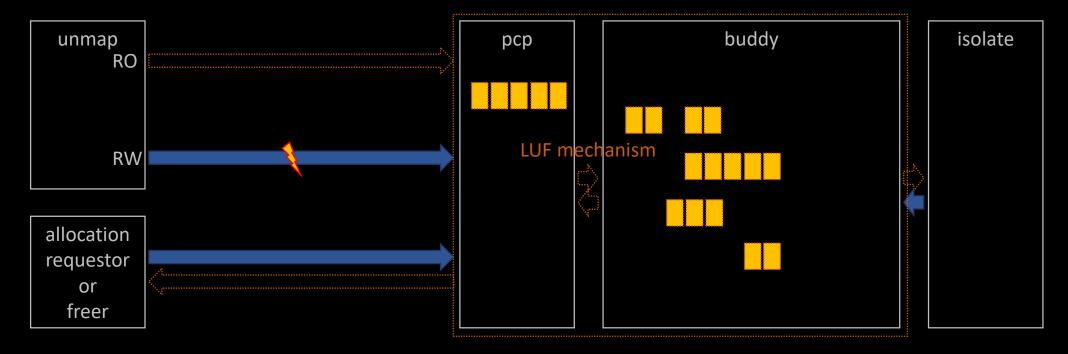


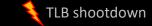
- ※ want_init_on_free() should be false for LUF to work.
- \times should_skip_kasan_poison() should be true for LUF to work.

Keep Read Copies Residing Allocator

Approaches

Considerations





Approaches

Considerations

Measurement

struct page: manages each page

struct mm_struct : manages virtual space per task

struct address_space : manages page cache per file

	1	Track CPUs to TLB shootdown per page
	2	Track CPUs to TLB shootdown per mm_struct
	3	Track CPUs to TLB shootdown per address_space
Considerations		
	4	When to TLB shootdown per page
	5	When to TLB shootdown per mm_struct
	6	When to TLB shootdown per address_space

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Approaches

Considerations

- Request page allocation.
- 2 Check if there are enough free pages.
- ③ If hitting WMARK_LOW, let kswapd reclaim.
- 4 If hitting WMARK_MIN, do direct reclaim.
- 5 Do TLB shootdown during reclaim.
- 6 Return pages on successful reclaim.
- (7) LUF does TLB shootdowns if needed on return.

Approaches

Considerations



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- Approaches
- Considerations
- Measurement

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- Approaches
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Approaches

Considerations

Page Allocation Steps under Pressure



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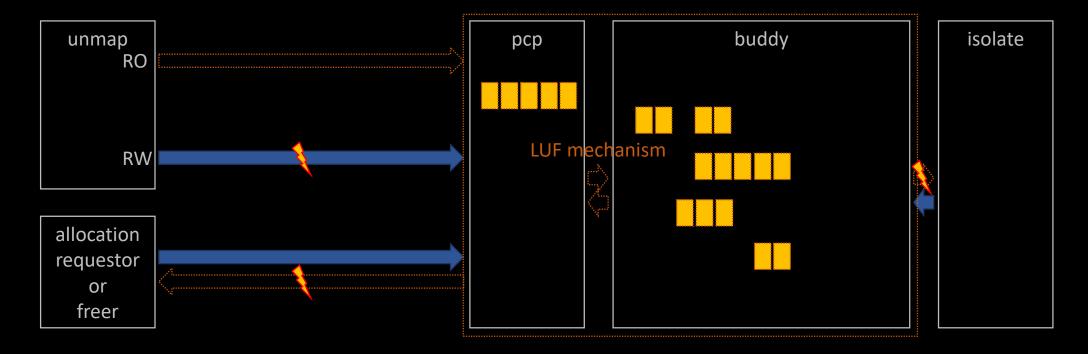
Approaches

Considerations

Keep Read Copies Residing Allocator

Approaches

Considerations



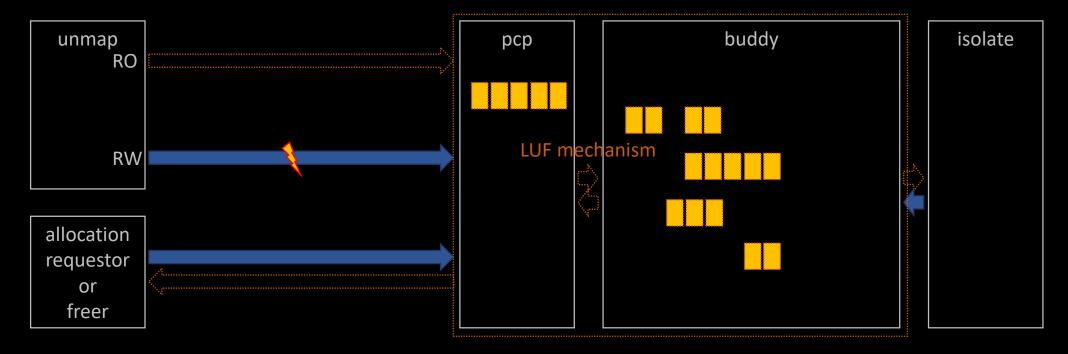


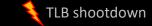
- ※ want_init_on_free() should be false for LUF to work.

Keep Read Copies Residing Allocator

Approaches

Considerations





Page Allocation Steps under Pressure



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- 2 Check if there are enough free pages.
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Approaches

Considerations

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- Request page allocation.
- 2 Check if there are enough free pages.
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Approaches

Considerations

When to TLB Shootdowns Vanilla

Approaches

Considerations

Measuremen^a





TLB shootdown

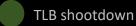
When to TLB Shootdowns Vanilla + LUF

Approaches

Considerations

Measuremen⁻

```
kswapd reclaim
page allocation
direct reclaim (on page allocation)
                                                    ---
                                                                 . . . . .
page allocation
direct reclaim (on page allocation)
page allocation
direct reclaim (on page allocation)
page allocation
direct reclaim (on page allocation)
page allocation
                                                         . . . . .
direct reclaim (on page allocation)
                                                                              page allocation
direct reclaim (on page allocation)
```



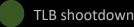
When to TLB Shootdowns Vanilla + LUF

Approaches

Considerations

Measuremen⁻

```
kswapd reclaim
page allocation
direct reclaim (on page allocation)
page allocation
direct reclaim (on page allocation)
                -----
page allocation
direct reclaim (on page allocation)
page allocation
direct reclaim (on page allocation)
      page allocation
direct reclaim (on page allocation)
page allocation
direct reclaim (on page allocation)
```



	1	Track CPUs to TLB shootdown per page	
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Considerations			
	4	When to TLB shootdown per page	
	5	When to TLB shootdown per mm_struct	
	6 When to TLB shootdown per address_sr		

Approaches

Considerations

Measurement

Change via system call

Changing Mappings Driven by Users

Approaches

Considerations

```
int madvise cold or pageout pte range(...)
or int madvise_free_pte_range(...)
or unsigned long zap_pte_range(...)
or long change pte range(...)
or int move ptes(...)
      flush tlb batched pending(mm);
```

Approaches

Considerations

Measurement

Change via page fault

Creating Write Mappings

Approaches

Considerations

Approaches

Considerations

Measurement

File pages in page cache

Updating Page Cache via Buffered IO

Approaches

Considerations

Approaches

Considerations

Measurement

File pages not in page cache

Updating Files via Direct IO

Approaches

Considerations

Approaches

Considerations

Measurement

What if I miss things?

Design LUF Debug Feature

Approaches

Considerations

Measurement

Use additional space in struct page.

Force to strongly synchronize on objects for checking.

Check the sanity on every kmap() and its family.

Check the sanity on every {pte,pmd,pud}_mkwrite().

Approaches

Considerations

Measurement

1 TLB shootdown number

2 TLB miss number

3 Runtime of the workload

TLB Shootdown Number Vanilla

While the workload of llama.cpp runs for 4000 secs...

\$ grep TLB /proc/interrupts

TLB:

Measurement

```
108968588 119175230 115779676 118377498 119325266 120300143
124514185 116697222 121068466 118031913 122660681 117494403
121819907 116960596 120936335 117217061 118630217 122322724
119595577 111693298 119232201 120030377 115334687 113179982
118808254 116353592 140987367 137095516 131724276 139742240
136501150 130428761 127585535 132483981 133430250 133756207
131786710 126365824 129812539 133850040 131742690 125142213
128572830 132234350 131945922 128417707 133355434 129972846
```

126331823 134050849 133991626 121129038 124637283 132830916

126875507 122322440 125776487 124340278 TLB shootdowns



TLB Shootdown Number Vanilla + LUF

While the workload of llama.cpp runs for 4000 secs...

\$ grep TLB /proc/interrupts

Measurement

TLB:

2121206	2615108	2983494	2911950	3055086	30926/2
3204894	3346082	3286744	3307310	3357296	3315940
3428034	3112596	3143325	3185551	3186493	3322314
3330523	3339663	3156064	3272070	3296309	3198962
3332662	3315870	3234467	3353240	3281234	3300666
3345452	3173097	4009196	3932215	3898735	3726531
3717982	3671726	3728788	3724613	3799147	3691764
3620630	3684655	3666688	3393974	3448651	3487593
3446357	3618418	3671920	3712949	3575264	3715385
3641513	3630897	3691047	3630690	3504933	3662647
3629926	3443044	3832970	3548813	TLB shootdowns	



TLB Shootdown Number Vanilla + LUF

Measurement

TLB:

2121206	2615108	2983494	50	305508	309/ 372
3204894	3346082	3286744	0	729	3 15040
3428034	3112596	3143325	318 51	31 493	33 3
3330523	3339663	3156064	3272070	3296309	3198962
3332662	3315870	3234467	3353240	3281234	3300666
3345452	3173097	4009196	3932215	3898735	3726531
3717982	3671726	3728788	3724613	3799147	3691764
3620630	3684655	3666688	3393974	3448651	3487593
3446357	3618418	3671920	3712949	3575264	3715385
3641513	3630897	3691047	3630690	3504933	3662647
3629926	3443044	3832970	3548813	TLB shootd	lowns



TLB Miss Number Vanilla

Annroaches

Considerations

Measurement

While the workload of llama.cpp runs for 4000 secs...

```
$ perf stat -a \
```

- > -e dTLB-load-misses \
- > -e dTLB-store-misses \
- > -e iTLB-load-misses \
- > \$(llama.cpp.test.sh)

25296288810 dTLB-load-misses

21592110362 dTLB-store-misses

3985725200 iTLB-load-misses



TLB Miss Number Vanilla + LUF

While the workload of llama.cpp runs for 4000 secs...

\$ perf stat -a \

- > -e dTLB-load-misses \
- > -e dTLB-store-misses \
- > -e iTLB-load-misses \
- > \$(llama.cpp.test.sh)

12892995056 dTLB-load-misses

5357137957 dTLB-store-misses

669019529 iTLB-load-misses





TLB Miss Number Vanilla + LUF

While the workload of llama.cpp runs for 4000 secs...

\$ perf stat -a \

-50%

> -e dTLB-load-misses \

> -e dTLB-store-misses \

> -e iTLB-load-misses \

> \$(llama.cpp.test.sh)

-70%

Measurement

12892995056 dTLB-load = 180%

5357137957 dTLB-store-misses

669019529 iTLB-load-misses



Runtime of the Workload Vanilla

```
llama print timings:
                       total time = 883450.54 ms / 24 tokens
llama print timings:
                       total time = 861665.91 ms / 24 tokens
llama_print_timings:
                       total time = 898079.02 ms / 24 tokens
                       total time = 879897.69 ms / 24 tokens
llama print timings:
llama print timings:
                       total time = 892360.75 ms / 24 tokens
llama print timings:
                       total time = 884587.85 ms / 24 tokens
llama print timings:
                       total time = 861023.19 ms / 24 tokens
llama print timings:
                       total time = 900022.18 ms / 24 tokens
llama print timings:
                       total time = 878771.88 ms / 24 tokens
                       total time = 889027.98 ms / 24 tokens
llama print timings:
llama print timings:
                       total time = 880783.90 ms / 24 tokens
llama print timings:
                       total time = 856475.29 ms / 24 tokens
llama print timings:
                       total time = 896842.21 ms / 24 tokens
llama print timings:
                       total time = 878883.53 ms / 24 tokens
llama print timings:
                       total time = 890122.10 ms / 24 tokens
```



Runtime of the Workload Vanilla + LUF

```
llama print timings:
                       total time = 871060.86 ms / 24 tokens
llama print timings:
                       total time = 825609.53 ms / 24 tokens
llama_print_timings:
                       total time = 836854.81 ms / 24 tokens
                       total time = 843147.99 ms / 24 tokens
llama print timings:
llama print timings:
                       total time = 831426.65 ms / 24 tokens
llama print timings:
                       total time = 873939.23 ms / 24 tokens
llama print timings:
                       total time = 826127.69 ms / 24 tokens
llama print timings:
                       total time = 835489.26 ms / 24 tokens
llama print timings:
                       total time = 842589.62 ms / 24 tokens
                       total time = 833700.66 ms / 24 tokens
llama print timings:
llama print timings:
                       total time = 875996.19 ms / 24 tokens
llama print timings:
                       total time = 826401.73 ms / 24 tokens
llama print timings:
                       total time = 839341.28 ms / 24 tokens
llama print timings:
                       total time = 841075.10 ms / 24 tokens
llama print timings:
                       total time = 835136.41 ms / 24 tokens
```



Runtime of the Workload Vanilla + LUF

```
total time = 871060.86 ms /
total time = 825609.53 ms /
                             24 tokens
total time = 836854.81 ms /
                             24 tokens
total time = 843147.99 ms /
                               4 tokens
total time = 8
                                tokens
total time
total time = 826
total time = 835489.26 ms /
total time = 842589.62 ms /
                             24 tokens
total time = 833700.66 ms /
total time = 875996.19 ms /
total time = 826401.73 ms /
total time = 839341.28 ms /
total time = 841075.10 ms /
total time = 835136.41 ms /
```



But...

When to TLB Shootdowns Vanilla

Approaches

Considerations

Measuremen^a





TLB shootdown

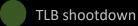
When to TLB Shootdowns Vanilla + LUF

Approaches

Considerations

Measuremen⁻

```
kswapd reclaim
page allocation
direct reclaim (on page allocation)
                                                    ---
                                                                 . . . . .
page allocation
direct reclaim (on page allocation)
page allocation
direct reclaim (on page allocation)
page allocation
direct reclaim (on page allocation)
page allocation
                                                         . . . . .
direct reclaim (on page allocation)
                                                                              page allocation
direct reclaim (on page allocation)
```



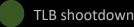
When to TLB Shootdowns Vanilla + LUF

Approaches

Considerations

Measuremen⁻

```
kswapd reclaim
page allocation
direct reclaim (on page allocation)
page allocation
direct reclaim (on page allocation)
                -----
page allocation
direct reclaim (on page allocation)
page allocation
direct reclaim (on page allocation)
      page allocation
direct reclaim (on page allocation)
page allocation
direct reclaim (on page allocation)
```



Still thinking how to synchronize...

Seen a better runtime
Seen a even worse runtime

Question?