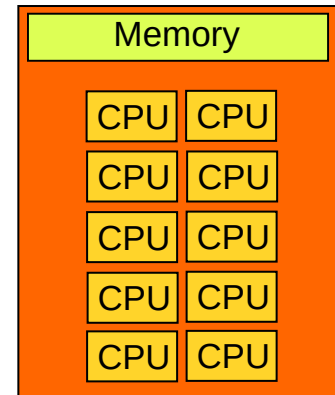
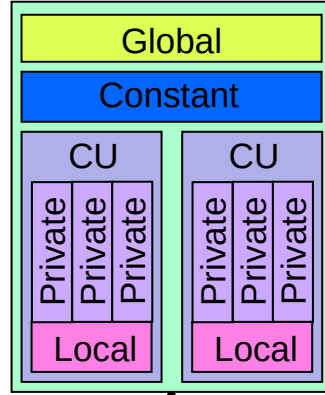


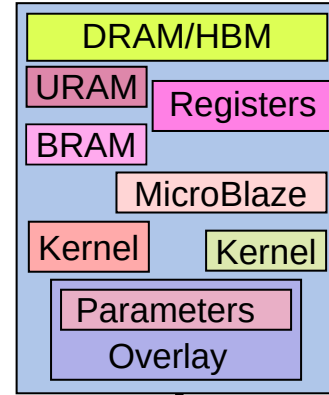
Host



GPU



FPGA (Xilinx Alveo)



FPGA CGRA MPSoC (Xilinx Versal ACAP)

